U?
74LS08

1Y	VCC
1A	4Y
1B	4B
2Y	4A
2A	3Y
2B	3B
GND	3A

U?
74LS08

1A	VCC
1B	4B
1Y	4A
2A	4Y
2B	3B
2Y	3A
GND	3Y

U?
74LS245

DIR	VCC
A1	$\overline{OE}$
A2	B1
A3	B2
A4	B3
A5	B4
A6	B5
A7	B6
A8	B7
GND	B8

U?
74LS32

1A	VCC
1B	4B
1Y	4A
2A	4Y
2B	3B
2Y	3A
GND	3Y

U?
74LS540

$\overline{E1}$	VCC
D1	$\overline{E2}$
D2	$\overline{Y1}$
D3	$\overline{Y2}$
D4	$\overline{Y3}$
D5	$\overline{Y4}$
D6	$\overline{Y5}$
D7	$\overline{Y6}$
D8	$\overline{Y7}$
GND	$\overline{Y8}$

U?
74LS688

$\overline{G}$	VCC
P0	$\overline{P=Q}$
Q0	Q7
P1	P7
Q1	P6
P2	Q5
Q2	P5
P3	Q4
Q3	P4
GND	

U?
74LS74

$\overline{1CLR}$	VCC
1D	$\overline{2CLR}$
1CLK	2D
$\overline{1PRE}$	2CLK
1Q	$\overline{2PRE}$
$\overline{1Q}$	2Q
GND	$\overline{2Q}$

U?
ATMega1284

PB0	(ADC0) PA0
PB1	(ADC1) PA1
PB2 (INT2)	(ADC2) PA2
PB3	(ADC3) PA3
PB4 (SS)	(ADC4) PA4
PB5 (MOSI)	(ADC5) PA5
PB6 (MISO)	(ADC6) PA6
PB7 (SCK)	(ADC7) PA7
$\overline{RESET}$	AREF
VCC	AGND
GND	AVCC
XTAL2	PC7
XTAL1	PC6
PD0 (RX0)	PC5
PD1 (TX0)	PC4
PD2 (RX1/INT0)	PC3
PD3 (TX1/INT1)	PC2
PD4	(SDA) PC1
PD5	(SCL) PC0
PD6	PD7

U?
AY-3-8910

GND	VCC
NC	TEST 1
ANALOG B	ANALOG C
ANALOG A	DA0
NC	DA1
IOB7	DA2
IOB6	DA3
IOB5	DA4
IOB4	DA5
IOB3	DA6
IOB2	DA7
IOB1	BC1
IOB0	BC2
IOA7	BDIR
IOA6	TEST 2
IOA5	A8
IOA4	$\overline{A9}$
IOA3	$\overline{RESET}$
IOA2	CLOCK
IOA1	IOA0

U?
MCP23S17

GPB0	GPA7
GPB1	GPA6
GPB2	GPA5
GPB3	GPA4
GPB4	GPA3
GPB5	GPA2
GPB6	GPA1
GPB7	GPA0
VCC	INTA
GND	INTB
$\overline{CS}$	$\overline{RESET}$
SCK	A2
SI	A1
SO	A0

U?
MT5C1008

NC	VCC
A16	A15
A14	CE2
A12	$\overline{WE}$
A7	A13
A6	A8
A5	A9
A4	A11
A3	A10
A2	$\overline{OE}$
A1	$\overline{CE}$
A0	DQ8
DQ1	DQ7
DQ2	DQ6
DQ3	DQ5
GND	DQ4

U?
V9958

GND	XTAL2
$\overline{DHCLK}$	XTAL1
$\overline{DLCLK}$	$\overline{RAS}$
VRESET	$\overline{CAS2}$
$\overline{HSYNC}$	$\overline{CAS1}$
$\overline{CSYNC}$	CASX
BLEO	VCC
CPUCLK/VDS	$\overline{WR}$
$\overline{RESET}$	AD7
YS	AD6
CBDR	AD5
C7	AD4
C6	AD3
C5	AD2
C4	AD1
C3	AD0
C2	RD7
C1	RD6
C0	RD5
GND/DAC	RD4
VCC/DAC	RD3
G	RD2
R	RD1
B	RD0
$\overline{INT}$	CD0
$\overline{WAIT}$	CD1
$\overline{HRESET}$	CD2
MODE1	CD3
MODE0	CD4
$\overline{CSW}$	CD5
$\overline{CSR}$	CD6
CD7	VBB

U?
Z80

A11	A10
A12	A9
A13	A8
A14	A7
A15	A6
CLK	A5
D4	A4
D3	A3
D5	A2
D6	A1
VCC	A0
D2	GND
D7	$\overline{RFSH}$
D0	$\overline{M1}$
D1	$\overline{RESET}$
$\overline{INT}$	$\overline{BUSRQ}$
NMI	$\overline{WAIT}$
$\overline{HALT}$	$\overline{BUSACK}$
$\overline{MREQ}$	$\overline{WR}$
$\overline{IORQ}$	$\overline{RD}$