

```
graph BT; A["/home/timo/Software<br/>/zephyr-riscv_dev/apps<br/>/irqperipheral_test/src<br/>/drivers/fpga-zynq_console<br/>/src/com_fesvr/syscalls.c"] --> C["/home/timo/Software<br/>/zephyr-riscv_dev/apps<br/>/irqperipheral_test/src<br/>/drivers/fpga-zynq_console<br/>/src/com_fesvr/syscalls.h"]; B["/home/timo/Software<br/>/zephyr-riscv_dev/apps<br/>/irqperipheral_test/src<br/>/drivers/fpga-zynq_console<br/>/src/uart_fpgazynq.c"] --> C;
```

/home/timo/Software
/zephyr-riscv_dev/apps
/irqperipheral_test/src
/drivers/fpga-zynq_console
/src/com_fesvr/syscalls.h

/home/timo/Software
/zephyr-riscv_dev/apps
/irqperipheral_test/src
/drivers/fpga-zynq_console
/src/com_fesvr/syscalls.c

/home/timo/Software
/zephyr-riscv_dev/apps
/irqperipheral_test/src
/drivers/fpga-zynq_console
/src/uart_fpgazynq.c