



```
graph BT; A["/home/timo/Software<br/>/zephyr-riscv_dev/apps<br/>/irqperipheral_test/src<br/>/drivers/fpga-zynq_console<br/>/src/com_fesvr/util.h"] --> B["/home/timo/Software<br/>/zephyr-riscv_dev/apps<br/>/irqperipheral_test/src<br/>/drivers/fpga-zynq_console<br/>/src/com_fesvr/syscalls.c"]
```

/home/timo/Software  
/zephyr-riscv\_dev/apps  
/irqperipheral\_test/src  
/drivers/fpga-zynq\_console  
/src/com\_fesvr/util.h

/home/timo/Software  
/zephyr-riscv\_dev/apps  
/irqperipheral\_test/src  
/drivers/fpga-zynq\_console  
/src/com\_fesvr/syscalls.c