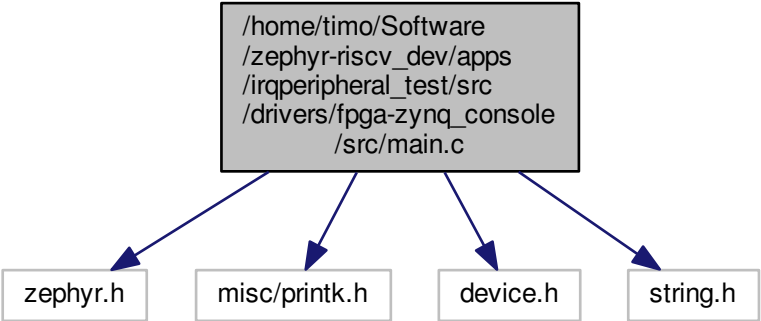


/home/timo/Software
/zephyr-riscv_dev/apps
/irqperipheral_test/src
/drivers/fpga-zynq_console
/src/main.c



```
graph TD; A["/home/timo/Software<br>/zephyr-riscv_dev/apps<br>/irqperipheral_test/src<br>/drivers/fpga-zynq_console<br>/src/main.c"] --> B[zephyr.h]; A --> C[misc/printk.h]; A --> D[device.h]; A --> E[string.h];
```

zephyr.h

misc/printk.h

device.h

string.h