

Semnale de control MIPS32

Instrucțiune	Opcode <i>Instr[31-26]</i>	Reg Dst	ExtOp	ALUSrc	Branch	Br_gez	Jump	Mem Write	Memto Reg	Reg Write	ALUOp[1:0]	function <i>Instr[5-0]</i>	ALUCtrl[2:0]
Add	000000	1	0	0	0	0	0	0	0	1	10(R)	100000	000(+)
Sub	000000	1	0	0	0	0	0	0	0	1	10(R)	100010	100(-)
Sll	000000	1	0	0	0	0	0	0	0	1	10(R)	000000	011(<<)
Srl	000000	1	0	0	0	0	0	0	0	1	10(R)	000010	101(>>)
And	000000	1	0	0	0	0	0	0	0	1	10(R)	100100	001(&)
Or	000000	1	0	0	0	0	0	0	0	1	10(R)	100101	010()
Addi	001000	0	1	1	0	0	0	0	0	1	00(+)	xxxxxx	000(+)
Lw	100011	0	1	1	0	0	0	0	1	1	00(+)	xxxxxx	000(+)
Sw	101011	X	1	1	0	0	0	1	X	0	00(+)	xxxxxx	000(+)
Beq	000100	X	1	0	1	0	0	0	X	0	01(-)	xxxxxx	100(-)
j	000010	X	X	X	X	0	1	0	X	0	xx	xxxxxx	xxx
Ori	001101	0	0	1	0	0	0	0	0	1	11()	xxxxxx	010()
Bgez	000001	X	1	0	0	1	0	0	X	0	01(-)	xxxxxx	100(-)
Sra	000000	1	0	0	0	0	0	0	0	1	10(R)	000011	111(>>a)
Xor	000000	1	0	0	0	0	0	0	0	1	10(R)	100110	110(^)