

	1	2	3	4	5	6
A	DAC interface		MCU interface			
B	File: DAC_interface.kicad_sch		File: MCU_sch.kicad_sch			
C	Output Stage		Power Supplies			
D	Connectors					
	1	2	3	4	5	6

DAC interface



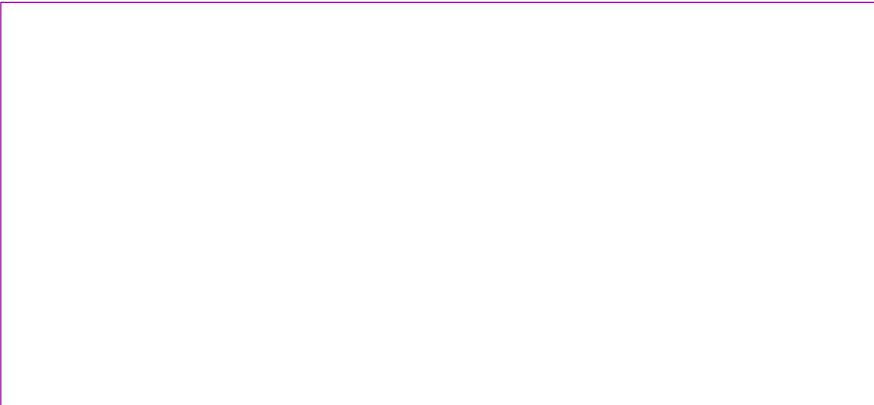
File: DAC_interface.kicad_sch

MCU interface



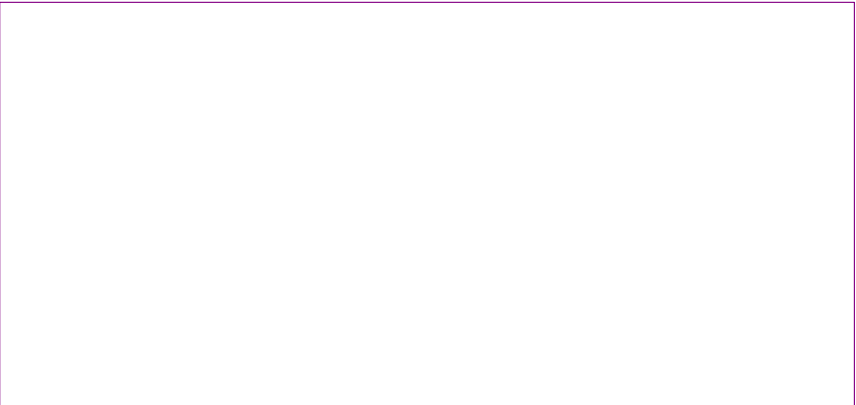
File: MCU_sch.kicad_sch

Output Stage



File: output_stage.kicad_sch

Power Supplies



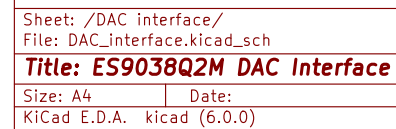
File: power.kicad_sch

Connectors

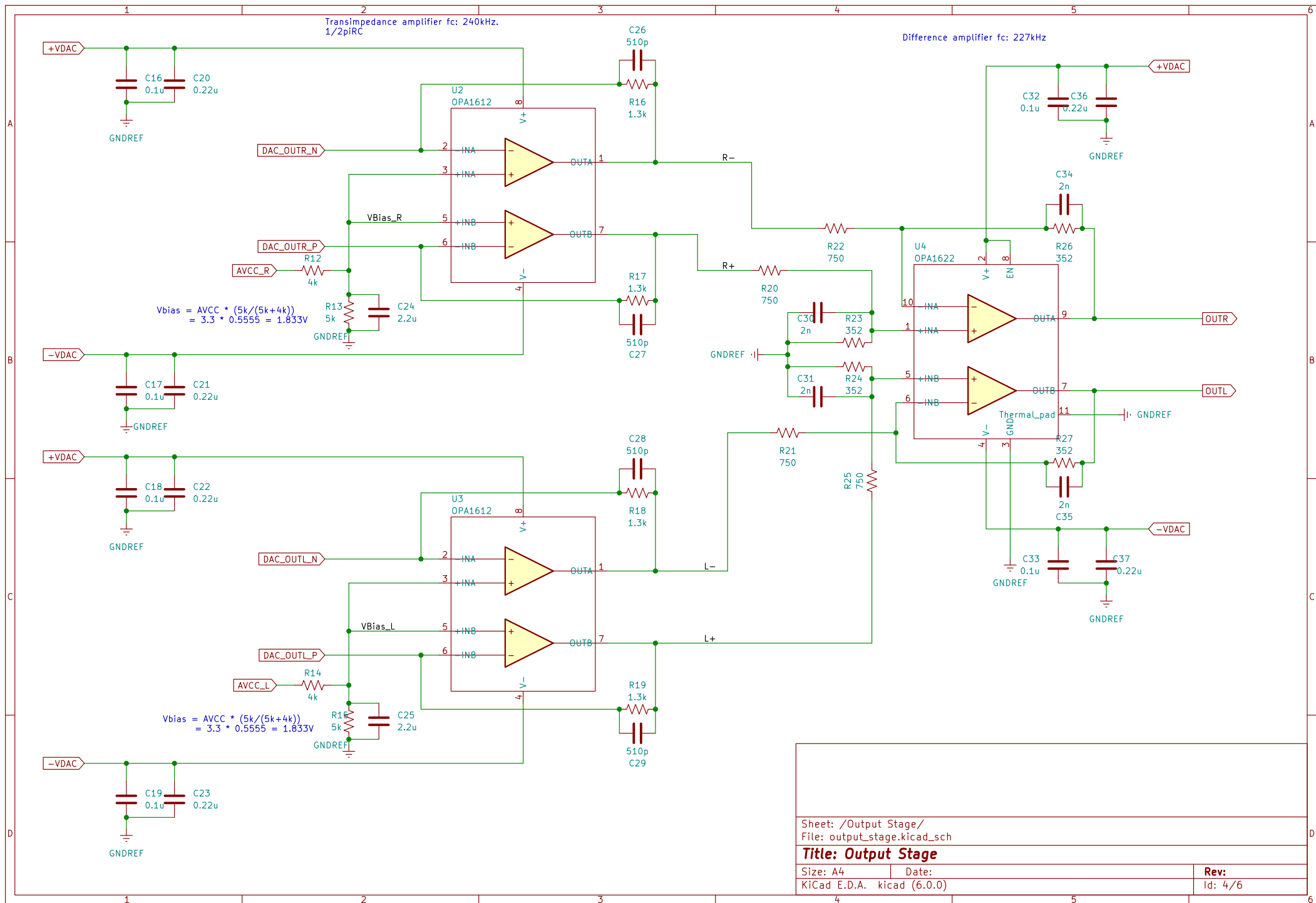


File: connectors.kicad_sch

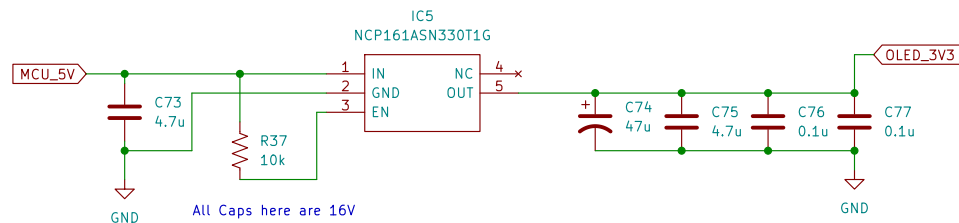
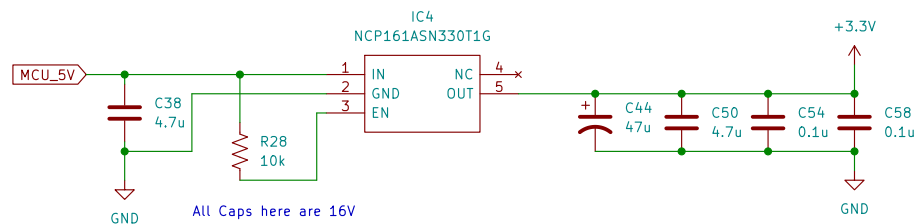
Sheet: /		
File: steDAC.kicad_sch		
Title: SteDAC overall sheet		
Size: A4	Date:	Rev:
KiCad E.D.A. kicad (6.0.0)		Id: 1/6



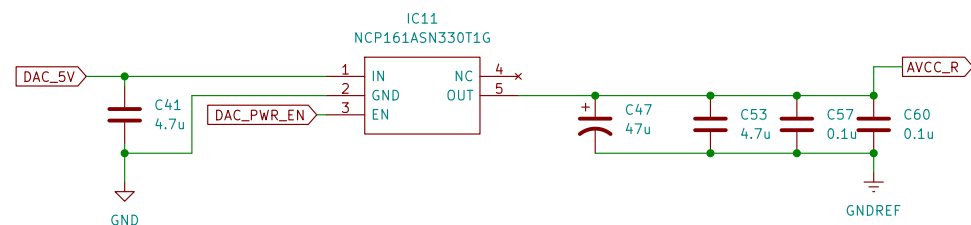
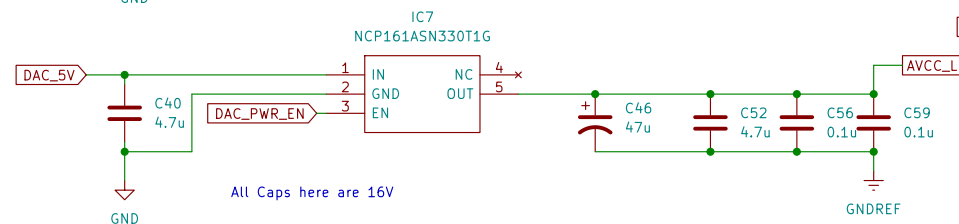
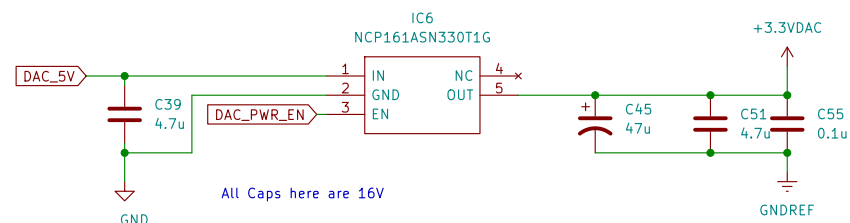




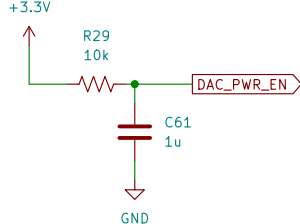
Digital Power



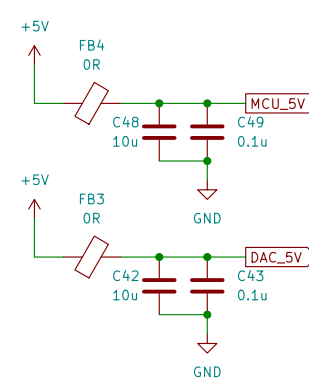
Analog DAC power supplies



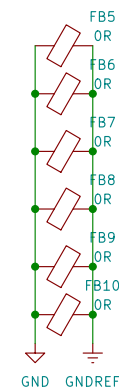
Generating a 3.3V logic high signal for enables



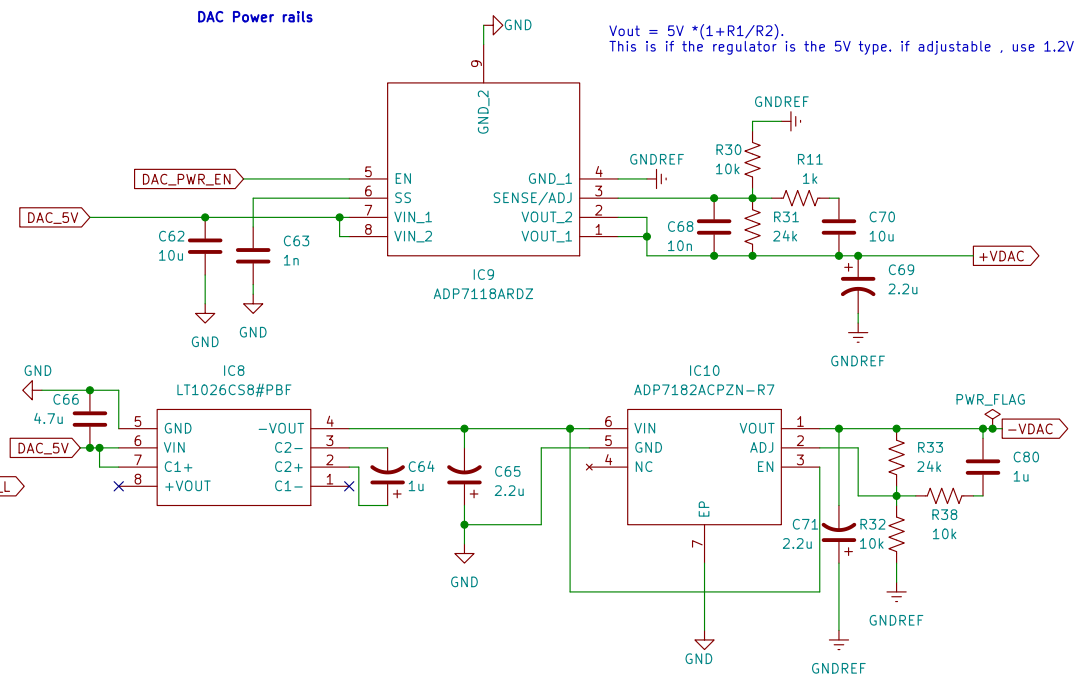
5V Power supply



Connecting analog and digital grounds



DAC Power rails



Sheet: /Power Supplies/
File: power.kicad_sch

Title: Power Supplies

Size: A4

Date:

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Rev:

Id: 5/6

