

1

2

3

4

5

6

A

B

C

D

DAC interface

File: DAC_interface.kicad_sch

MCU interface

File: MCU_sch.kicad_sch

Output Stage

File: output_stage.kicad_sch

Power Supplies

File: power.kicad_sch

Connectors

File: connectors.kicad_sch

Sheet: /
File: steDAC.kicad_sch

Title: **SteDAC overall sheet**

Size: A4

Date:

KiCad E.D.A. kicad (6.0.0)

Rev:
Id: 1/6

1

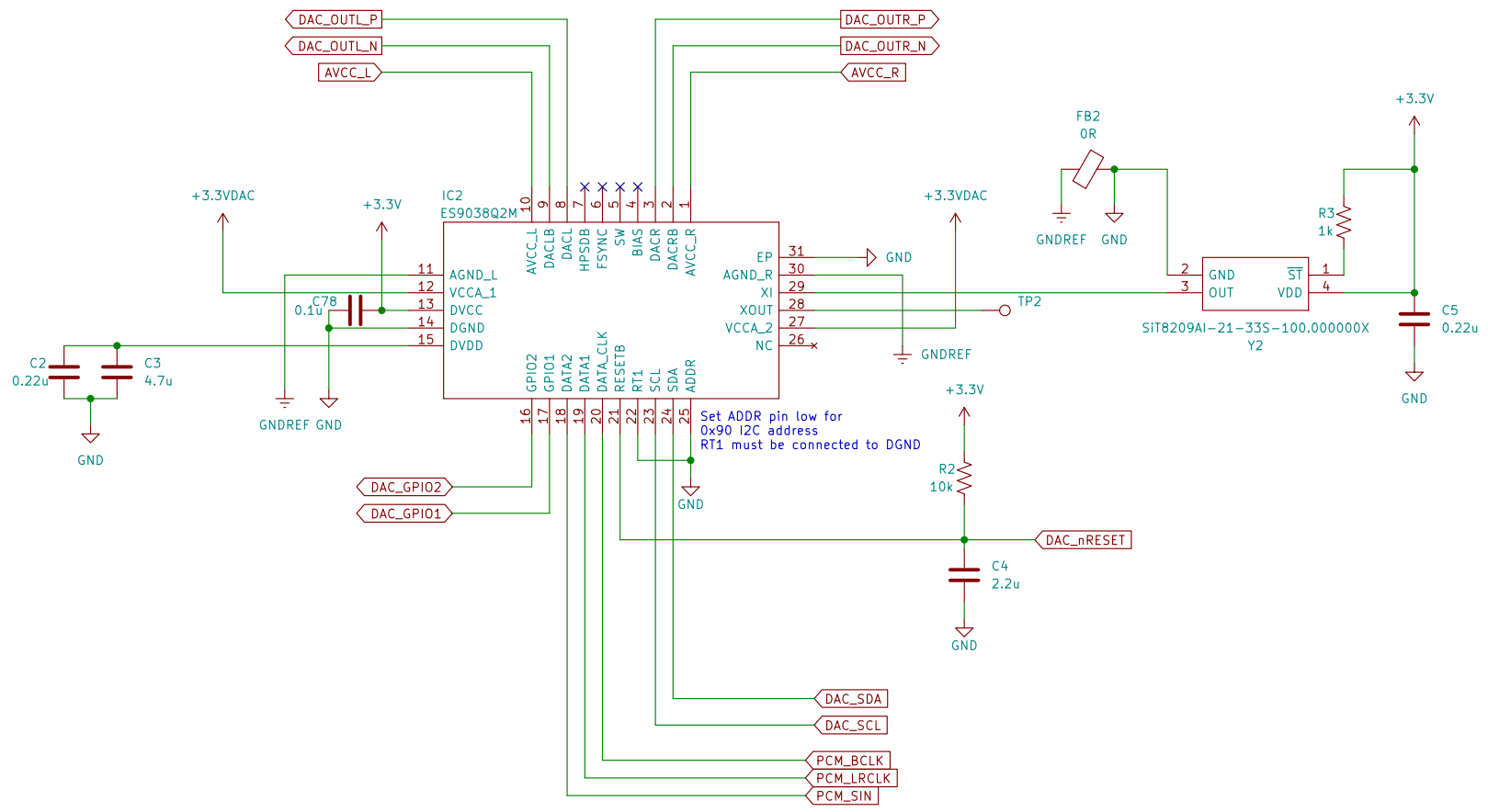
2

3

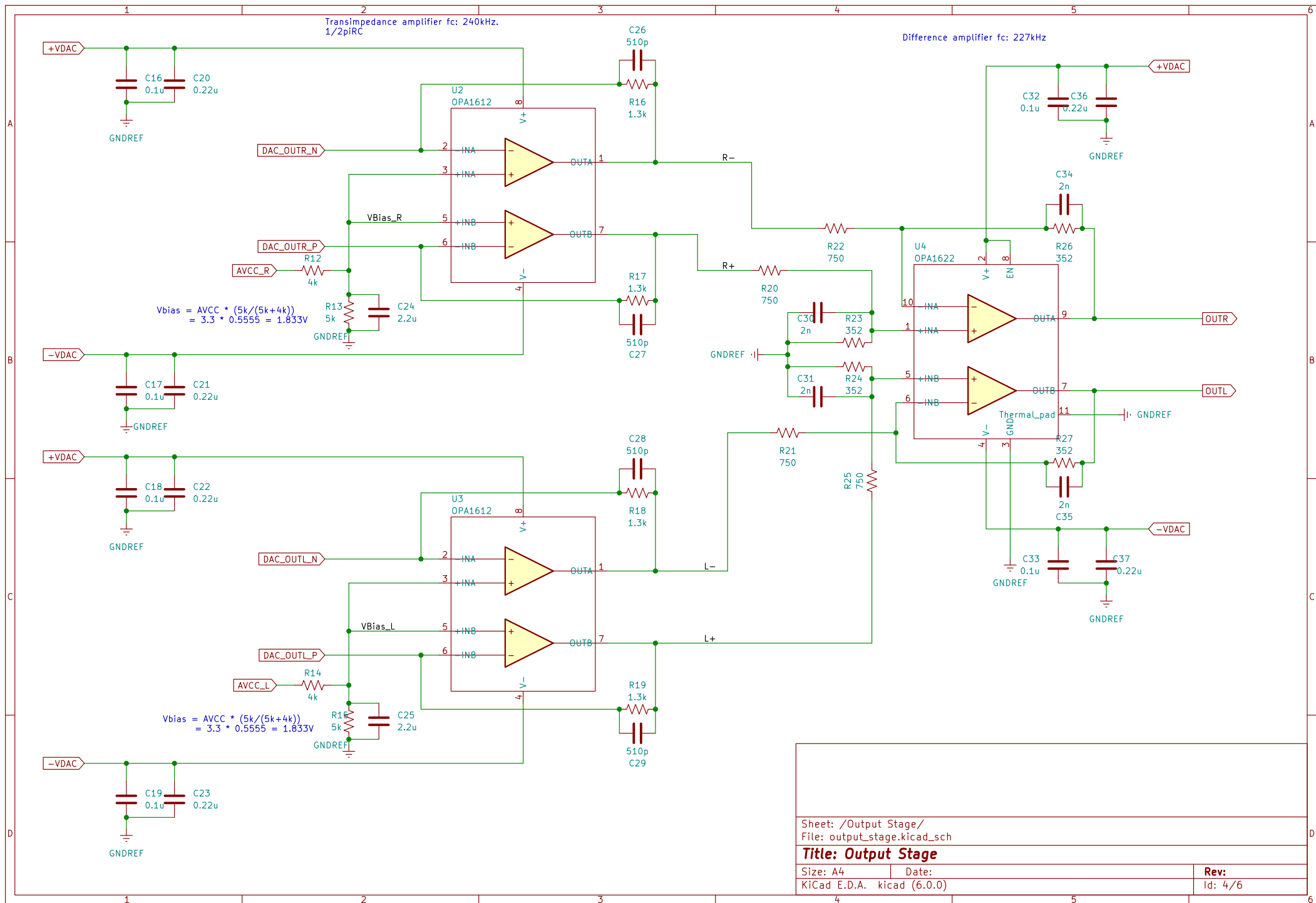
4

5

6







Sheet: /Output Stage/
File: output_stage.kicad_sch

Title: Output Stage

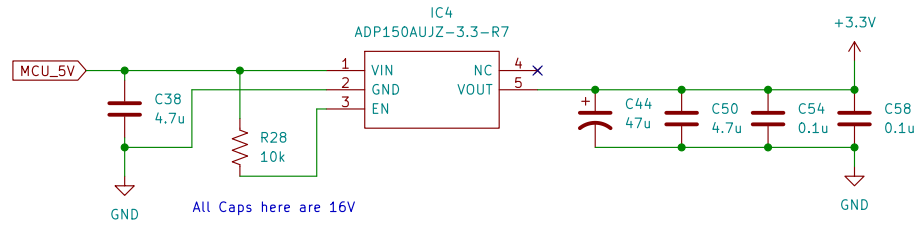
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KiCad E.D.A. kicad (6.0.0)

Date:

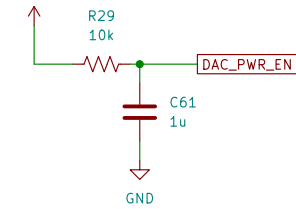
Rev:

Id: 4/6

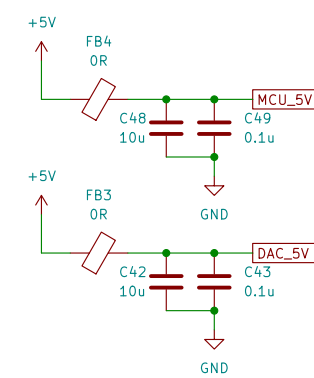
Digital Power



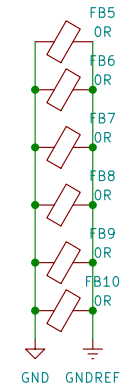
Generating a 3.3V logic high signal for enables



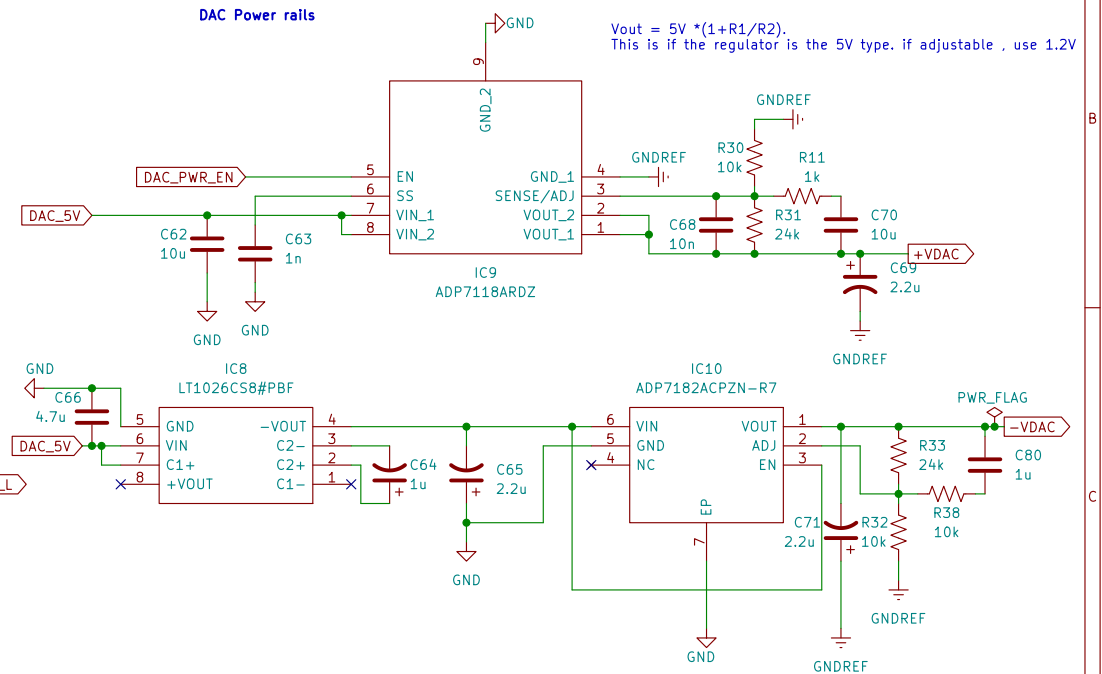
5V Power supply



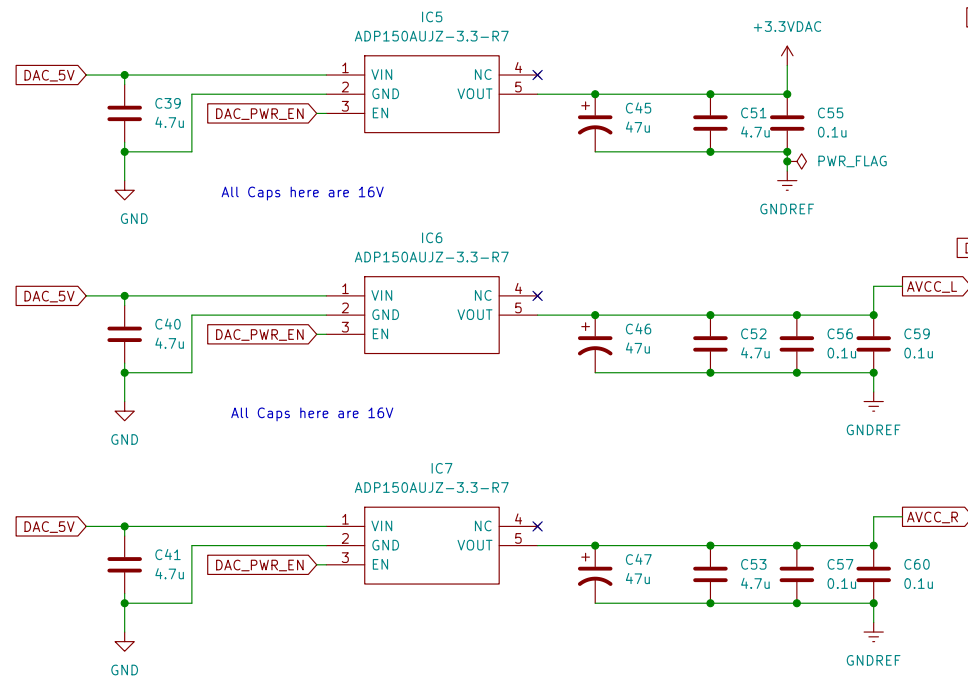
Connecting analog and digital grounds



DAC Power rails



Analog DAC power supplies



Sheet: /Power Supplies/
File: power.kicad_sch

Title: Power Supplies

Size: A4

Date:

KiCad E.D.A. kicad (6.0.0)

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Id: 5/6

