

STEFAN VLASKI

PERSONAL INFORMATION

Address	Electrical Engineering Building, Imperial College London, London SW7 2BT, UK
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Orcid	0000-0002-0616-3076
Publications	h-index 14, i10-index 21, 50+ publications and 2 patents, available on Google Scholar [link] .

EMPLOYMENT HISTORY

Since 09/2021	Imperial College London , Lecturer
10/2019–08/2021	École Polytechnique Fédérale de Lausanne (EPFL) Adaptive Systems Laboratory , Postdoctoral researcher, supervised by Prof. Ali H. Sayed, Lausanne, Switzerland.
10/2017–09/2019	École Polytechnique Fédérale de Lausanne (EPFL) Adaptive Systems Laboratory , Visiting Doctoral Assistant, supervised by Prof. Ali H. Sayed, Lausanne, Switzerland
06/2017–09/2017	Amazon Lab126 , Software Development Engineering Intern, Sunnyvale, CA, USA Environment learning
06/2016–09/2016	Apple Inc. , Engineering Intern, Cupertino, CA, USA Machine learning for audio applications
07/2014–06/2017	UCLA Adaptive Systems Laboratory , Graduate Student Researcher, Los Angeles, CA, USA

EDUCATION

07/2014–09/2019	University of California, Los Angeles , CA, USA Doctor of Philosophy, Electrical and Computer Engineering, GPA: 3.93 <ul style="list-style-type: none">• Date of Defense: June 17th, 2019, Advisor: Prof. Ali H. Sayed• Thesis: Distributed Stochastic Optimization in Non-Differentiable and Non-Convex Environments, available at https://escholarship.org/uc/item/7pb746mg.
09/2013–06/2014	University of California, Los Angeles , CA, USA Master of Science, Electrical Engineering, Signals and Systems Track, GPA: 3.92
10/2010–07/2013	Technical University Darmstadt , Germany Bachelor of Science, Electrical Engineering, GPA: 1.33 on a scale from 1.0 (best) to 5.0 <ul style="list-style-type: none">• Advisor: Prof. Abdelhak M. Zoubir• Bachelor thesis: Robust Bootstrap Methods for Signal Processing

SUPERVISION

Since 09/2021	Supervision of 7 undergraduate and 6 postgraduate student projects at Imperial College London.
06/2019–08/2021	Supervision of 5 student projects and co-supervision of 6 junior PhD students at EPFL.

TEACHING ACTIVITIES

09/2022–07/2023	Department of Electrical and Electronic Engineering, Imperial College London, UK. Module leader for “Distributed Optimization and Learning”, Class lecturer for “Probability and Statistics” with T. Pike and D. Nucinkis, Guest lectures in “Large Dimensional Data Processing” with W. Dai, and tutorials for second year undergraduate students in Electrical Engineering.
09/2021–07/2022	Department of Electrical and Electronic Engineering, Imperial College London, UK. Guest lectures in “Large Dimensional Data Processing” with W. Dai, Seminar Series on “Online Learning and Reinforcement Learning” with K. Leung, and tutorials for second year undergraduate students in Electrical Engineering.
02/2018–06/2019	Institute Electrical Engineering, EPFL, Lausanne, Switzerland Teaching Assistant for “Adaptation and Learning” (postgraduate)
10/2014–03/2017	Department of Electrical Engineering, UCLA, Los Angeles, CA, USA Teaching Associate/Teaching Assistant for “Adaptation and Learning” (postgraduate), “Digital Signal Processing” (undergraduate) and “Systems and Signals” (undergraduate)
10/2011–07/2013	Department of Electrical Engineering, TU Darmstadt, Germany Teaching Assistant for “Logic Design”, “Electrical Engineering I”, “Deterministic Signals and Systems”, “Project Week for 1st Semester Students” and “Stochastic Systems and Signals”

SELECTED SEMINARS, TUTORIALS AND KEYNOTES

08/2022	Keynote on “Provable and Efficient Learning over Networks”, STATOS 2022 Workshop, Belgrade, Serbia.
06/2021	Keynote on “Decentralised Learning in Non-Convex Environments”, 6GIC-CLICK Selected Advanced Topics Workshop series, featuring Wireless AI, Institute for Communication Systems (ICS), University of Surrey, UK.
05/2021	Tutorial on “Federated and Decentralized Multitask Learning”, with R. Nassif and A. H. Sayed, ICASSP 2022, Singapore.
12/2020	Invited seminar on “Learning over Graphs — Beyond Consensus and Convexity”, IEEE Finland SP/CAS Chapter, Helsinki, Finland (held online).

PRIZES, AWARDS, FELLOWSHIPS

12/2019	2nd prize at the best student paper competition of the IEEE International Workshop on Computational Advances in Multi-Sensor Adaptive Processing (CAMSAP), Le Gosier, Guadeloupe.
03/2016	1st prize in the track Signal Processing Theory and Methods at the best student paper competition of the IEEE International Conference on Acoustics, Speech and Signal Processing (ICASSP), Shanghai, China.
09/2013–06/2014	Fully funded tuition and stipend through the UCLA Graduate Division Fellowship.
10/2011–07/2013	Monthly stipend through the German national scholarship (sponsored by Deutsche Telekom AG), awarded to 91 out of 25,000 students at TU Darmstadt.

EXTERNAL VISIBILITY AND ENGAGEMENT

Since 05/2020	Regular session chair at international conferences such as IEEE ICASSP and EUSIPCO.
05/2020	Co-organiser and chair of a special session on “Learning and Optimization in Non-Convex Environments” at IEEE ICASSP 2020, Barcelona Spain.

Since 08/2014 Regular reviewer for IEEE Signal Processing Magazine, Proceedings of the IEEE, IEEE Transactions on Signal Processing, IEEE Transactions on Signal and Information Processing over Networks, IEEE Transactions on Automatic Control, IEEE Transactions on Control of Network Systems, IEEE Transactions on Parallel and Distributed Systems, IEEE/ACM Transactions on Networking, IEEE Signal Processing Letters, IEEE CAMSAP, IEEE ICASSP, Elsevier Signal Processing.

MEMBERSHIPS

Since 2020 Member of the Institute of Electrical and Electronics Engineers (IEEE) and the IEEE Signal Processing Society.

2013–2019 Student member of IEEE and the IEEE Signal Processing Society.

PATENTS

03/2020 M. Mansour, S. Kandadai, and **S. Vlaski**, assigned to **Amazon Technologies, Inc.**, “Multi microphone wall detection and location estimation”, *US Patent 10,598,543*, March 2020, granted. <https://patents.google.com/patent/US10598543B1/en>

05/2017 D. Marković, A. H. Sayed, S. Basir-Kazeruni, **S. Vlaski**, and H. Salami, assigned to **The Regents of the University of California**, “Systems and Methods for Reducing Noise Caused By Stimulation Artifacts in Neural Signals Received By Neuro-Modulation Devices”, *US Patent App. 16/306,234*, May 2017, pending. <https://patents.google.com/patent/US20190125269A1/en>

London, April 2nd, 2023