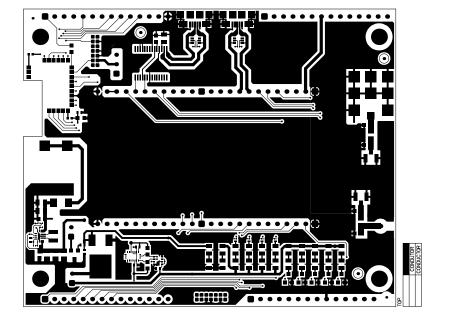
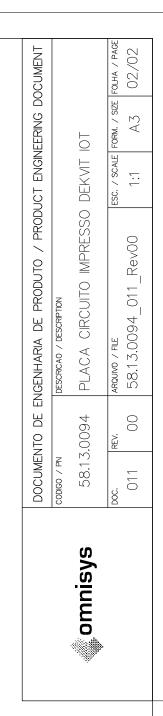
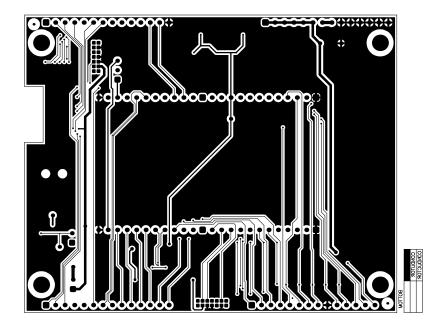
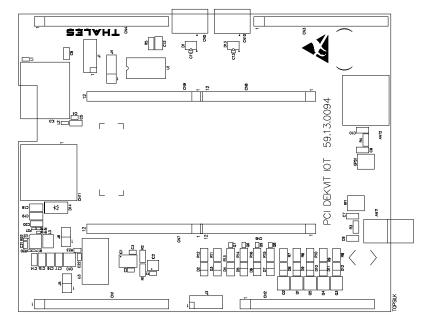
EMISSAO NICIAL / FIRST ISSUE	8		D. Aguiar	iar	E. Rodrigues	E. Rodrigues	L. Degenhardt	dt
HISTORICO DA REVISAO / REVISION HISTORY	REV.	DATA / DATE		ADO / ELABORATED	ELABORADO / ELABORATED VERFICADO / CHECKED	APROVADO / APPROVED		QUALIDADE / QUALITY
		DOCUMEN	UTO DE EN	JGENHARIA D	DOCUMENTO DE ENGENHARIA DE PRODUTO / PRODUCT ENGINEERING DOCUMENT	RODUCT ENGIN	EERING DC	CUMENT
		CODIGO / PN		DESCRICAO / DESCRIPTION	SRIPTION			
omnisys	ູ້	58.13	58.13.0094	PLACA CIF	PLACA CIRCUITO IMPRESSO DEKVIT IOT	SSO DEKVIT I	LC	
×		DOC.	REV.	ARQUIVO / FILE		ESC. / SCALE FORM. / SIZE FOLHA / PAGE	FORM. / SIZE	FOLHA / PAGE
		011	00	58.13.009	58.13.0094_011_Rev00	<u></u>	A3 01/02	01/02

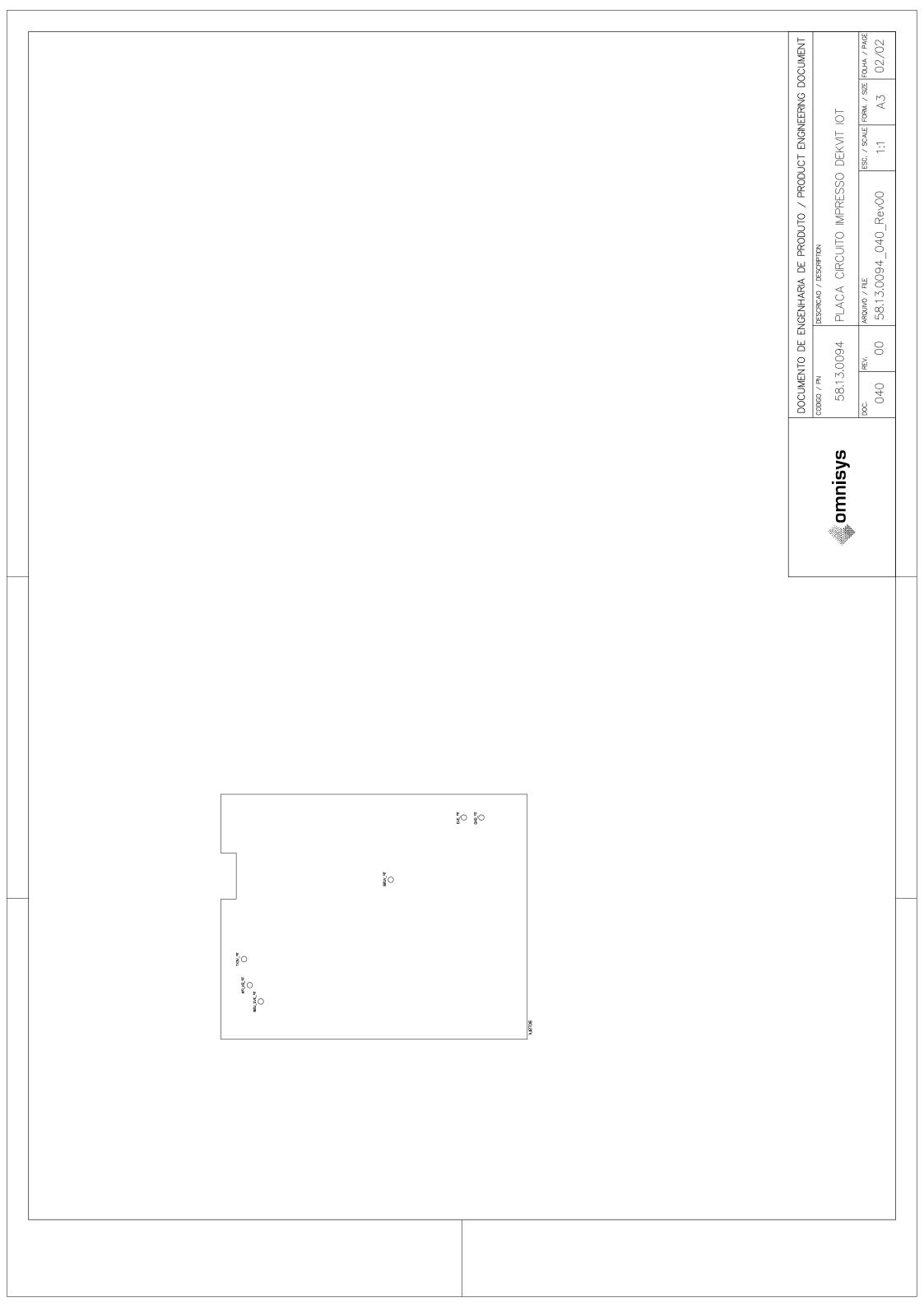




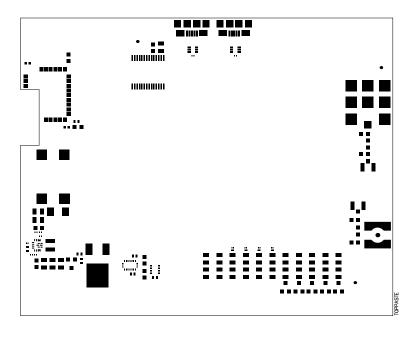


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		DOCUMEN	JTO DE EN	NGENHARIA D	DOCUMENTO DE ENGENHARIA DE PRODUTO / PRODUCT ENGINEERING DOCUMENT	RODUCT ENGINE	ERING DO	COMEN-
		CODIGO / PN		DESCRICAO / DESCRIPTION	CRIPTION			
omnisys	S)	58.13.	58.13.0094	PLACA CII	PLACA CIRCUITO IMPRESSO DEKVIT IOT	SO DEKVIT IG	LC	
>		DOC.	REV.	ARQUIVO / FILE		ESC. / SCALE FORM. / SIZE FOLHA / PAGE	ORM. / SIZE	FOLHA / PAG
		040	00	58.13.009	58.13.0094_040_Rev00	<u></u>	A3	A3 01/02

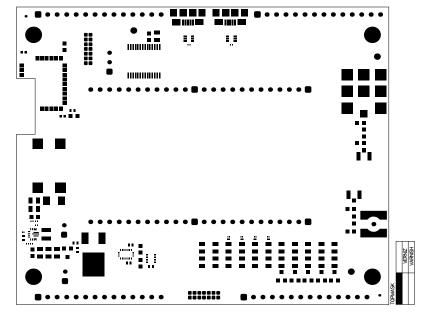


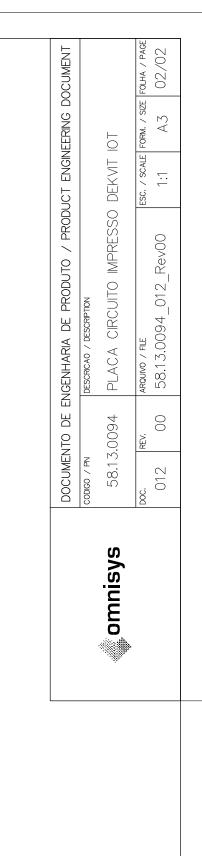


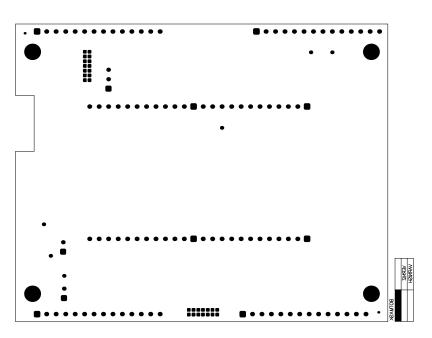
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		DOCUMEN	TO DE EN	NGENHARIA D	DOCUMENTO DE ENGENHARIA DE PRODUTO / PRODUCT ENGINEERING DOCUMENT	RODUCT ENGINE	ERING DOCUM
		CODIGO / PN		DESCRICAO / DESCRIPTION	CRIPTION		
omnisys	ົດ	58.13.	58.13.0094	PLACA CIF	PLACA CIRCUITO IMPRESSO DEKVIT IOT	SSO DEKVIT IN	LC
>		DOC.	REV.	ARQUIVO / FILE		ESC. / SCALE	ESC. / SCALE FORM. / SIZE FOLHA / PAGE
		235	00	58.13.009	58.13.0094_235_Rev00	<u></u>	A3 01/01

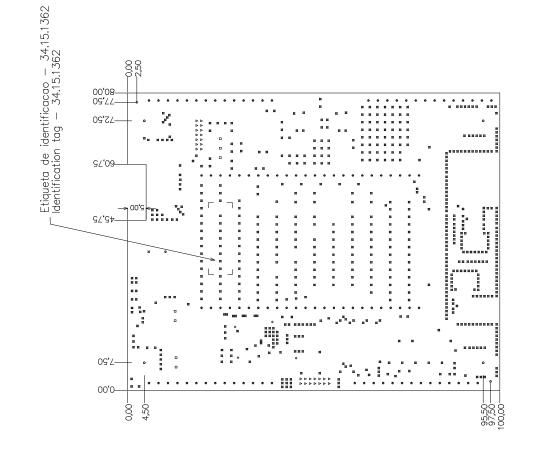


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		DOCUMENT	TO DE ENGENHARI	DOCUMENTO DE ENGENHARIA DE PRODUTO / PRODUCT ENGINEERING DOCUMENT	RODUCT ENGINEE	RING DOCUMEN
		CODIGO / PN	DESCRICAO /	DESCRICAO / DESCRIPTION		
omnisys	S/	58.13.(0094 PLACA	58.13.0094 PLACA CIRCUITO IMPRESSO DEKVIT IOT	SSO DEKVIT 10	<u></u>
»		DOC.	REV. ARQUIVO / FILE OO 58.13.00	ARQUIVO / FILE 58.13.0094_012_Rev00		ESC. / SCALE FORM. / SIZE FOLHA / PAGE 1:1 A 3 01/02









Plated Via/Pad	Pad	Pad	۷ia	Pad	۷ia	Pad	Pad	Pad	Pad	Via	
Plated	NPTH	NPTH	PTH	NPTH	PTH	PTH	PTH	PTH	PTH	PTH	
Hole Size	66,93mil (1,700mm)	70,87mil (1,800mm)	20,00mil (0,508mm)	98,43mil (2,500mm)	12,00mil (0,305mm)	137,79mil (3,500mm) PTH	39,00mil (0,991mm)	28,00mil (0,711mm)	43,31mil (1,100mm)	8,00mil (0,203mm)	
	1	_	2	2	4	4	7	28	104	695	848 Total
Symbol Count	¤	⇔		0		Ф		\triangleright	0	×	

			ESPE	CIFICACA	ESPECIFICACAO DE FABRICACAO / MANUFACTURING SPECIFICATION	CAO / MAN	UFACTUR	NG SPECIFIC	ATION.	
Acabament Finish: ENIG	Acabamento: ENIG Finish: ENIG	9		Material: Material:	Material: 84200230 HP Material: 84200230 HP	0 0				
Metalizc Plated-	acao: (X) Through t	Metalizacao: (X) sim () nao Plated—Through holes: (X) yes () no	Linhas (Controlle	Linhas de impedancia controlada (X) sim (Controlled impedance lines (X) yes () no	controlada ines (X) yes	(X) sim	opu ()	Espessur	Linhas de impedancia controlada (X) sim () nao Espessura entre camadas: 1,6mm Controlled impedance lines (X) yes () no
Cu: M	Cu: min. 20um Cu: Min. thickness 20um	ss 20um		Teste el Electric	letrico (X) sim Test (X) yes	opu ()	Dimenso Minimum	es minimas; dimensions	: Trilha 5m :: Track 5n	Teste eletrico (X) sim () nao Dimensoes minimas: Trilha 5mils Pad 18mils Isolacao 5mils Electric Test (X) yes () no Minimum dimensions: Track 5mils Pad 18mils Gap 5mils
Contatc Edge co	os de bord ontacts: (Contatos de borda: () sim (X) Edge contacts: () yes (X) no	Contatos de borda: () sim (X) nao Edge contacts: () yes (X) no	Cupom Test co	Cupom de teste () sim (X) nao Test coupon () yes (X) no	sim (X) r		Classificaca PCB classifi	o da PCI: cation: Cla	Classificacao da PCI: Classe 2 IPC—6012C PCB classification: Classe 2 IPC—6012C
Ni: min. 6um Ni: min. 6um	. 6um	Au: min. 0,8um Au: min. 0,8um	. 0,8um	Mascarc Green s	Mascara anti-solda Verde nos dois lados (X) sim () nao Green soldermask on both side (X) yes () no	erde nos do voth side (X	is lados () mis (X)	000 (
Tolerand Hole tol	cia dos fu erance:	Tolerancia dos furos: $+/-$ 0,10 Hole tolerance: $+/-$ 0,10mm	Tolerancia dos furos: +/- 0,10mm Hole tolerance: +/- 0,10mm	Mascarc White m	Mascara seletiva Branca (X) sim () nao White markings (X) yes () no	sa (X) sim s () no	ou ()	0		
				Dimensa	Dimensoes em mm Tolerancia geral: +/- 0,2mm Dimensions in mm General tolerance: +/- 0,2mm	Tolerancia General to	geral: +_	Tolerancia geral: +/- 0,2mm General tolerance: +/- 0,2mm		Empenamento maximo: 0,7% Maximum bending: 0,7% Is requested by 16262721 specification
Camada ^E	Esp. do Cobre,	Camada Esp. do Cobre/Cu thickness			Esp. da isolacao Empilhamento Furacao Esp. da placa Impedancia	Empilhamenta	Furacao	Sp. da placa	Impedancia	
Layer	Base	Ξ. Mi.	Material reference		Insula, thickness	Stacking	Drilling	lhickness	Impedance	Additional information
01/04	17,5um	33,4um	EPOXY HIGH PERFORMANCE	ORMANCE	1.6mm		<u></u>	×01-/-	sim/yes	Sim/yes Iinhas 70 Ohms / Iines 70 Ohms (60mils)
02/04	17,5um	33,4um						+99'1	ou/opu	
Exigenci	as gerais	/ General	Exigencias gerais / General requirements:							

Exigencias specificas / Specific requirements: RF PCB to be controlled according 16261983 Specification

 $\left\langle xxx\right\rangle$ Preencher com o codigo do fornecedor $\left\langle xxx\right\rangle$ Fill in the supplier code

(xxxxxx) Data de fabricacao / Date code (x/x) Painel e pos. no painel / Panel and position on the panel

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		DOCUMENT) DE ENGENHAF	DOCUMENTO DE ENGENHARIA DE PRODUTO / PRODUCT ENGINEERING DOCUMENT	RODUCT ENGINE	ERING DOC	JMENT
		CODIGO / PN	DESCRICAO	DESCRICAO / DESCRIPTION			
omnisys	ຶ່	58.13.0	094 PLAC	58.13.0094 PLACA CIRCUITO IMPRESSO DEKVIT IOT	SSO DEKVIT IC	T(
,		Doc. REV.	7. ARQUINO / FILE 00 58.13.00	ARQUINO / FILE 00 58.13.0094_003_Rev00		ESC. / SCALE FORM. / SIZE FOLHA / PAGE	14 / PAGE