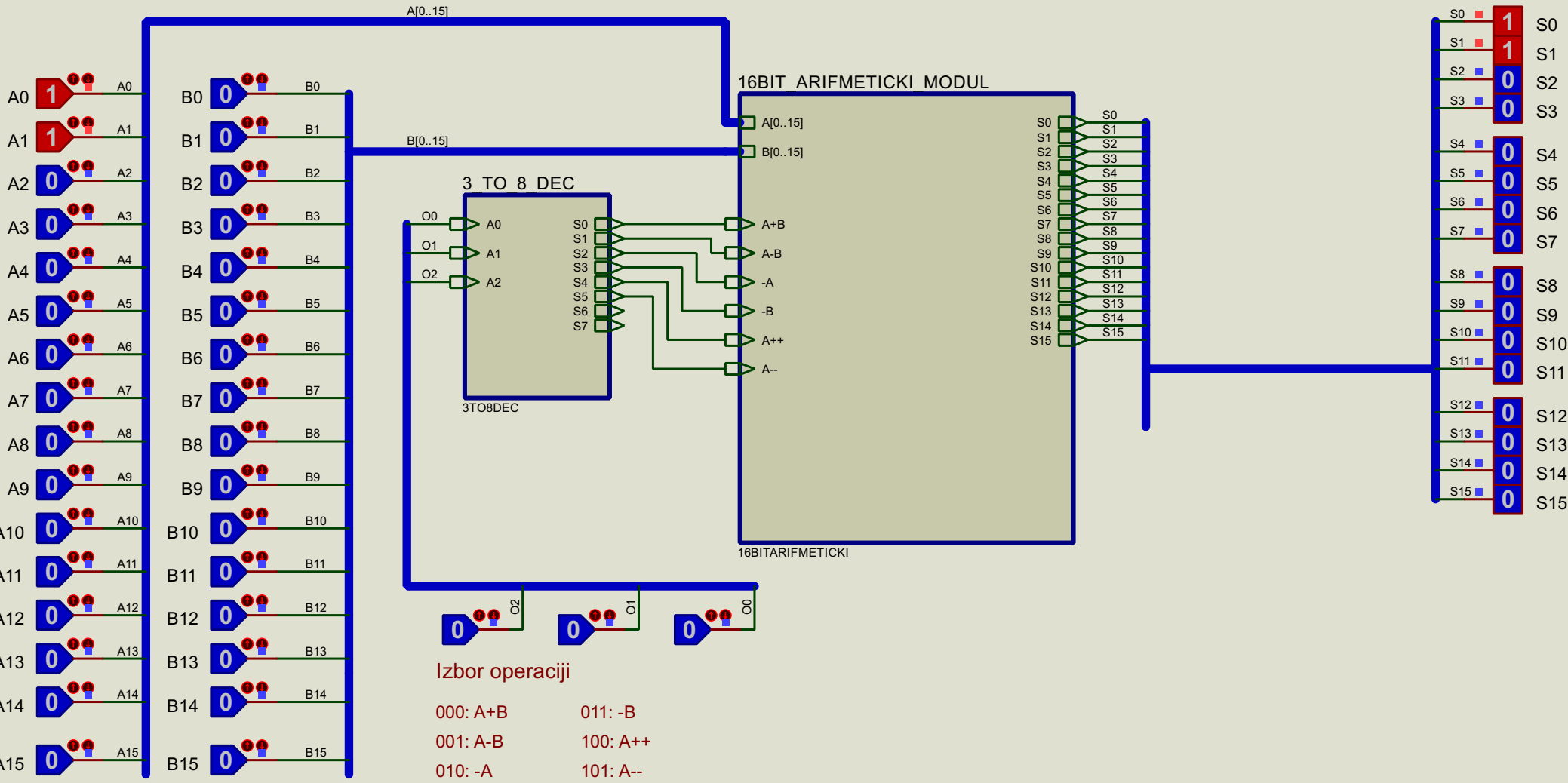


# Aritmeticki modul.

A i B su 16 bitni brojeva sa predznakom (Z broj)

A+B, A-B, -A, -B, A+1, A-1

(Bez namenskih kola)

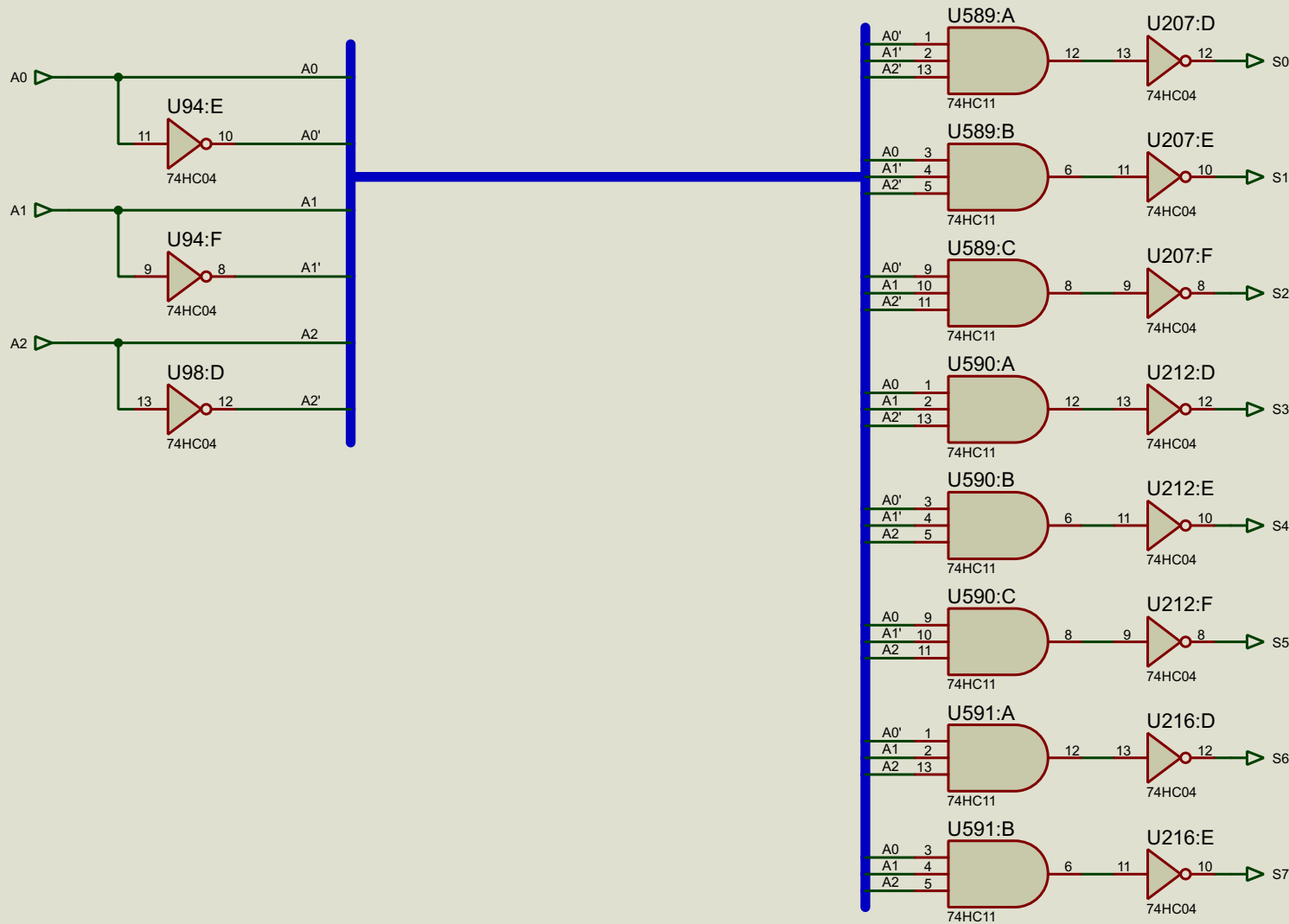


Ako A15/B15 je 0 - pozitivan broj

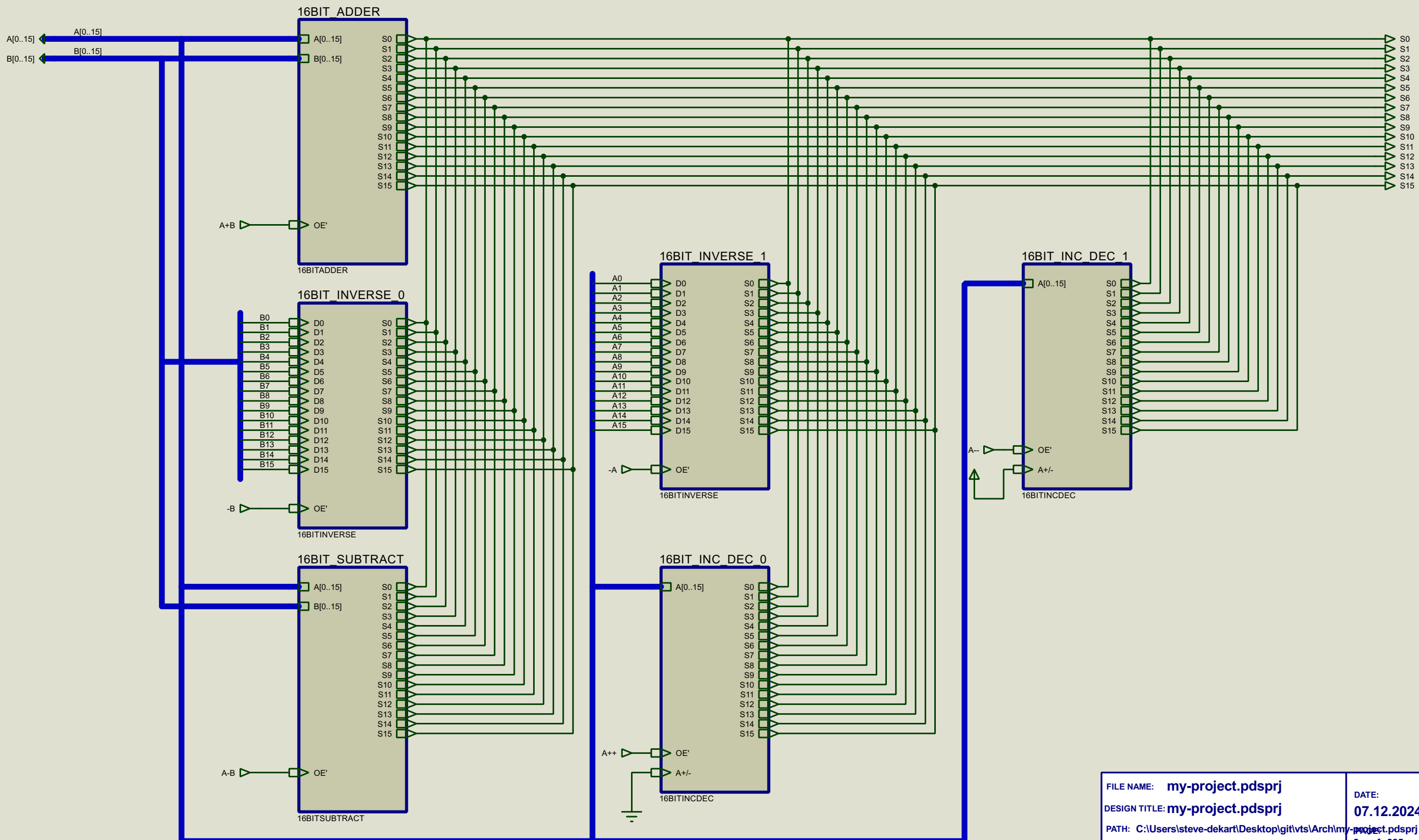
Ako A15/B15 je 1 - negativan broj

# 3 to 8 Decoder (Inverse)

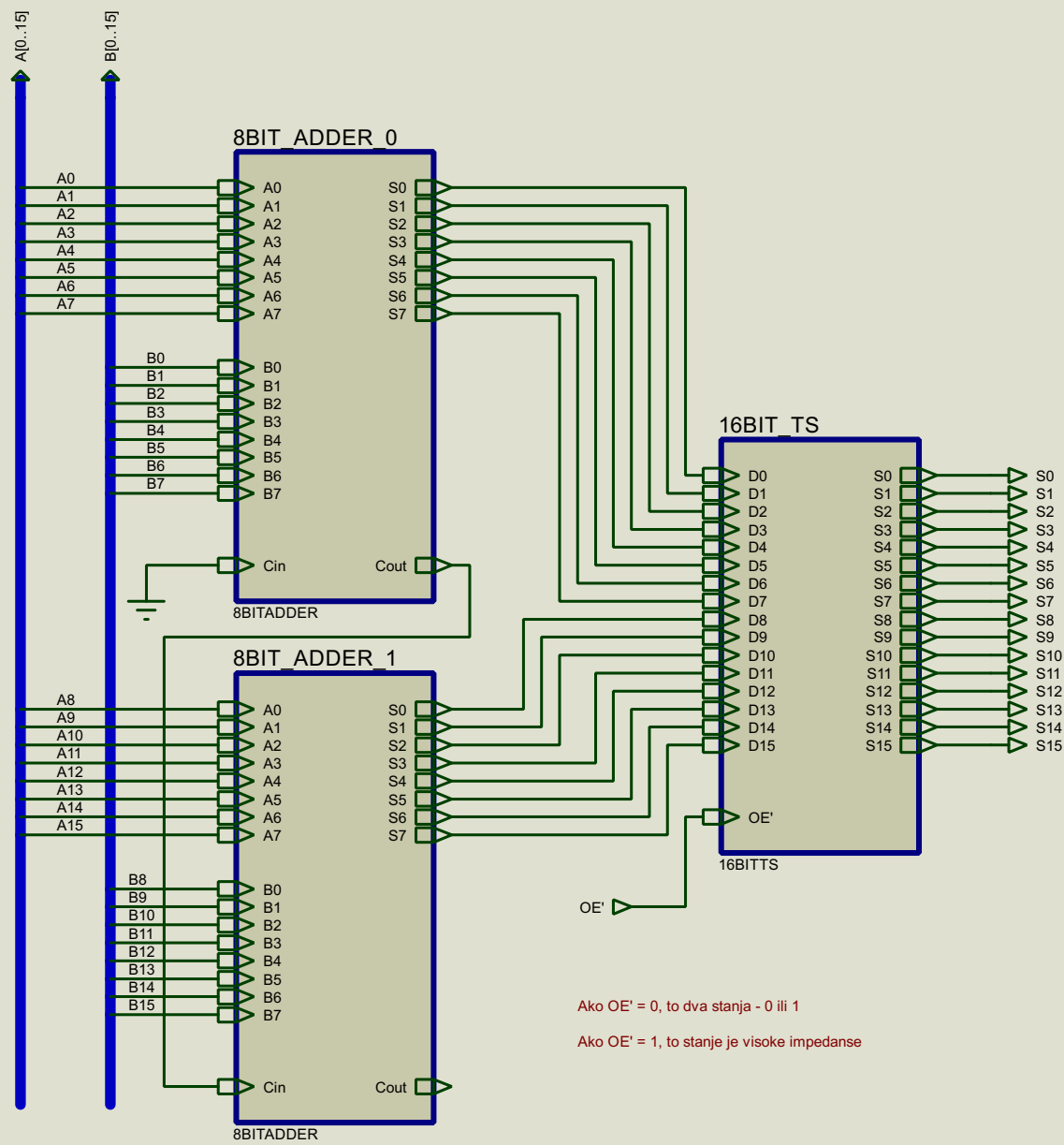
Za izbor arifmeticki operaciji



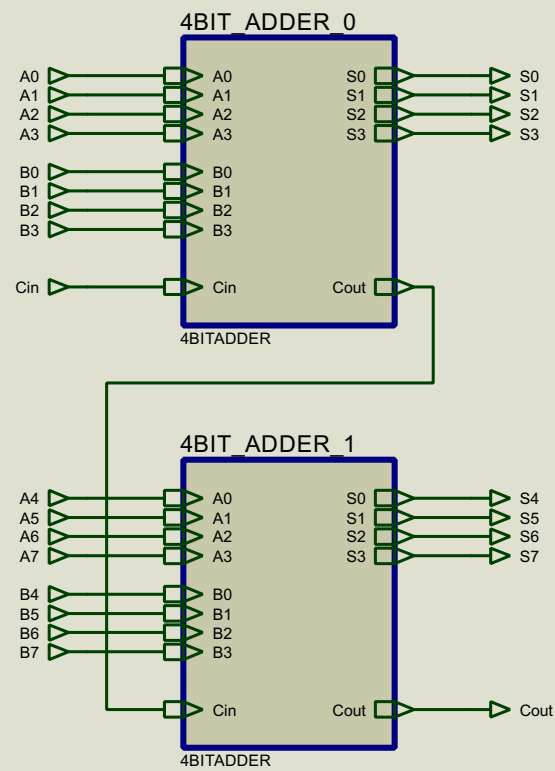
# 16bit Arifmeticki modul



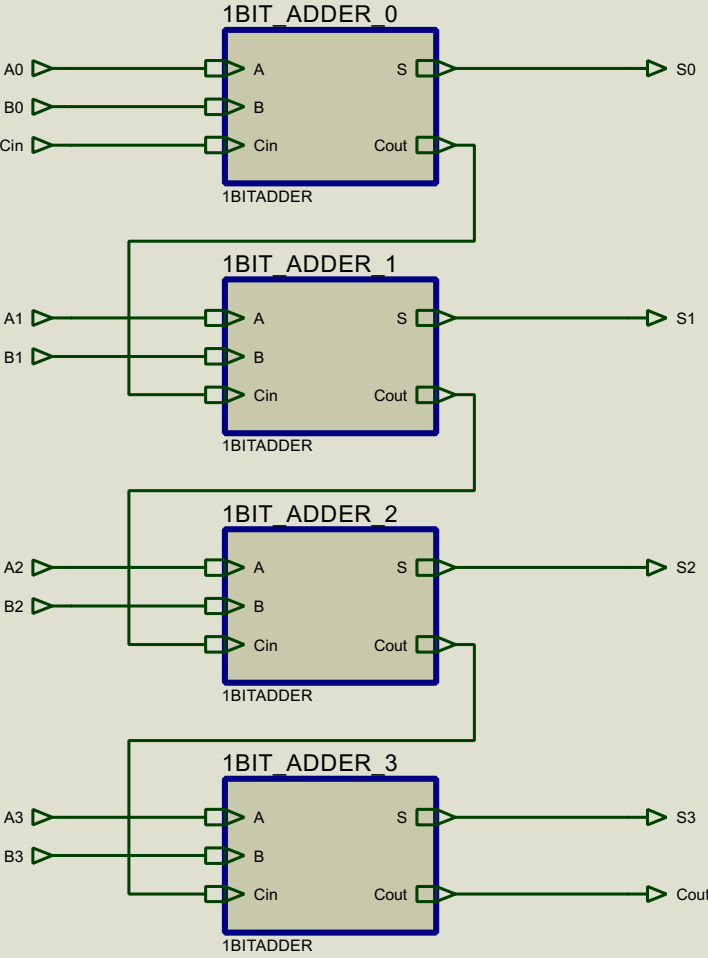
# 16bit Full Adder



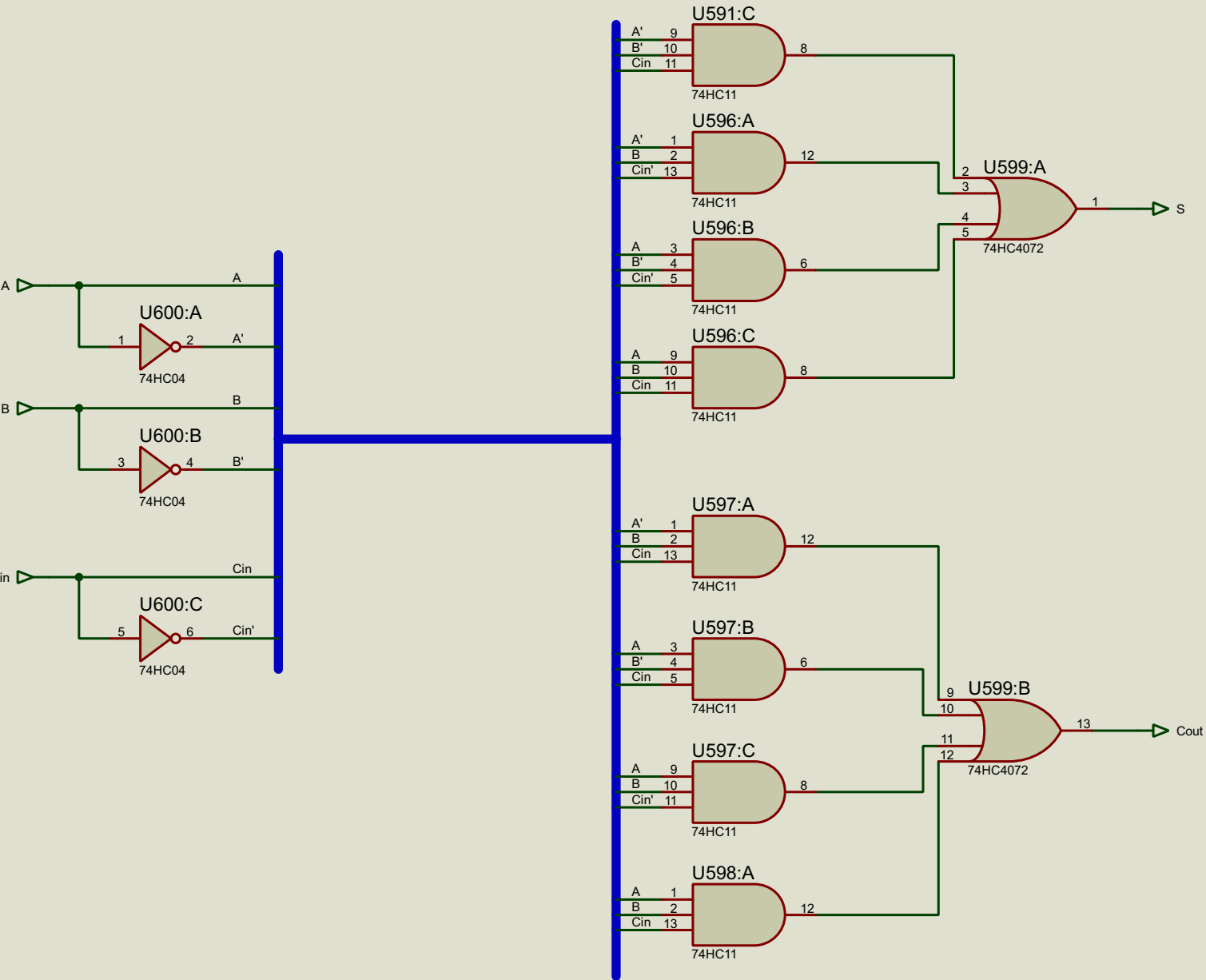
# 8bit Full Adder



# 4bit Full Adder

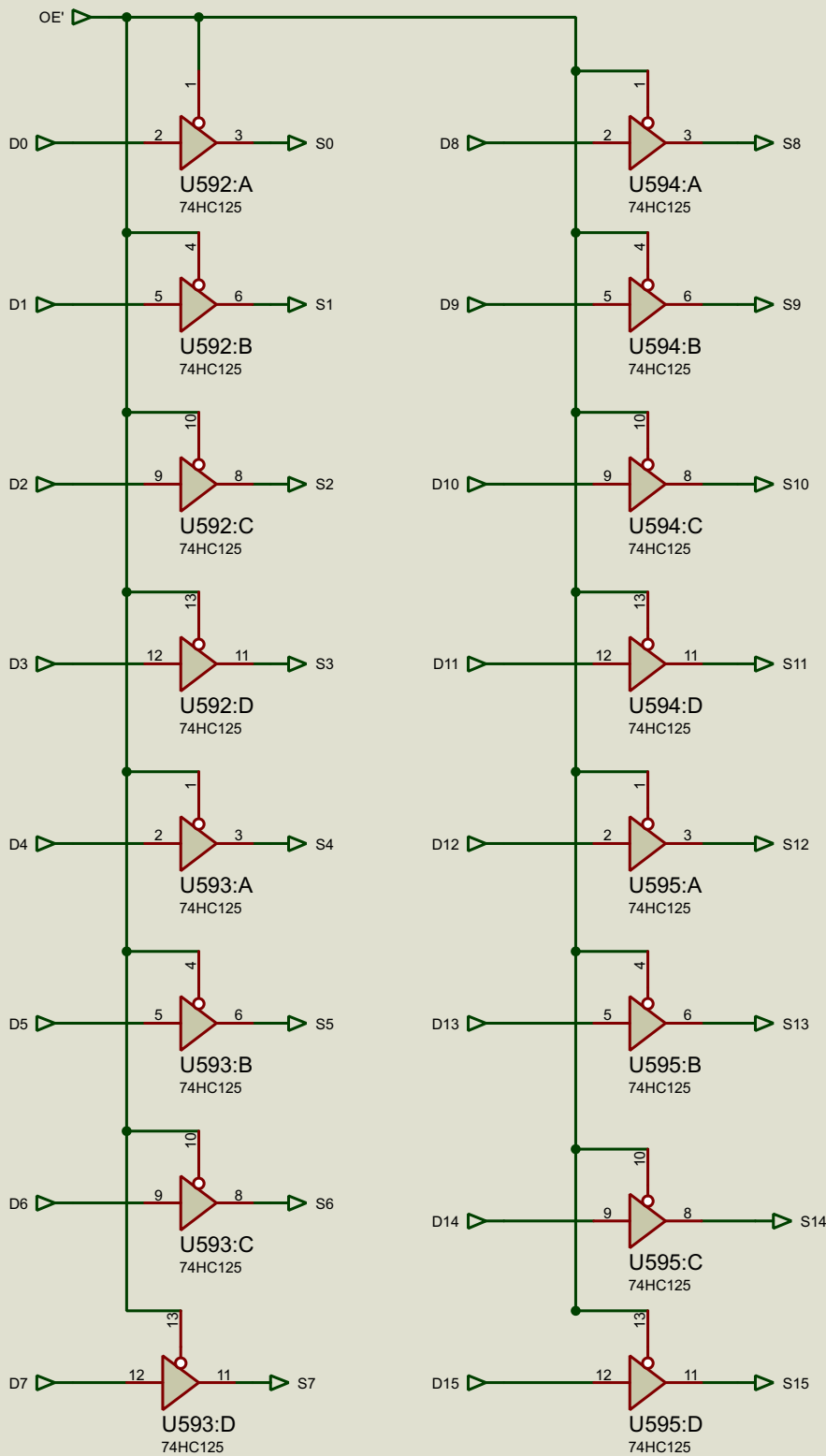


# 1Bit Full Adder



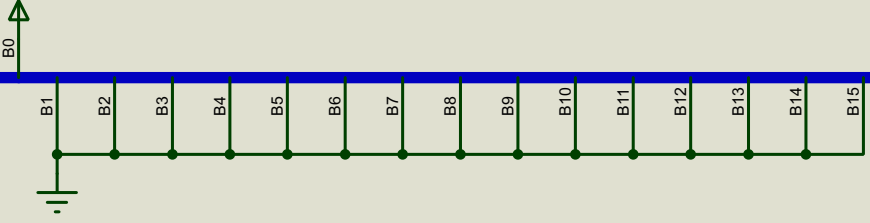
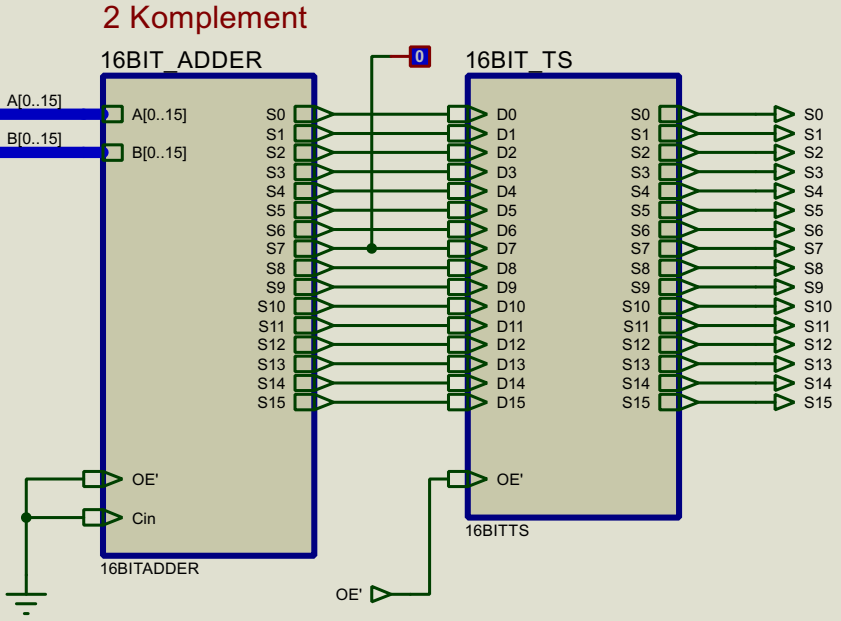
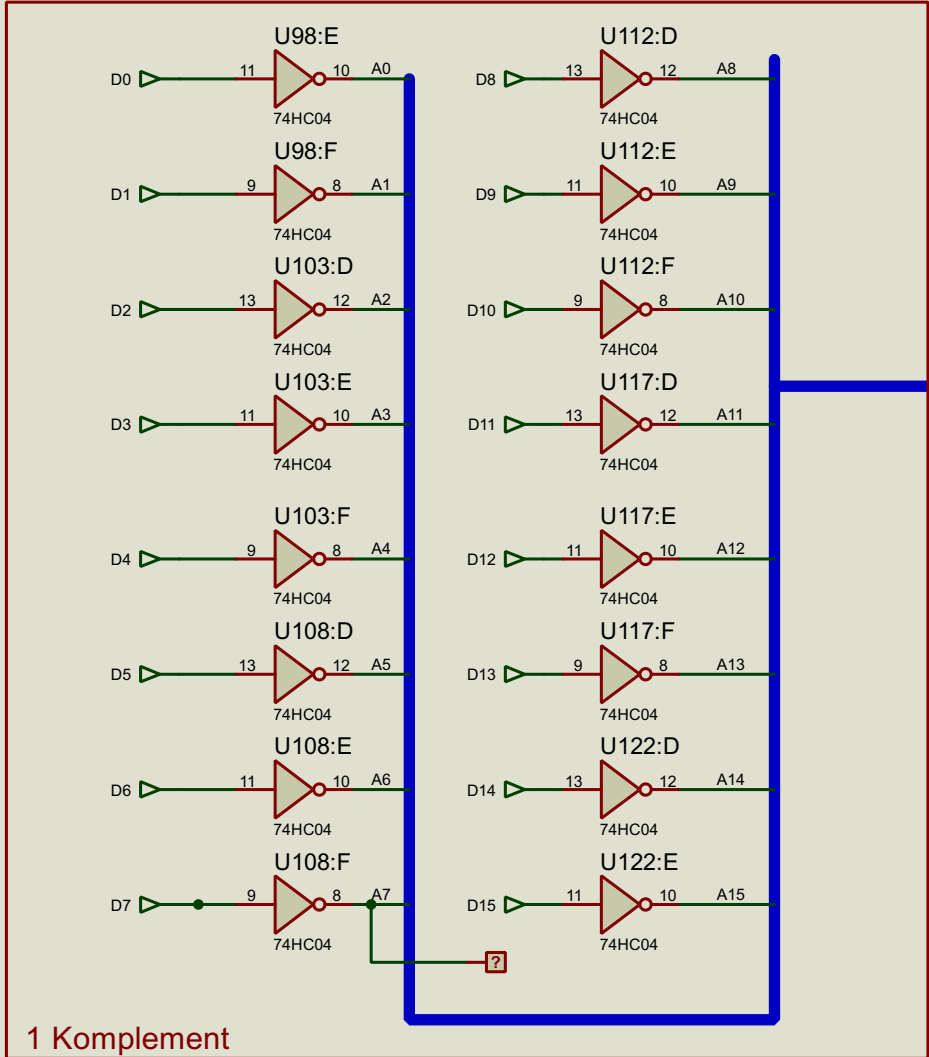
# 16bit TS output

Ako OE' = 0, to dva stanja - 0 ili 1  
Ako OE' = 1, to stanje je visoke impedanse

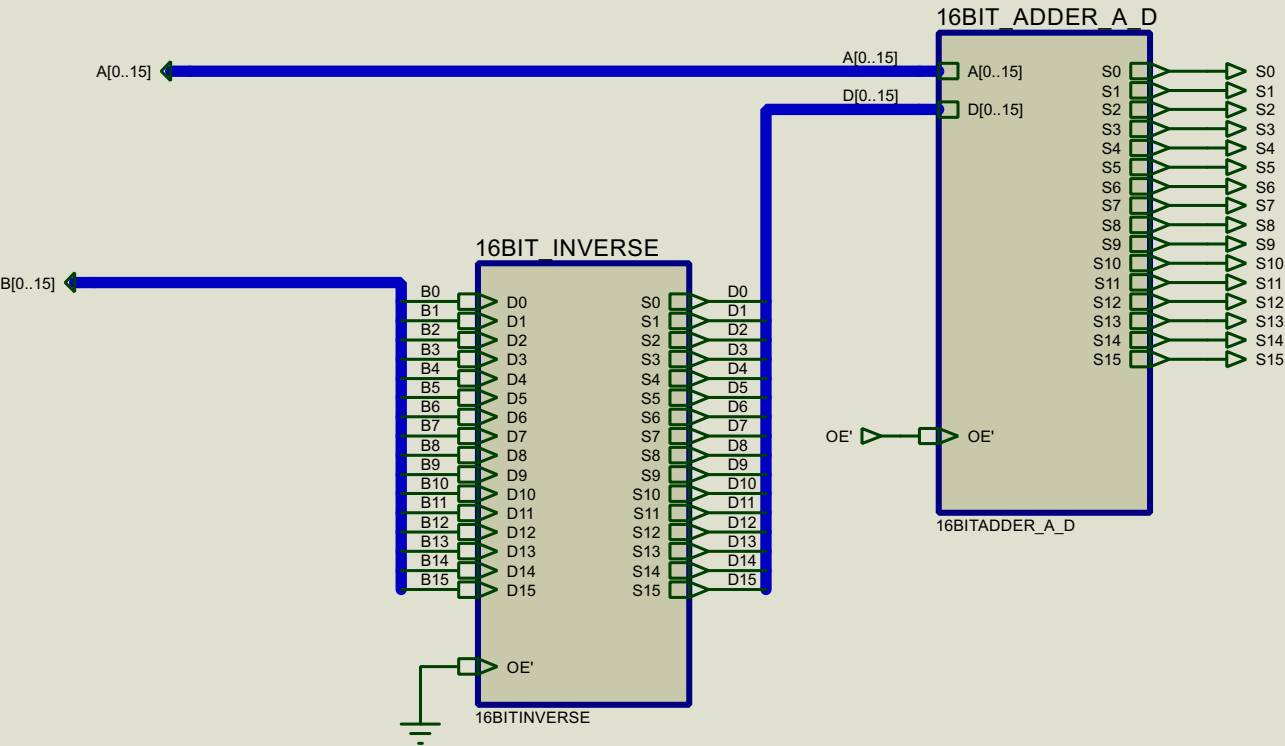




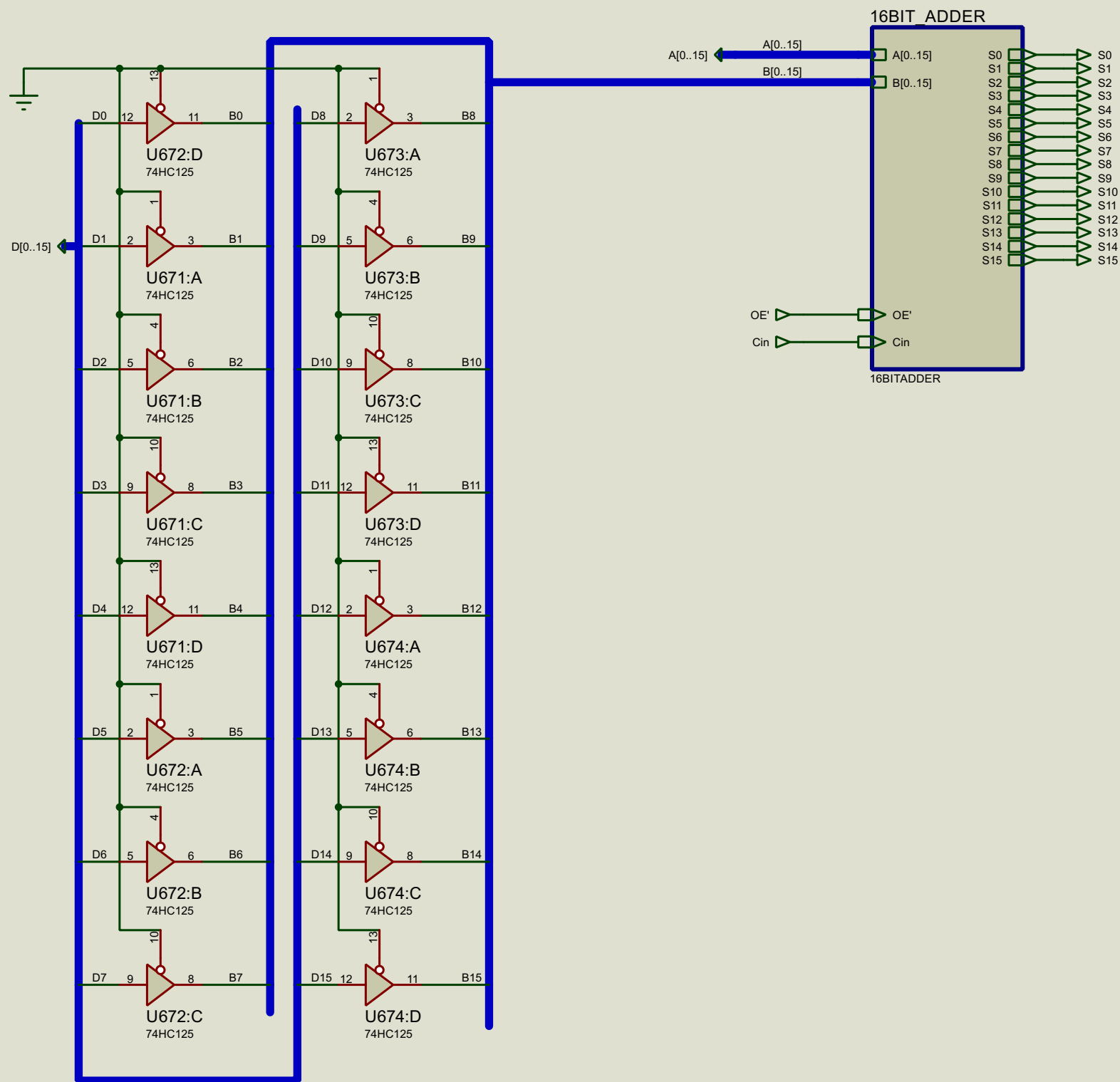
# 16bit Inverse



# 16bit Subtractor



# 16bit Full Adder (wiith A and D bus)



# 16bit Increment/Decrement A

