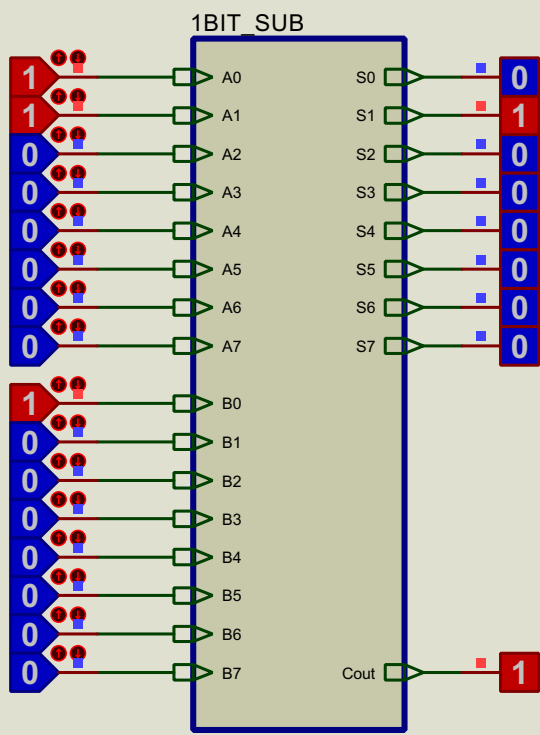


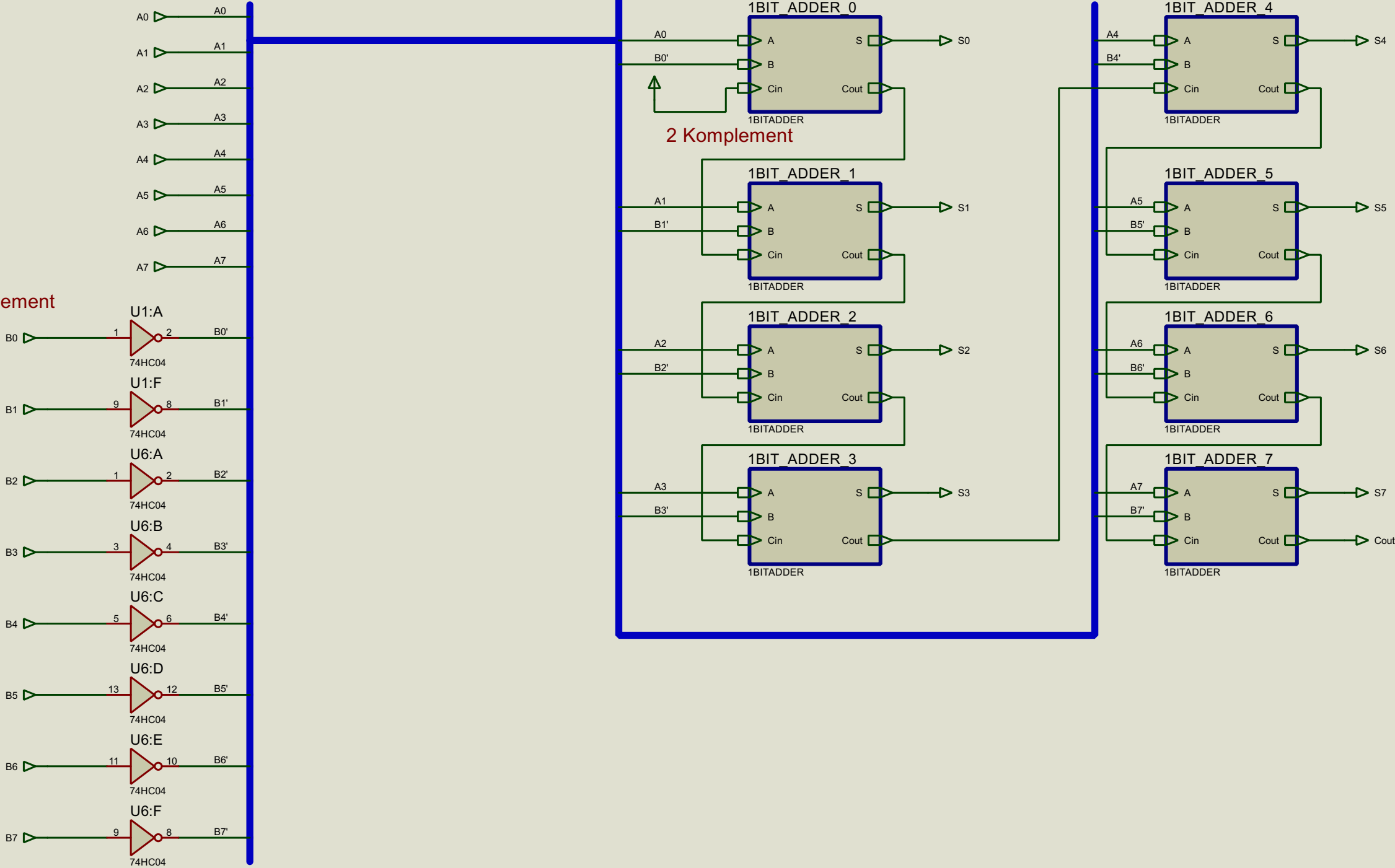
8Bit subtractor



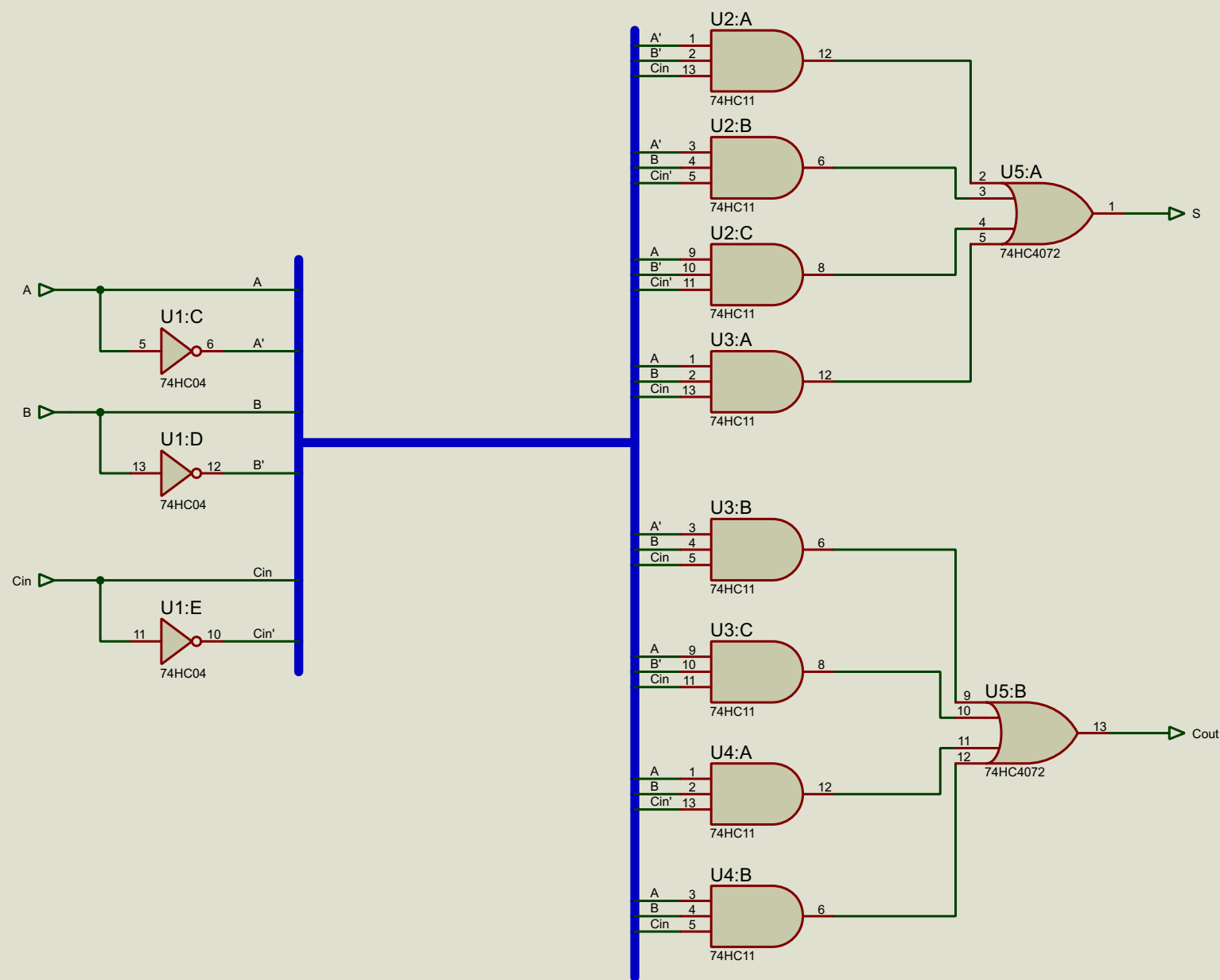
# 8bit Subtractor

1 Komplement

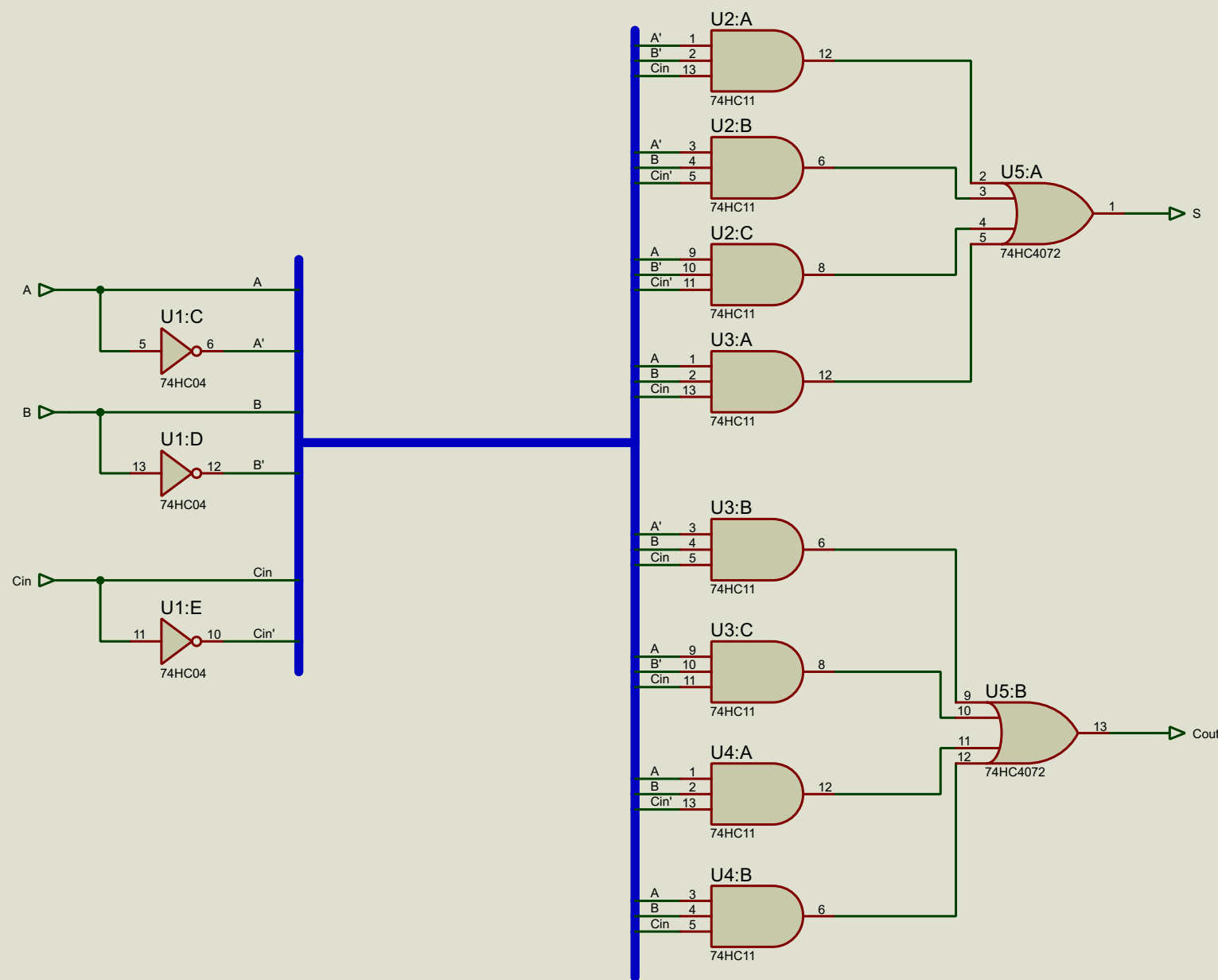
2 Komplement



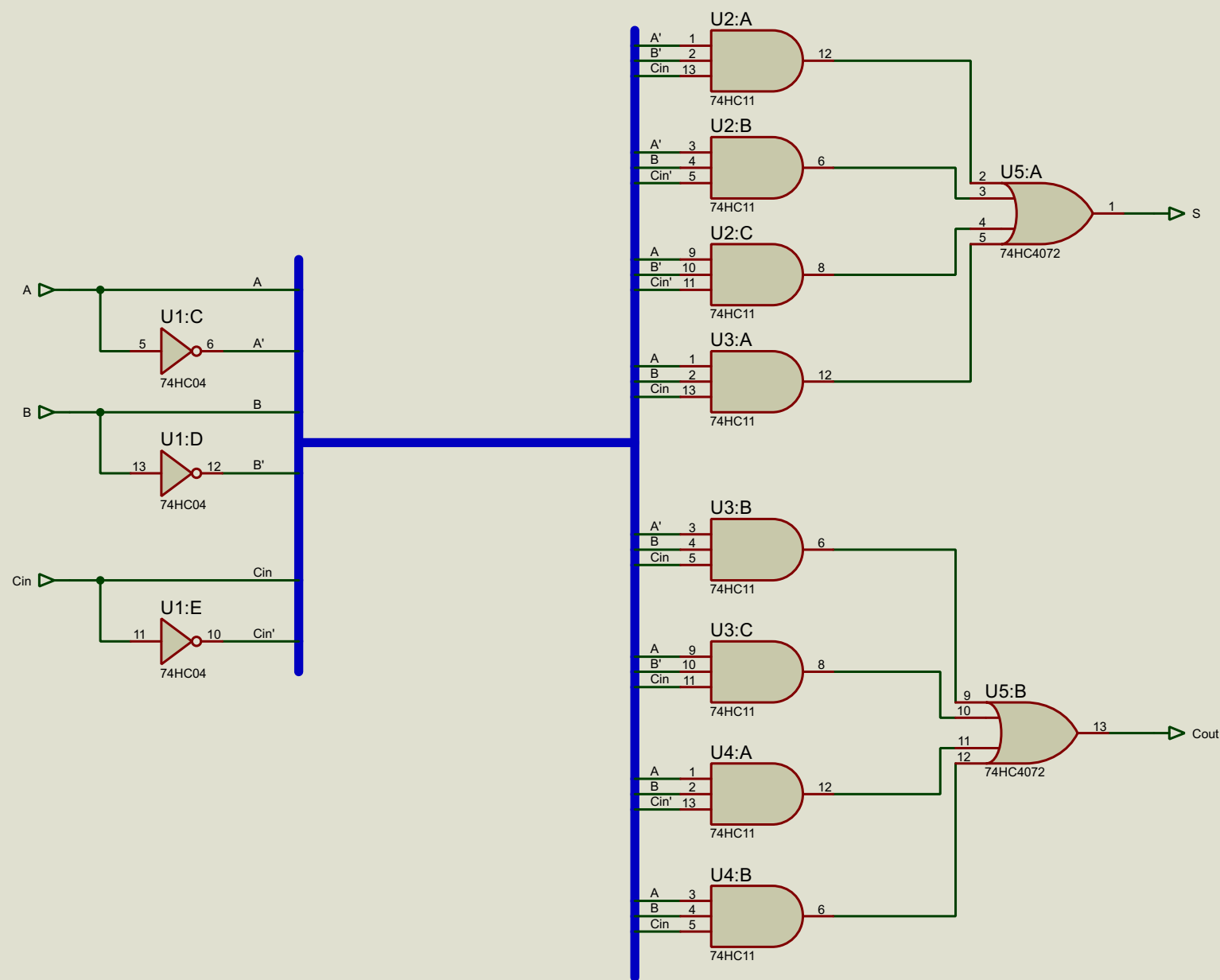
# 1Bit Full Adder



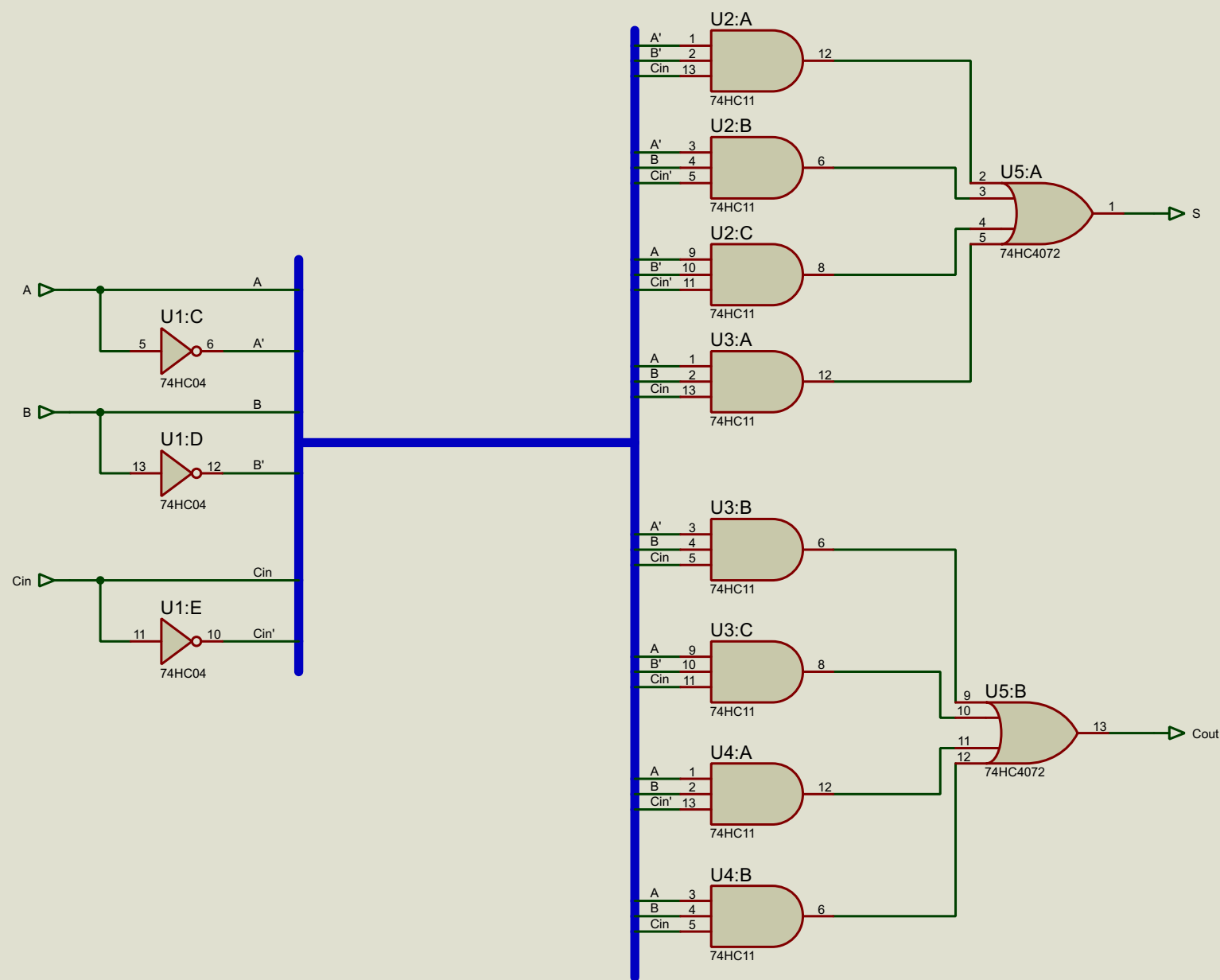
# 1Bit Full Adder



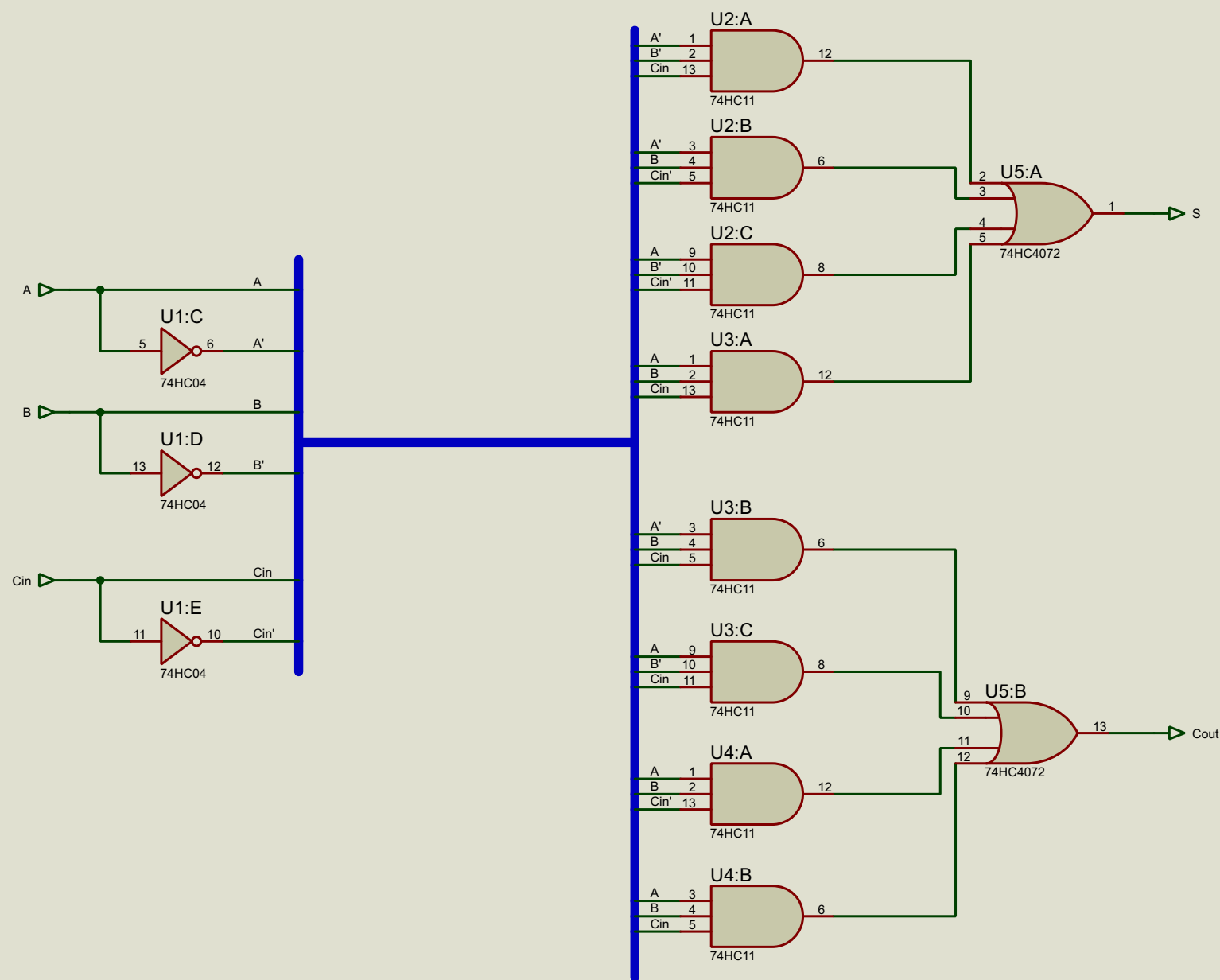
# 1Bit Full Adder



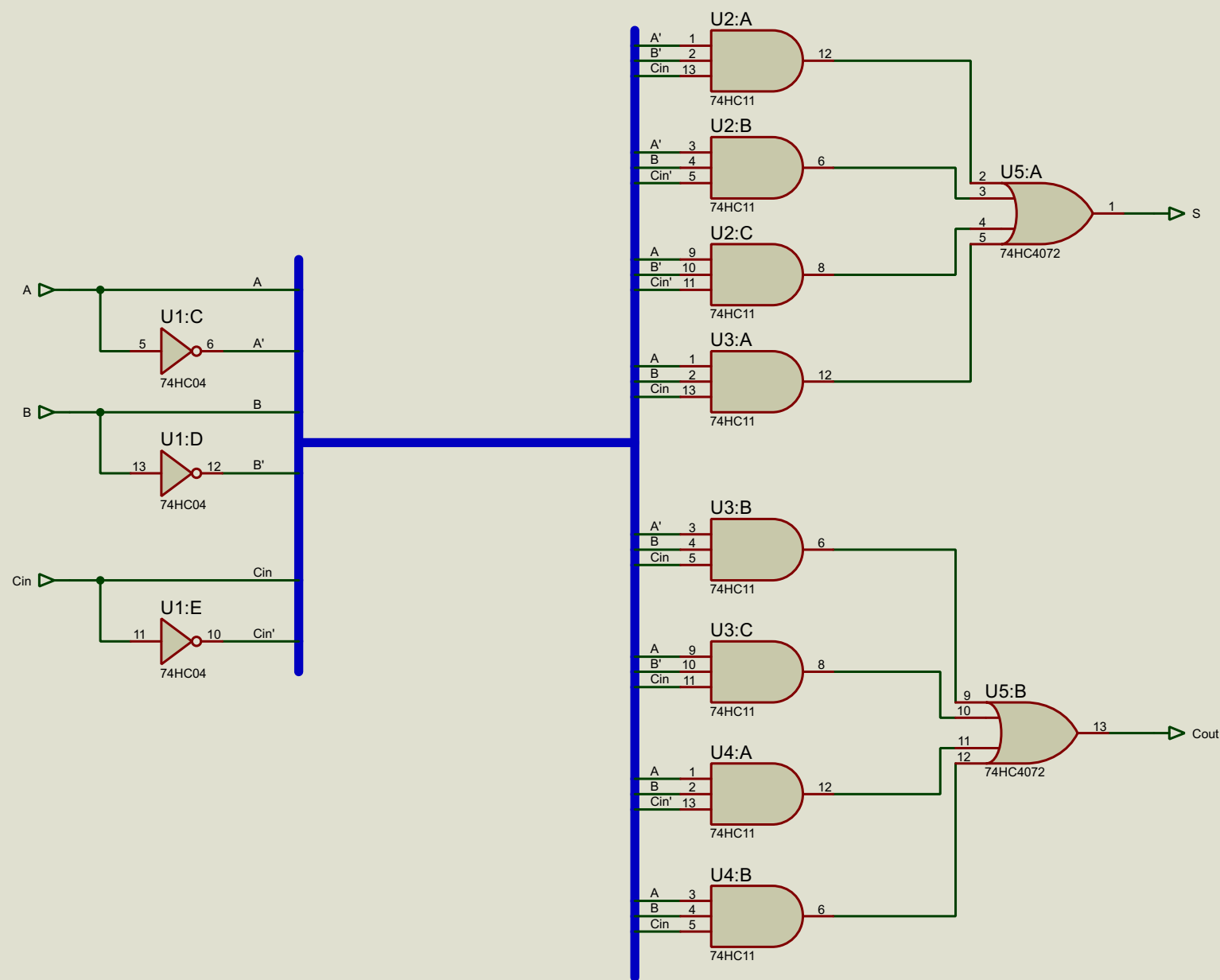
# 1Bit Full Adder



# 1Bit Full Adder

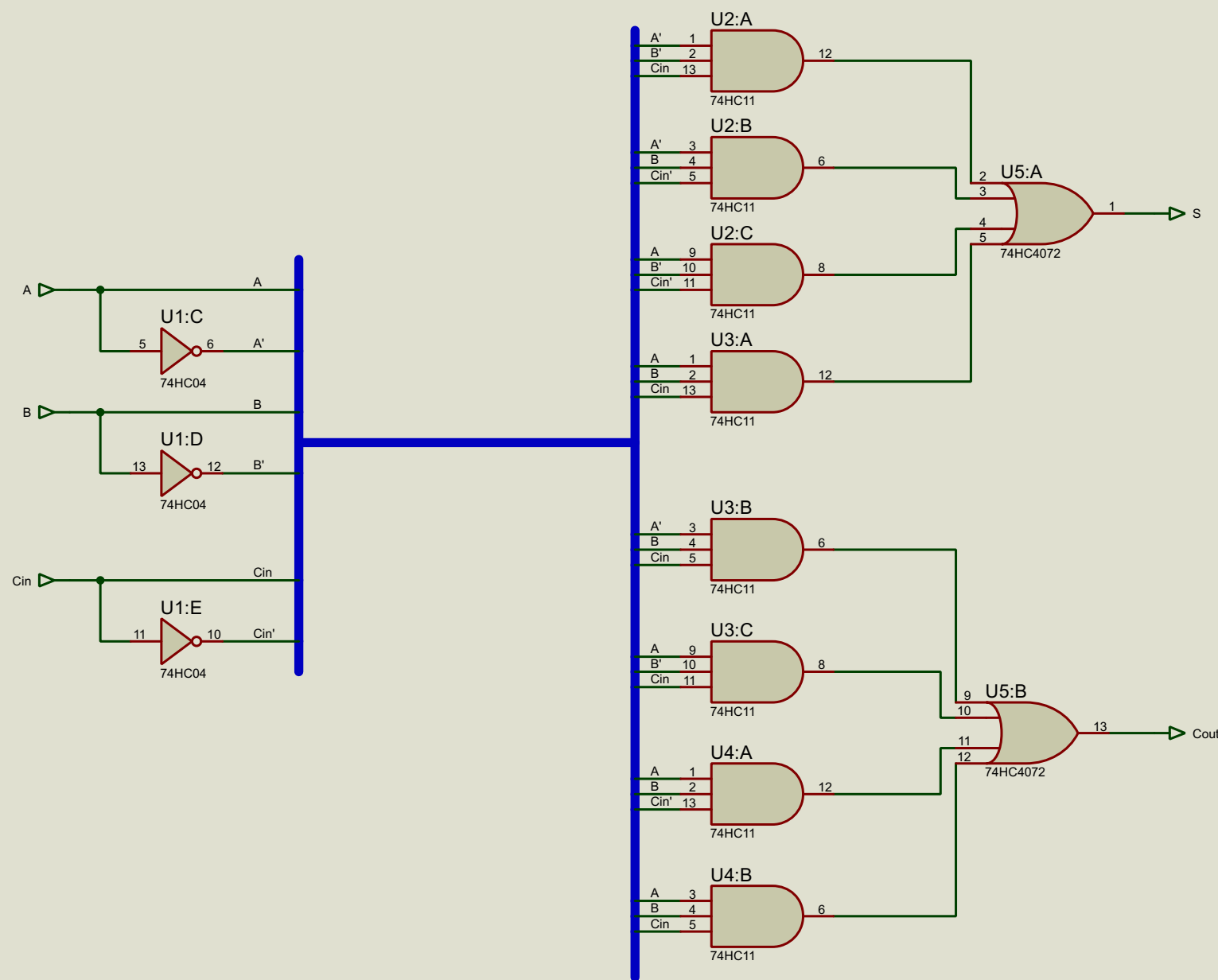


# 1Bit Full Adder





# 1Bit Full Adder



# 1Bit Full Adder

