

Course: Computer Organization – ENCM 369

Lab #: Lab 6

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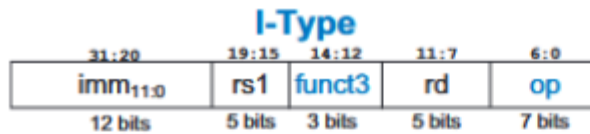
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Lab Section: B03

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Exercise A

1)



textbook p.334

$$\text{imm} = -160 \quad \text{imm}_{11:0} = 1111 \ 0110 \ 0000$$

$$\text{rs1} = 00010 \quad \text{rd} = 00010$$

$$\text{funct3} = 000 \quad \text{op} = 19 = 0010011$$

$$\text{Ans. } 111101100000_00010_000_00010_0010011$$

2)

$$\begin{array}{r}
 1 \\
 0111_ \boxed{12 \text{ ones}}_ 1110_1111_1010_0000 \quad \leftarrow \text{sp} \\
 + \quad 1111_ \boxed{12 \text{ ones}}_ 1111_1111_0110_0000 \quad \leftarrow \text{imm} \\
 \hline
 0111_ \boxed{12 \text{ ones}}_ 1110_1111_0000_0000 \quad \leftarrow \text{sp}
 \end{array}$$

Exercise B

$$\begin{array}{r} 1) \quad \begin{array}{r} 011 \\ 1011 - 0100 \\ + 1011 - 0011 \\ \hline 0100 011 \end{array} \end{array}$$

Signed overflow: MSB carry-out/in don't match.

Unsigned overflow: MSB carry-out = 1

$$\begin{array}{r} 2) \quad \begin{array}{r} 11 \\ 1101 - 0000 \\ + 1110 - 0000 \\ \hline 1011 - 0000 \end{array} \end{array}$$

Unsigned overflow: MSB carry-out = 1

$$\begin{array}{r} 3) \quad \begin{array}{r} 0111 \\ 0111 - 1000 \\ + 0000 - 1011 \\ \hline 1000 - 0011 \end{array} \end{array}$$

Signed overflow: MSB carry-out/in don't match.

$$\begin{array}{r} 4) \quad \begin{array}{r} 001111 \\ 0011 - 0101 \\ + 0010 - 1101 \\ \hline 0110 - 0010 \end{array} \end{array}$$

Exercise E

$$\begin{array}{r} 1) \quad \begin{array}{c} 011 \\ 0010-0000 \\ + \quad 0111-0001 \\ \hline 1001-0001 \end{array} \end{array}$$

Unsigned overflow: no MSB carry-out

Signed overflow: MSB carry-in/out don't match

$$\begin{array}{r} 2) \quad \begin{array}{c} 10 \\ 1100-1000 \\ + \quad 1001-0010 \\ \hline 0101-1010 \end{array} \end{array}$$

Signed overflow: MSB carry-in/out don't match

$$\begin{array}{r} 3) \quad \begin{array}{c} 1111 \\ 1010-1100 \\ + \quad 0101-1011 \\ \hline 0000 \quad 0111 \end{array} \end{array}$$

$$\begin{array}{r} 4) \quad \begin{array}{c} 00 \\ 0010-0110 \\ + \quad 1101-1001 \\ \hline 1111-1111 \end{array} \end{array}$$

Unsigned overflow: no MSB carry-out

Exercise G

```
.text
.globl int2str
int2str:
# Replace these two comment lines and the following instruction with
# code to match the definition of int2str in int2str.c.
    bne    a1, zero, L1          # if (src != 0) goto L1
    li     t6, '0'               # t6 = '0'
    sb     t6, (a0)              # dest[0] = t6
    sb     zero, 1(a0)           # dest[1] = '\0'
    jr     ra

L1:
    li     t6, -2147483648       # t6 = -2147483648
    bne    a1, t6, L2           # if (src != t6) goto L2
    lui    t0, 0x800000         # abs_src = 0x80000000
    j      L4

L2:
    bge    a1, zero, L3         # if (src >= 0) goto L3
    sub    t0, zero, a1         # abs_src = -src
    j      L4

L3:
    mv     t0, a1               # abs_src = src

L4:
    mv     t4, a0               # p = dest
    li     t1, 10               # ten = 10

L5:
    beq    t0, zero, L6         # if (abs_src == 0) goto L6
    remu   t2, t0, t1           # rem = abs_src % ten
    la     t6, digits           # t6 = &digits
    add    a3, t6, t2           # a3 = &digits[rem]
    lbu    a4, (a3)             # a4 = digits[rem]
    sb     a4, (t4)             # *p = a4
    addi   t4, t4, 1            # p++
    divu   t0, t0, t1           # abs_src = abs_src / ten
    j      L5

L6:
    bge    a1, zero, L7         # if (src >= 0) goto L7
    li     t6, '-'              # t6 = '-'
    sb     t6, (t4)             # *p = t6
    addi   t4, t4, 1            # p++

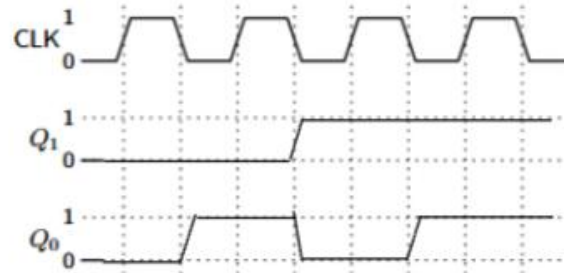
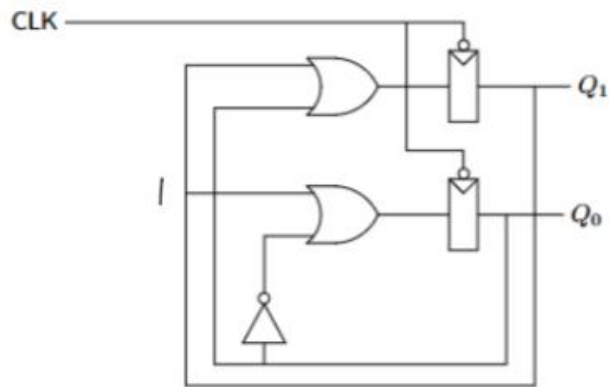
L7:
    sb     zero, (t4)           # *p = '\0'

    addi   t5, t4, -1           # q = p - 1
```

```
    mv      t4, a0                # p = dest
L8:    bge     t4, t5, L9           # if (p >= q) goto L9
    lbu     t3, (t4)              # temp = *p
    lbu     t6, (t5)              # t6 = *q
    sb      t6, (t4)              # *p = t6
    sb      t3, (t5)              # *q = temp
    addi    t4, t4, 1             # p++
    addi    t5, t5, -1            # q--
    j       L8
L9:    jr      ra
```

Exercise H

Part I



$$Q_1' = Q_1 + Q_0$$
$$Q_2' = Q_1 + \overline{Q_0}$$

Part II

$$\begin{aligned} Q_0' &= A \\ Q_1' &= Q_0 \\ Q_2' &= Q_1 \end{aligned}$$

