Iintel参考分析：

static int \_\_init xgold\_gpio\_init(void)

{

return platform\_driver\_register(&xgold\_gpio\_driver);

}

static struct platform\_driver xgold\_gpio\_driver = {

.probe = xgold\_gpio\_probe,

}

struct xgold\_pcl\_gpio {

struct gpio\_chip pchip;

void \_\_iomem \*gpio\_base;

phys\_addr\_t gpio\_base\_phys;

u32 dir\_base;

struct xgold\_pcl\_field dir\_field;

u32 dir\_out;

u32 out\_base;

struct xgold\_pcl\_field out\_field;

u32 in\_base;

struct xgold\_pcl\_field in\_field;

u32 nirqs;

struct xgold\_gpio\_irq gpio\_irq[MAX\_GPIO\_IRQS];

bool io\_master;

}

static int xgold\_gpio\_probe(struct platform\_device \*pdev)

{

xgold\_gpio = devm\_kzalloc(&pdev->dev, sizeof(\*xgold\_gpio), GFP\_KERNEL);

pchip = &xgold\_gpio->pchip;

//设置参数

pchip->ngpio = ngpios;

pchip->label = np->name;

pchip->owner = THIS\_MODULE;

pchip->request = xgold\_gpio\_request;

pchip->free = xgold\_gpio\_free;

pchip->direction\_input = xgold\_gpio\_direction\_input;

pchip->get = xgold\_gpio\_get;

}

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static int smp2p\_init(void)

{

return platform\_driver\_register(&smp2p\_gpio\_driver);

}