Kevin Molumphy & Stephen Tambussi - COEN122											
Instruction:	Opcode:	regWrite:	memToReg:	PCtoReg:	branchN:	branchZ:	jump:	memRead:	memWrite:	aluOp:	aluSrc:
NOP	0000:	0	C		0	0	0	0	C	000:	
Save PC	1111:	1	C	1	1 0	0	0	0	C	100:	•
Load	1110:	1	1	(0	0	0	1	C	111:	
Store	0011:	0	C) (0	0	0	0	1	111:	
Add	0100:	1	C)	0	0	0	0	C	100:	
Addl	0101:	1	C		0	0	0	0	C	100:	•
Negate	0110:	1	C	(0	0	0	0	C	010:	
Subtract	0111:	1	C) (0	0	0	0	C	001:	
Jump	1000:	0	C		0	0	1	0	C	111:	
Branch if zero	1001:	0	C)	0	1	0	0	C	111:	
Where an I	1010:	1	C	1	1 0	0	0	0	C	111:	
Branch if negative	1011:	0	C) (1	0	0	0	C	111:	(
MAX	0001:										