Advanced Cybersecurity Topics

Reversing

20-21

Reverse Engineering

 "is the process by which a man-made object is deconstructed to reveal its designs, architecture, or to extract knowledge from the object similar to scientific research."

Reverse Engineering - Software

- Probe the software to gain knowledge
 - Statically
 - Binary is not running
 - Dynamically
 - Running the binary

Static and Dynamic Analysis

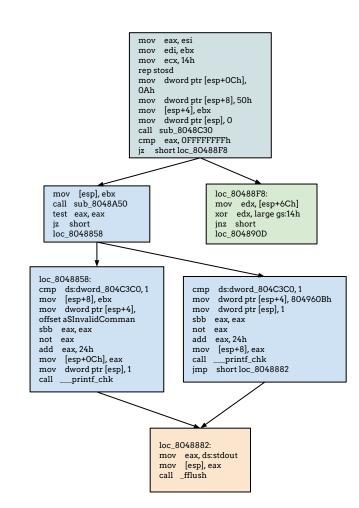
The reverse engineering process is a **sequence** of **static** and **dynamic** analysis that slowly **refine** your knowledge about the malware sample.



Static Analysis

Understand the functionalities of a binary looking at its code.

- Disassemble instructions
- Recover CFG
- Recover Functions
- Recover Types
- Decompile
- Fingerprints



Reverse Engineering - Static

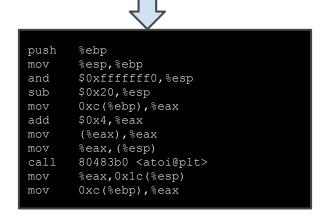
```
Developer
         e <stdlib.h>
      int foo(int a, int b) {
      int c = 14;
      c = (a + b) * c;
      return c;
      int main(int argc, char * argv[]) {
      int avar;
      int bvar;
      int cvar;
      char * str;
      avar = atoi(argv[1]);
      cvar = foo(avar, bvar);
      gets(str);
      printf("foo(%d, %d) = %d\n", avar, bvar, cvar);
      return 0;
Compiler
         .cfi startproc
         pushl %ebp
         .cfi def cfa offset 8
         .cfi offset 5, -8
         movl %esp, %ebp
         .cfi def cfa register 5
         andl $-16, %esp
         subl $32, %esp
             12(%ebp), %eax
             $4, %eax
         addl
Assembler
        Machine
```

```
#include <stdint.h>
#include <stdio.h>
#include <stdlib.h>
int32 t foo(int32 t a, int32 t b);
// From module: layout.c
// Address range: 0x80484ac - 0x80484cd
// Line range: 5 - 10
   int32 t c = 14 * (b + a); // 0x80484c4
    return c;
// From module: layout.c
// Address range: 0x80484cf - 0x8048559
// Line range: 13 - 30
int main(int argc, char **argv) {
    int32 t apple = (int32 t)argv; // 0x80484d8
    int32 t str as i = atoi((int8 t *)*(int32 t *)(apple + 4));
    int32 t str as i2 = atoi((int8 t *)*(int32 t *)(apple + 8));
   int32 t banana = foo(str as i, str as i2); // 0x804850f
    gets (NULL);
    printf("foo(%d, %d) = %d\n", str as i, str as i2, banana);
    return 0;
                                            Decompiler
      %ebp
      %esp,%ebp
      $0xffffffff0,%esp
      $0x20,%esp
      0xc(%ebp), %eax
      $0x4,%eax
      (%eax),%eax
      %eax, (%esp)
      80483b0 <atoi@plt>
                                              Disassembler
      %eax,0x1c(%esp)
      0xc(%ebp),%eax
```

Static Analysis - Disassembler

Linear Sweep

Recursive



Static Analysis - Disassembler Tools

objdump - Disasm

radare2 - Disasm

capstone - Programmable Disasm

Binary Ninja - Disasm + Primitive Decompiler

GHIDRA - Disasm + Decompiler (https://ghidra-sre.org/)

IDA Pro - Disasm + Decompiler (de facto standard)

Static Analysis - Decompile

You can go back to pseudo code:

```
%ebp
push
       %esp, %ebp
mov
       $0xfffffff0, %esp
and
       $0x20,%esp
sub
       0xc(%ebp), %eax
mov
       $0x4,%eax
add
       (%eax),%eax
mov
       %eax, (%esp)
mov
call
       80483b0 <atoi@plt>
       %eax, 0x1c(%esp)
mov
       0xc(%ebp),%eax
```

```
#include <stdint.h>
#include <stdio.h>
#include <stdlib.h>
int32 t foo(int32 t a, int32 t b);
// From module: layout.c
// Address range: 0x80484ac - 0x80484cd
// Line range: 5 - 10
int32 t foo(int32 t a, int32 t b) {
   int32 t c = 14 * (b + a); // 0x80484c4
   return c;
// From module:
                 layout.c
// Address range: 0x80484cf - 0x8048559
// Line range:
                 13 - 30
int main(int argc, char **argv) {
    int32 t apple = (int32 t)argv; // 0x80484d8
   int32 t str as i = atoi((int8 t *)*(int32 t *)(apple + 4));
    int32 t str as i2 = atoi((int8 t *)*(int32 t *)(apple + 8));
    int32 t banana = foo(str as i, str as i2); // 0x804850f
   gets (NULL);
   puts(NULL);
   printf("foo(%d, %d) = %d\n", str as i, str as i2, banana);
   return 0;
```

Static Analysis - Decompile

Binary Ninja - Disasm + Primitive Decompiler

GHIDRA - Disasm + Decompiler (https://ghidra-sre.org/)

IDA Pro - Disasm + Decompiler (de facto standard)

rev.ng - not available yet

Static Analysis - Struct

Building correct types make decompilation more readable.

```
void fastcall sub 1413( int64 a1)
 signed int i; // [rsp+10h] [rbp-10h]
 signed int v2; // [rsp+14h] [rbp-Ch]
 void *v3; // [rsp+18h] [rbp-8h]
  for ( i = 0; i \le 15; ++i )
   if ( !*( DWORD *)(24LL * i + a1) )
     printf("Size: ");
     v2 = sub 1AD5();
     if (v2 > 0 && v2 <= 88)
       v3 = calloc(v2, 1uLL);
       if (!v3)
         exit(-1);
       *( DWORD *) (24LL * i + a1) = 1;
        *( QWORD *) (a1 + 24LL * i + 8) = v2;
        *( QWORD *) (a1 + 24LL * i + 16) = v3;
       printf("Chunk %d Allocated\n", (unsigned int)i);
     else
        puts("Invalid Size");
```

```
void fastcall allocate(nota *notes)
 signed int i; // [rsp+10h] [rbp-10h]
 signed int sz; // [rsp+14h] [rbp-Ch]
 void *chunk; // [rsp+18h] [rbp-8h]
 for ( i = 0; i \le 15; ++i )
   if (!notes[i].state)
     printf("Size: ");
     sz = get long();
     if (sz > 0 \&\& sz \le 0x58)
       chunk = calloc(sz. 1uLL);
       if (!chunk)
       notes[i].state = 1;
       notes[i].size = sz;
       notes[i].data = ( int64)chunk;
       printf("Chunk %d Allocated\n", (unsigned int)i);
     else
       puts("Invalid Size");
```

```
signed int i; // [rsp+10h] [rbp-10h]
                                                      signed int i; // [rsp+10h] [rbp-10h]
  signed int v2; // [rsp+14h] [rbp-Ch]
                                                      signed int sz; // [rsp+14h] [rbp-Ch]
 void *v3; // [rsp+18h] [rbp-8h]
                                                      void *chunk; // [rsp+18h] [rbp-8h]
  for ( i = 0; i \le 15; ++i )
                                                      for (i = 0; i \le 15; ++i)
   if (!*( DWORD *)(24LL * i + a1) )
                                                        if (!notes[i].state)
     printf("Size: ");
                                                          printf("Size: ");
     v2 = sub 1AD5();
                                                          sz = qet lonq();
      if (v2 > 0 \&\& v2 \le 88)
                                                          if (sz > 0 \&\& sz \le 0x58)
       v3 = calloc(v2, 1uLL);
                                                            chunk = calloc(sz, lull);
        if (!v3)
                                                            if (!chunk)
        * ( DWORD *) (24LL * i + a1) = 1;
                                                            notes[i].state = 1;
        * ( QWORD ^*) (a1 + 24LL * i + 8) = v2;
                                                            notes[i].size = sz;
        *( QWORD *) (a1 + 24LL * i + 16) = v3;
                                                            notes[i].data = ( int64)chunk;
        printf("Chunk %d Allocated\n". (unsigned
                                                            printf("Chunk %d Allocated\n".
int)i);
                                                    (unsigned int)i);
      else
                                                          else
        puts("Invalid Size");
                                                            puts("Invalid Size");
      return;
                                                          return;
```

Static Analysis - Struct IDA (local type)

Building correct types make decompilation more readable.

```
struct __attribute__((aligned(8))) nota
{
  int state;
  int unk1;
   __int64 size;
   __int64 data;
};
```

Static Analysis - Other Tools (Lifter)

You can Lift binary to perform program analysis tasks.

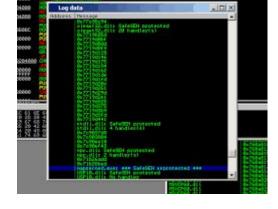
Angr - Binary Analysis (VEX IR) + Symbolic Execution (http://angr.io/)

rev.ng - Binary analysis with LLVM IR + Binary translationtion

BAP - Binary analysis with BIL IR

Dynamic Analysis - Debuggers

- Run the executable and see/modify process state live.
- Step by step Debugging
- BreakPoints
 - Hardware breakpoints
- Watchpoints
- syscall catch
- signal catch
- Scriptable



Tools: windbg, ollydbg, Immunity dbg, IDA pro, GDB, etc.

Dynamic Analysis - GDB

disassemble	set disassembly-fl disass *address	avor intet	#sets syntax #disassemble
execution	step (s) next(n) finish (f) continue (c)	#exec nextline - enter fun #exec nectline - jump call #exec til ret #continue execution	
examine	x/numF *address printf "%c", \$reg	(useful Fs are bx, wx	m data of type F r from register

Dynamic Analysis - GDB

breakpoints

b *address hb *address b *address if \$reg==val del br_num #set software breakp at addr #set hardware breakp at addr #set conditional breakp #remove breakpoint br_num

watchpoints

w *address rw *address

#set watch for write at addr #set watch for read at addr

Dynamic Analysis - GDB

Setting registers to a certain value:

Change return value of a function, to jump into some memory are, etc.

You can also call a function with:

call address

Dynamic Analysis - GDB Automate

Create command that are runned **after** a break point

commands br_num

command_list

end

Reversing in Adversarial Contest - Static

- Complex CFG
 - not aligned jmp
 - dead code
- Packing
- Header Corruption

Reversing in Adversarial Contest - Dynamic

- Debugging only Once
 - ptrace
- Check for Debugger
 - 0xcc
- Divert Execution
 - Signals / multithread