

# Diagram Report

Version •



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Author:

Thomas Batt

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## 1.1.1 Software\_Architecture\_View diagram

Class diagram in package 'Software\_Architecture'

Software\_Architecture\_View

Version 1.0

Thomas Batt created on 13.01.2022. Last modified 28.01.2022

Name: Software\_Architecture\_View

Package: Software\_Architecture

Version: 1.0

Author: Thomas Batt

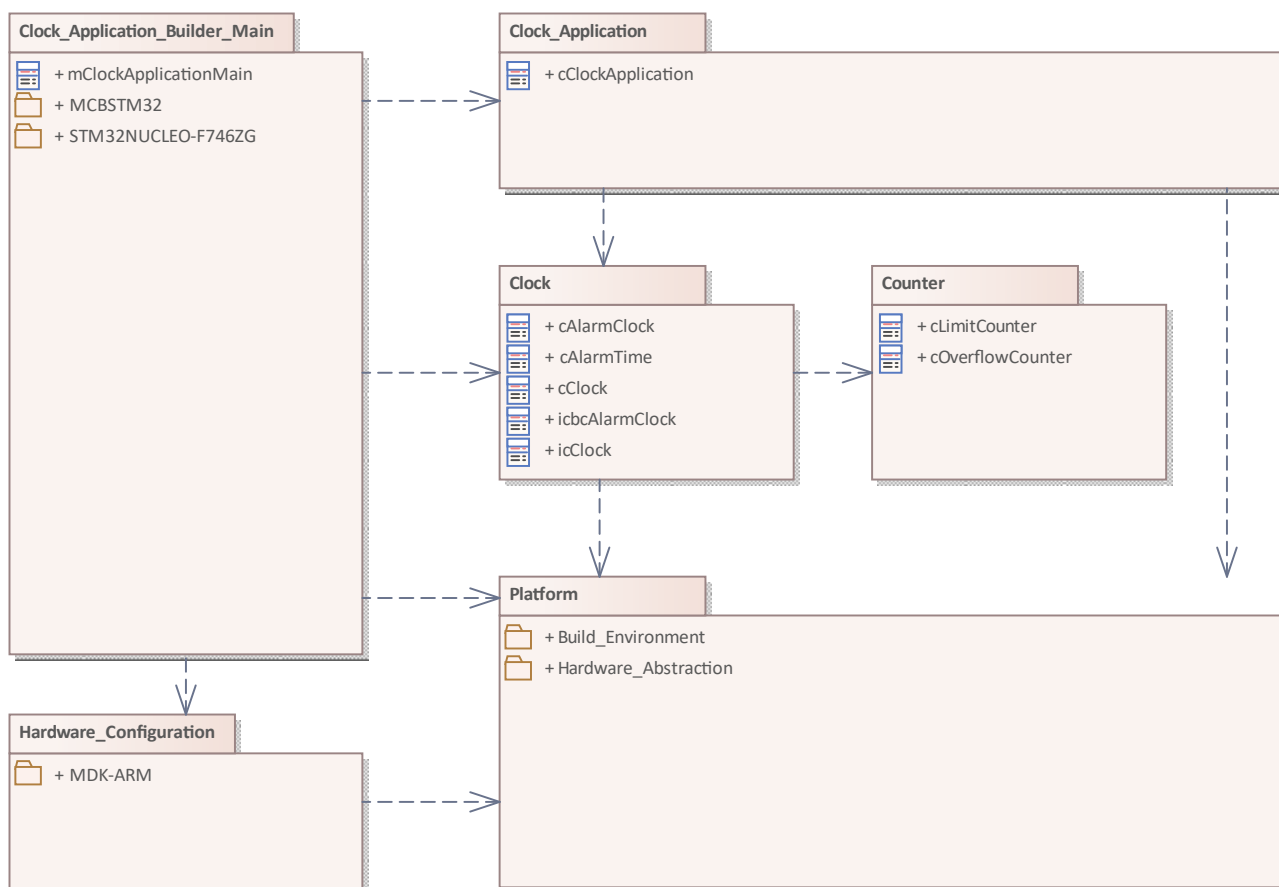


Figure 1: Software\_Architecture\_View

### 1.1.1.1 Clock\_Application\_Builder\_Main diagram

Class diagram in package 'Clock\_Application\_Builder\_Main'

Clock\_Application\_Builder\_Main  
Version 1.0

Thomas Batt created on 13.01.2022. Last modified 28.01.2022

Name: Clock\_Application\_Builder\_Main  
Package: Clock\_Application\_Builder\_Main  
Version: 1.0  
Author: Thomas Batt

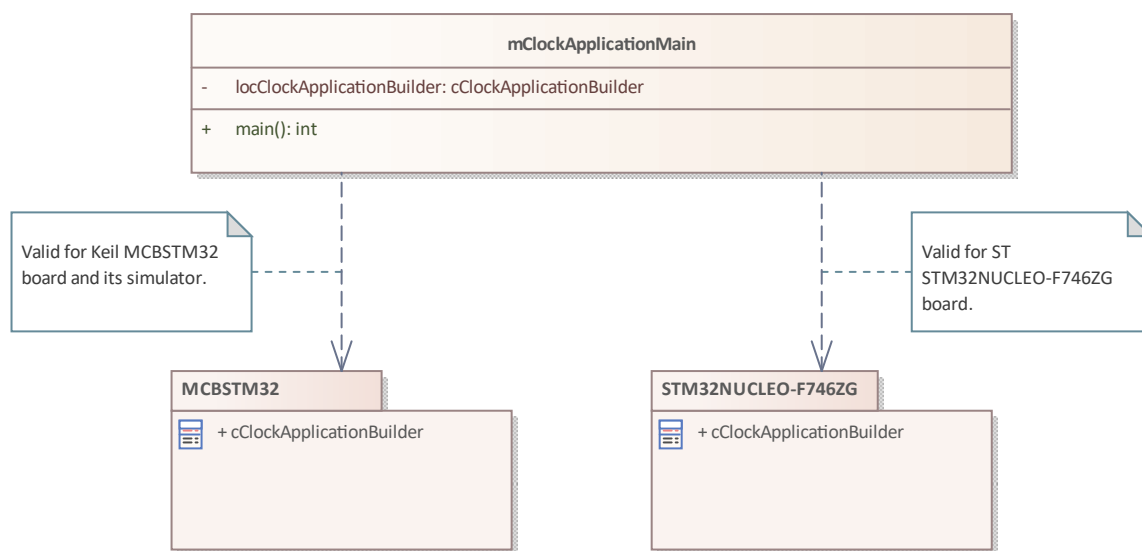


Figure 2: Clock\_Application\_Builder\_Main

### 1.1.1.1.1 MCBSTM32\_Clock\_Application\_Builder diagram

Class diagram in package 'MCBSTM32'

MCBSTM32\_Clock\_Application\_Builder

Version 1.0

Thomas Batt created on 13.01.2022. Last modified 26.09.2022

Name: MCBSTM32\_Clock\_Application\_Builder

Package: MCBSTM32

Version: 1.0

Author: Thomas Batt

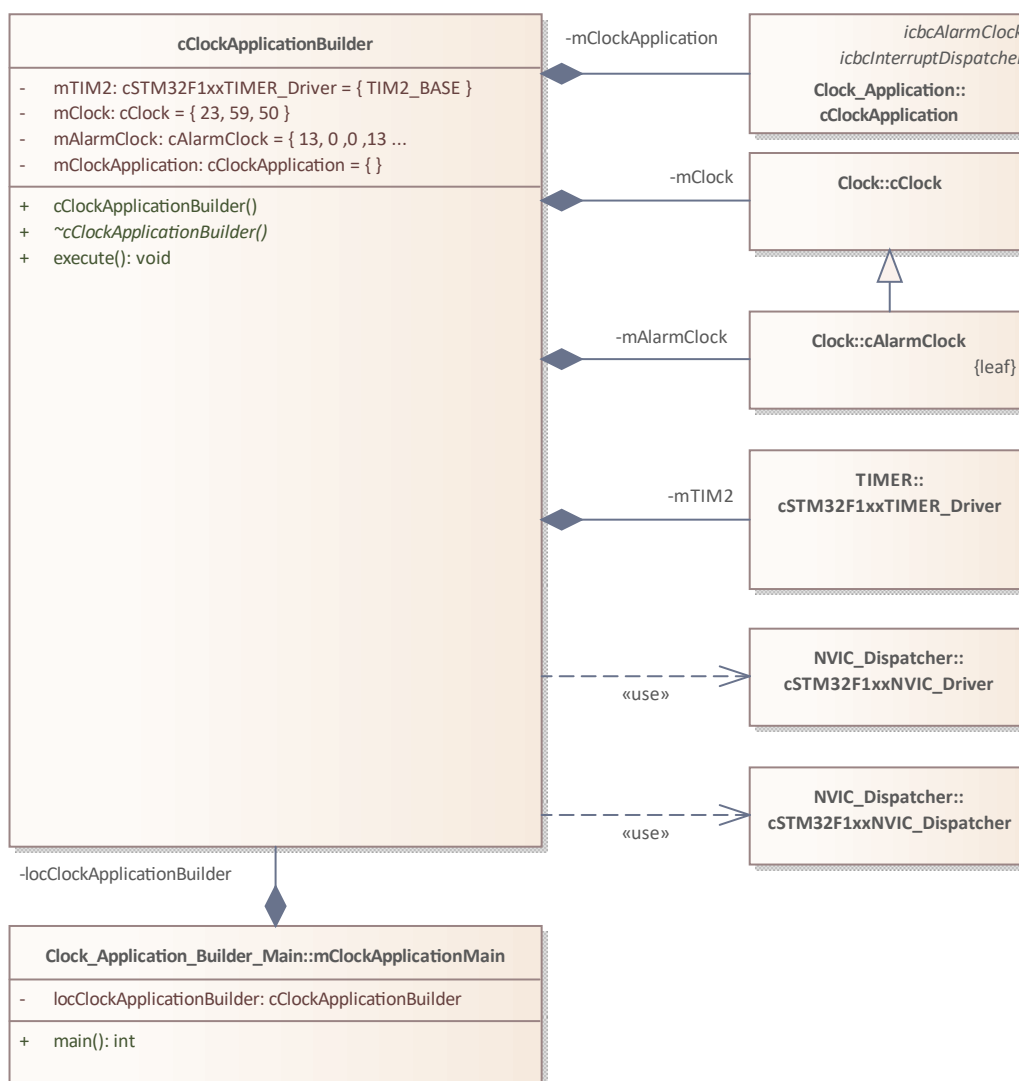


Figure 3: MCBSTM32\_Clock\_Application\_Builder

### 1.1.1.1.2 STM32NUCLEO-F746ZG\_Clock\_Application\_Builder diagram

Class diagram in package 'STM32NUCLEO-F746ZG'

STM32NUCLEO-F746ZG\_Clock\_Application\_Builder

Version 1.0

Thomas Batt created on 13.01.2022. Last modified 26.09.2022

Name: STM32NUCLEO-F746ZG\_Clock\_Application\_Builder

Package: STM32NUCLEO-F746ZG

Version: 1.0

Author: Thomas Batt

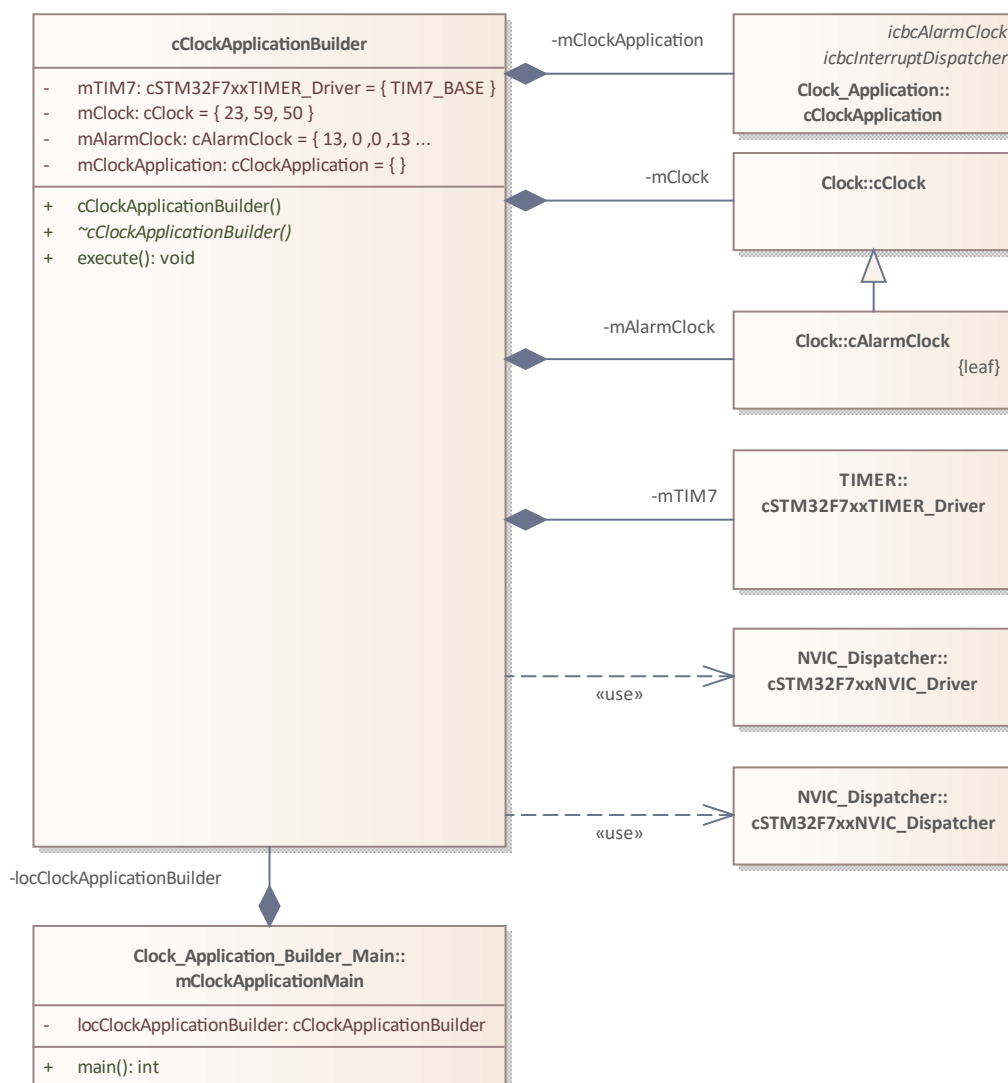


Figure 4: STM32NUCLEO-F746ZG\_Clock\_Application\_Builder

### 1.1.1.2 Clock\_Application diagram

Class diagram in package 'Clock\_Application'

Clock\_Application  
Version 1.0

Thomas Batt created on 13.01.2022. Last modified 28.01.2022

Name: Clock\_Application  
Package: Clock\_Application  
Version: 1.0  
Author: Thomas Batt

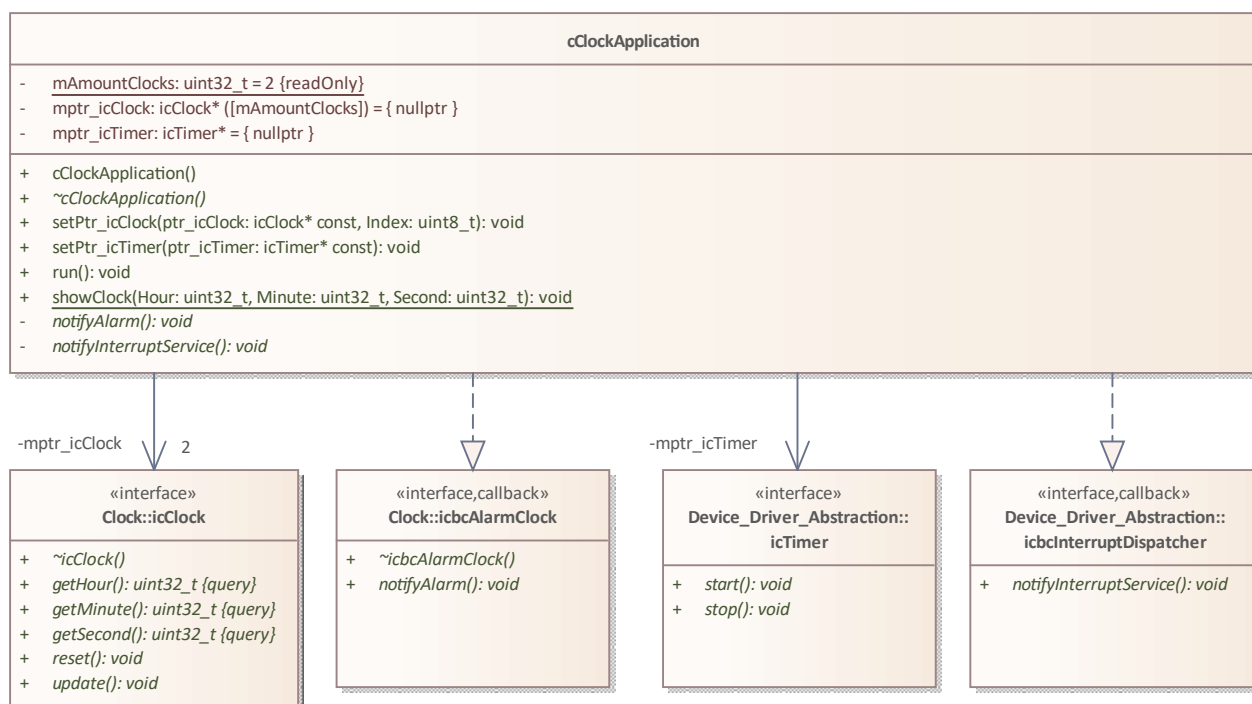


Figure 5: Clock\_Application

### 1.1.1.3 Clock diagram

Class diagram in package 'Clock'

Clock  
Version 1.0

Thomas Batt created on 13.01.2022. Last modified 26.09.2022

Name: Clock  
Package: Clock  
Version: 1.0  
Author: Thomas Batt

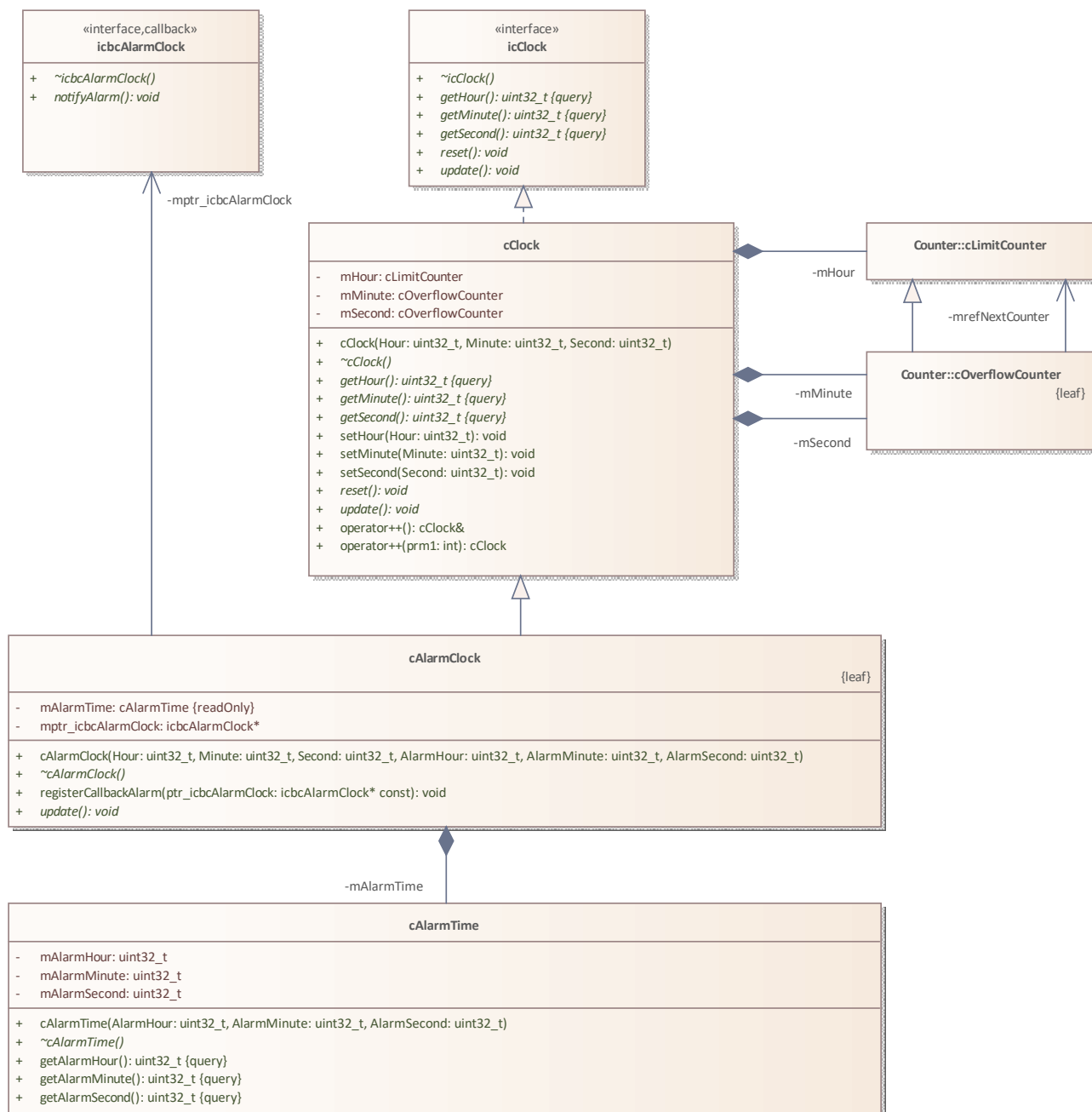


Figure 6: Clock



### 1.1.1.4 Clock\_Object\_View diagram

Object diagram in package 'Clock'

Clock\_Object\_View  
Version 1.0

Thomas Batt created on 13.01.2022. Last modified 27.01.2022

Name: Clock\_Object\_View  
Package: Clock  
Version: 1.0  
Author: Thomas Batt

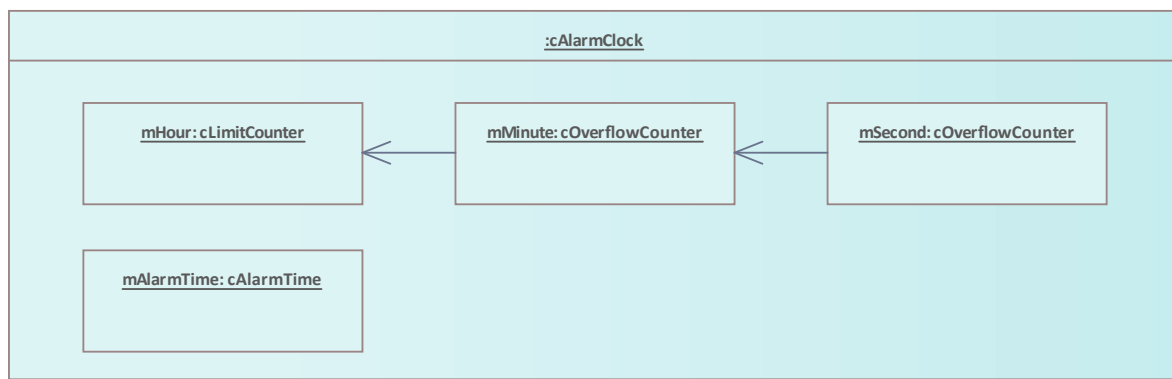
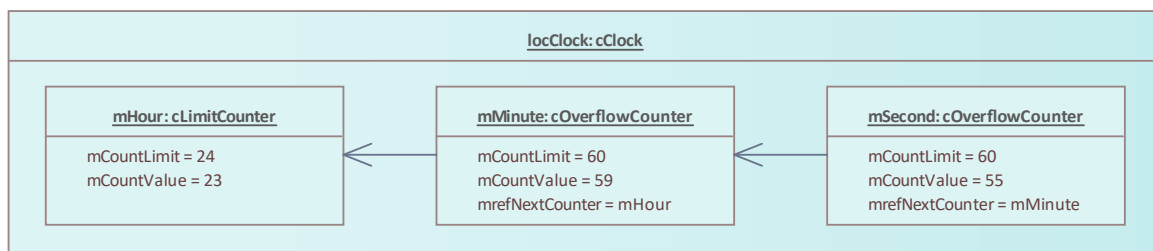


Figure 7: Clock\_Object\_View

### 1.1.1.5 Counter diagram

Class diagram in package 'Counter'

Counter  
Version 1.0

Thomas Batt created on 13.01.2022. Last modified 28.01.2022

Name: Counter  
Package: Counter  
Version: 1.0  
Author: Thomas Batt

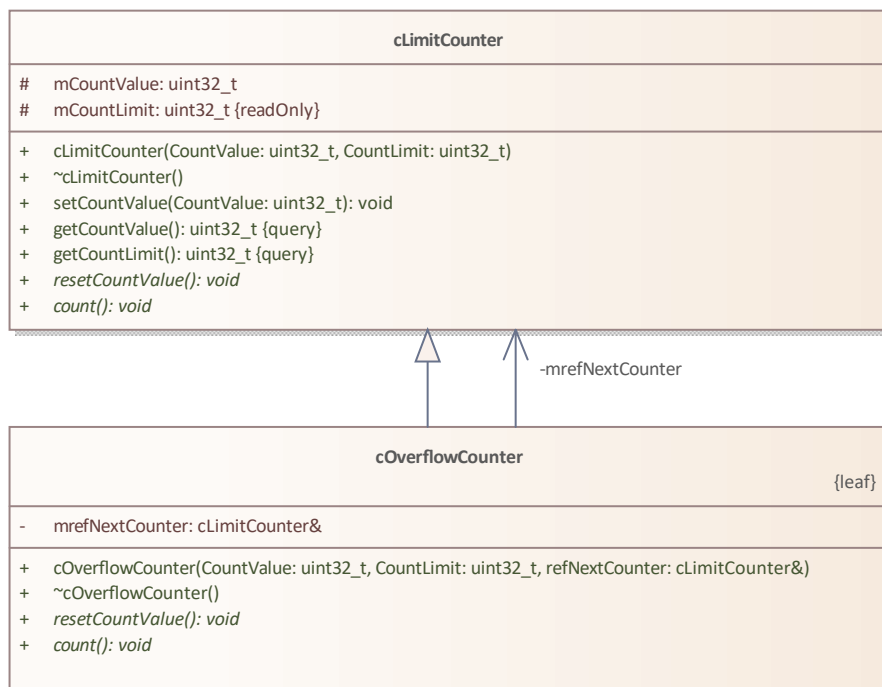


Figure 8: Counter

### 1.1.1.6 Platform diagram

*Class diagram in package 'Platform'*

Platform  
Version 1.0

Thomas Batt created on 13.01.2022. Last modified 28.01.2022

Name: Platform  
Package: Platform  
Version: 1.0  
Author: Thomas Batt



Figure 9: Platform

### 1.1.1.6.1 Build\_Environment diagram

*Class diagram in package 'Build\_Environment'*

Build\_Environment

Version 1.0

Thomas Batt created on 13.01.2022. Last modified 28.01.2022

Name: Build\_Environment  
Package: Build\_Environment  
Version: 1.0  
Author: Thomas Batt



Figure 10: Build\_Environment

### 1.1.1.6.2 Hardware\_Abstraction diagram

Class diagram in package 'Hardware\_Abstraction'

Hardware\_Abstraction

Version 1.0

Thomas Batt created on 13.01.2022. Last modified 28.01.2022

Name: Hardware\_Abstraction

Package: Hardware\_Abstraction

Version: 1.0

Author: Thomas Batt

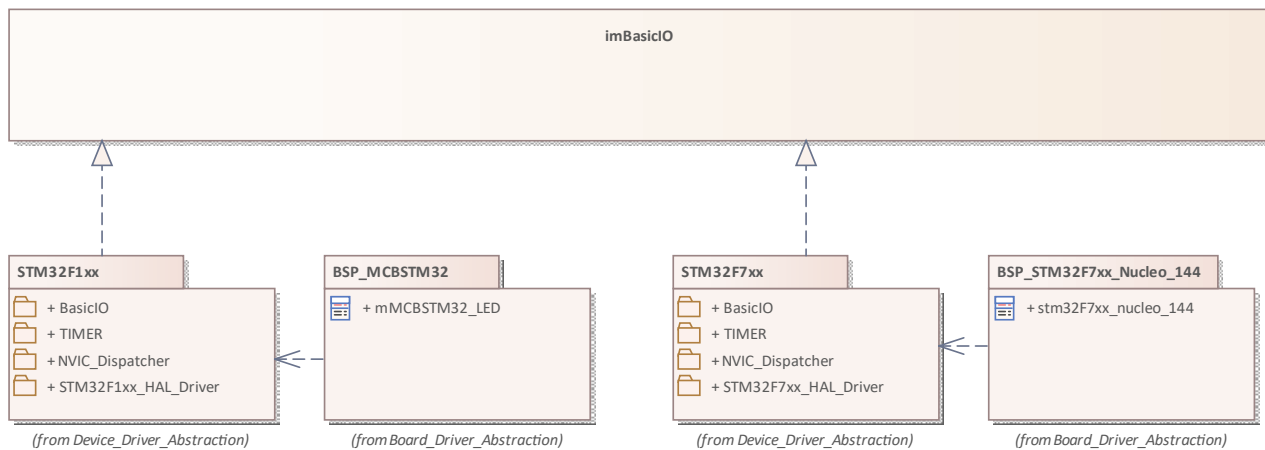


Figure 11: Hardware\_Abstraction

### 1.1.1.6.2.1 Device\_Driver\_Abstraction diagram

Class diagram in package 'Device\_Driver\_Abstraction'

Device\_Driver\_Abstraction

Version 1.0

Thomas Batt created on 13.01.2022. Last modified 28.01.2022

Name: Device\_Driver\_Abstraction  
Package: Device\_Driver\_Abstraction  
Version: 1.0  
Author: Thomas Batt

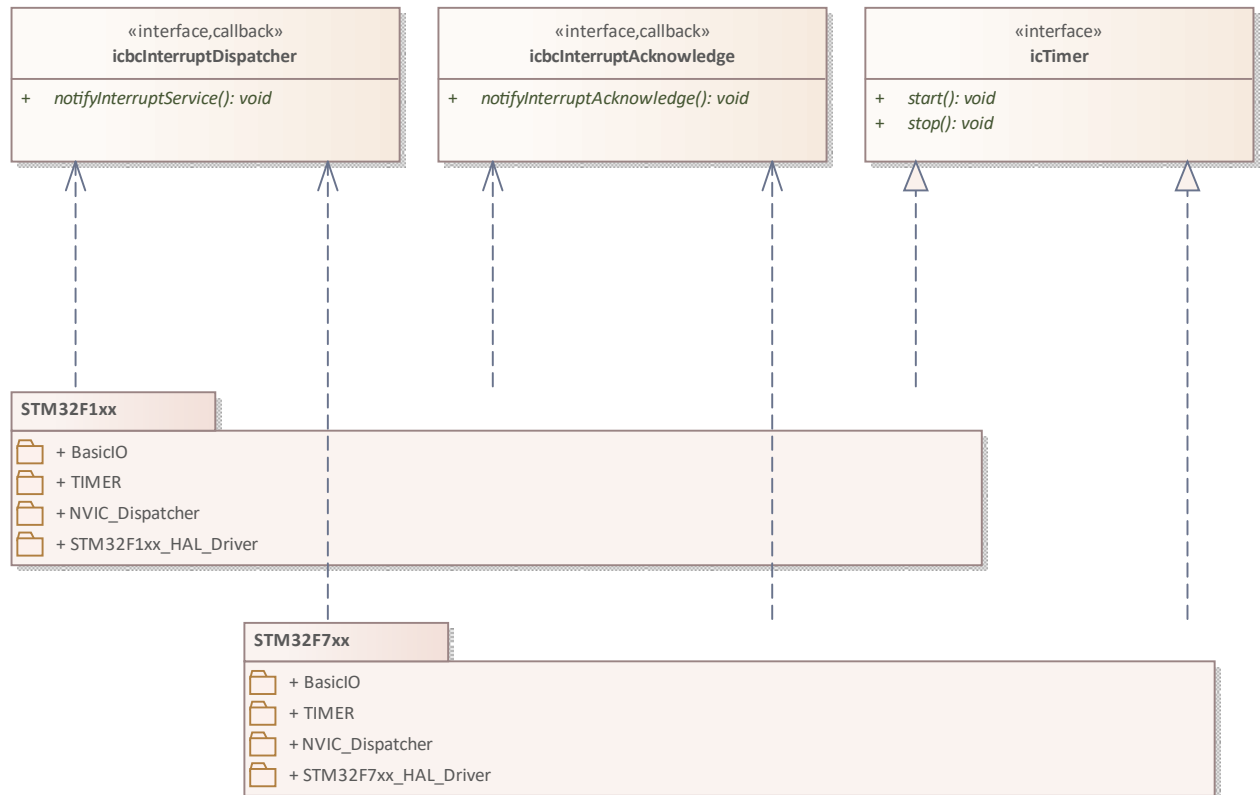


Figure 12: Device\_Driver\_Abstraction

### 1.1.1.6.2.1.1.1 STM32F1xx\_BasicIO diagram

*Class diagram in package 'BasicIO'*

STM32F1xx\_BasicIO

Version 1.0

Thomas Batt created on 13.01.2022. Last modified 28.01.2022

Name: STM32F1xx\_BasicIO

Package: BasicIO

Version: 1.0

Author: Thomas Batt

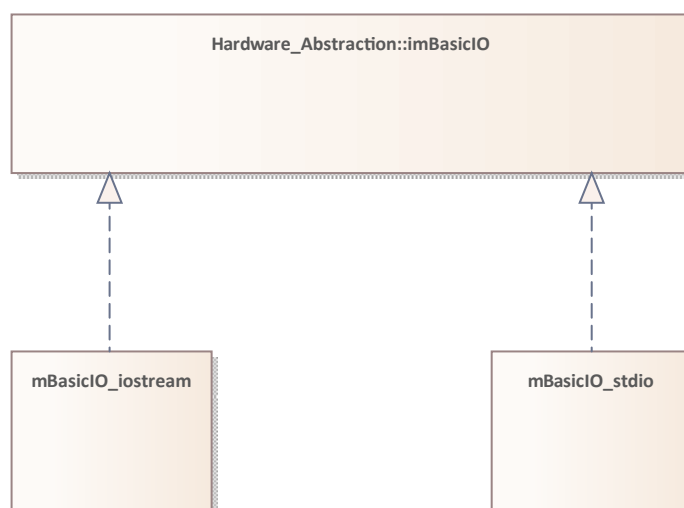


Figure 13: STM32F1xx\_BasicIO

### 1.1.1.6.2.1.1.2 STM32F1xx\_TIMER diagram

Class diagram in package 'TIMER'

STM32F1xx\_TIMER

Version 1.0

Thomas Batt created on 13.01.2022. Last modified 28.01.2022

Name: STM32F1xx\_TIMER

Package: TIMER

Version: 1.0

Author: Thomas Batt

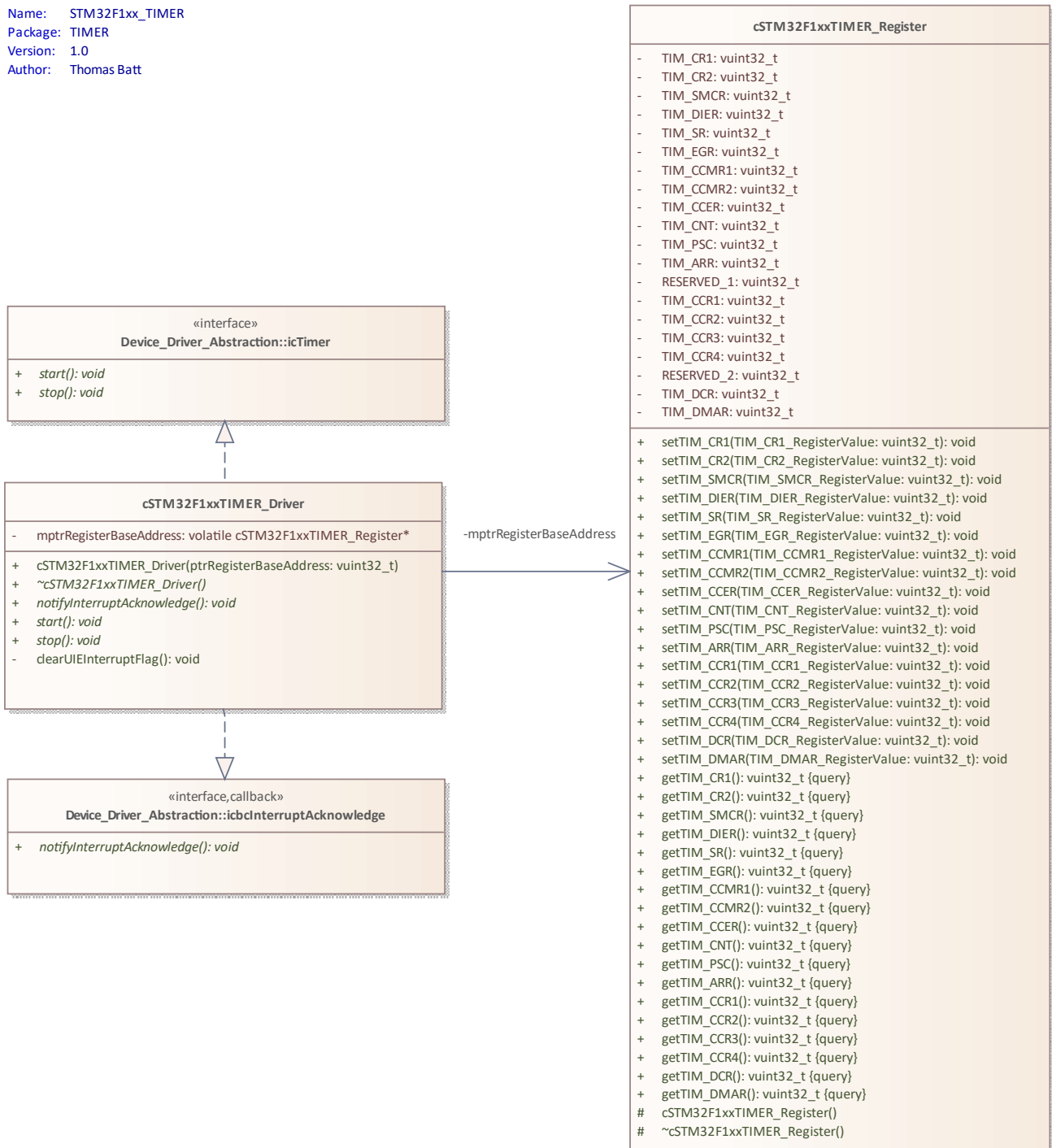


Figure 14: STM32F1xx\_TIMER



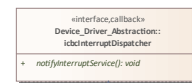
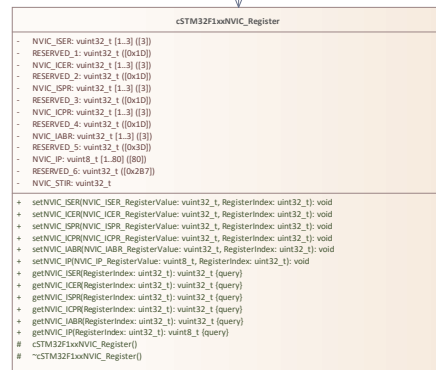
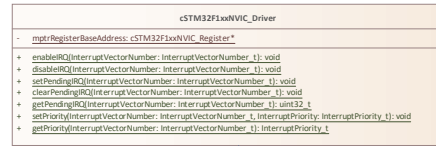
**1.1.1.6.2.1.1.3 STM32F1xx\_NVIC\_Dispatcher diagram***Class diagram in package 'NVIC\_Dispatcher'*

STM32F1xx\_NVIC\_Dispatcher

Version 1.0

Thomas Batt created on 13.01.2022. Last modified 28.01.2022

Name: STM32F1xx\_NVIC\_Dispatcher  
 Package: NVIC\_Dispatcher  
 Version: 1.0  
 Author: Thomas Batt



-mptrAcknowledgeObject

-mptrCallbackObject

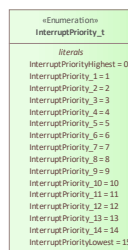
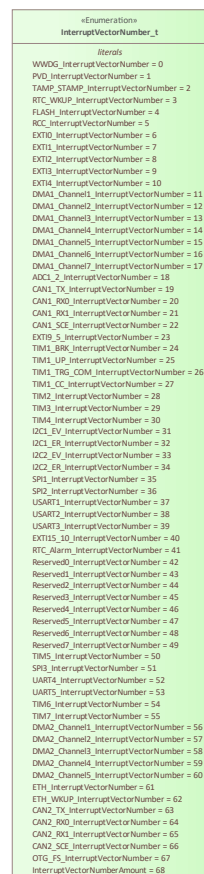
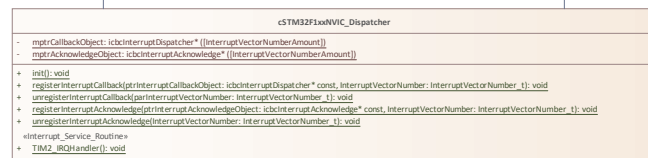


Figure 15: STM32F1xx\_NVIC\_Dispatcher

#### 1.1.1.6.2.1.1.4 STM32F7xx\_BasicIO diagram

*Class diagram in package 'BasicIO'*

STM32F7xx\_BasicIO

Version 1.0

Thomas Batt created on 13.01.2022. Last modified 28.01.2022

Name: STM32F7xx\_BasicIO  
Package: BasicIO  
Version: 1.0  
Author: Thomas Batt

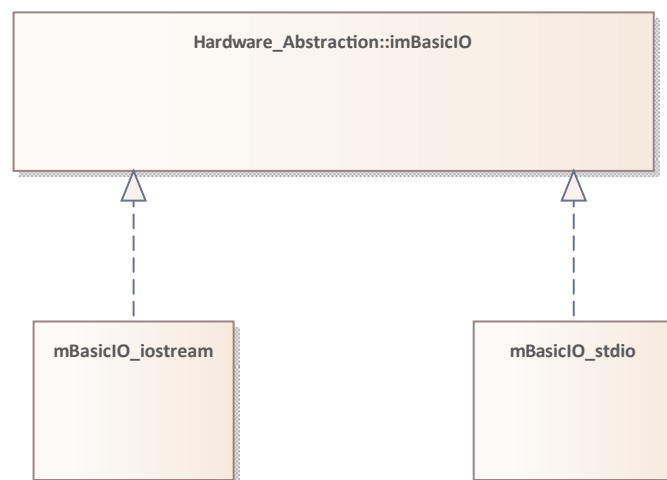


Figure 16: STM32F7xx\_BasicIO

### 1.1.1.6.2.1.1.5 STM32F7xx\_TIMER diagram

Class diagram in package 'TIMER'

STM32F7xx\_TIMER

Version 1.0

Thomas Batt created on 13.01.2022. Last modified 28.01.2022

Name: STM32F7xx\_TIMER

Package: TIMER

Version: 1.0

Author: Thomas Batt



Figure 17: STM32F7xx\_TIMER

**1.1.1.6.2.1.1.6 STM32F7xx\_NVIC\_Dispatcher diagram**

*Class diagram in package 'NVIC\_Dispatcher'*

STM32F7xx\_NVIC\_Dispatcher

Version 1.0

Thomas Batt created on 13.01.2022. Last modified 28.01.2022

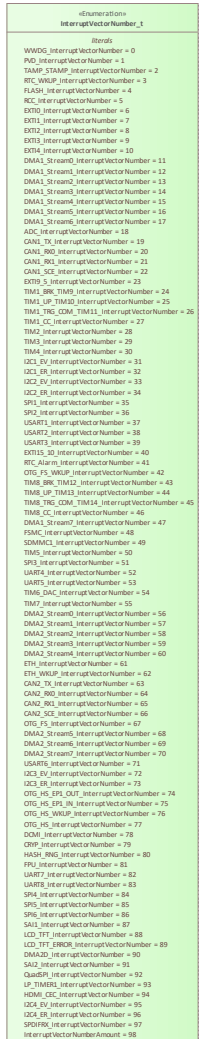


Figure 18: STM32F7xx\_NVIC\_Dispatcher



### 1.1.1.7 Hardware\_Configuration diagram

Class diagram in package 'Hardware\_Configuration'

Hardware\_Configuration

Version 1.0

Thomas Batt created on 13.01.2022. Last modified 27.01.2022

Name: Hardware\_Configuration  
Package: Hardware\_Configuration  
Version: 1.0  
Author: Thomas Batt

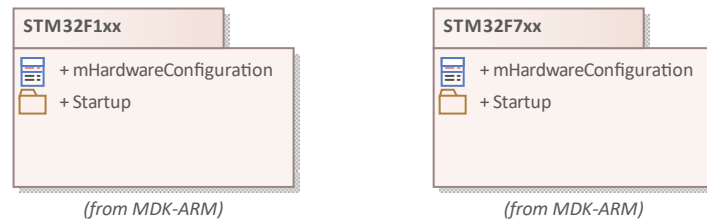


Figure 19: Hardware\_Configuration

### 1.1.1.8 Exercise\_1: cLimitCounter Class View diagram

*Class diagram in package 'Exersice\_1: Class and Object'*

Exersice\_1: cLimitCounter Class View  
Version 1.0

Thomas Batt created on 13.01.2022. Last modified 28.01.2022

Name: Exercise\_1: cLimitCounter Class View  
Package: Exercise\_1: Class and Object  
Version: 1.0  
Author: Thomas Batt

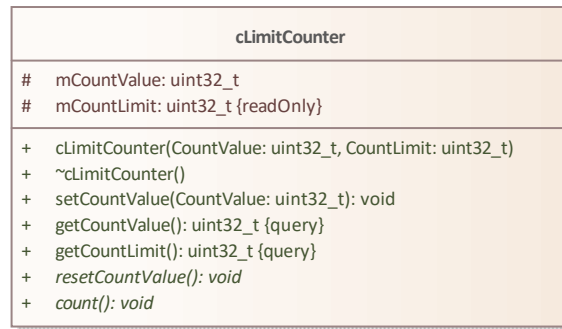


Figure 20: Exercise\_1: cLimitCounter Class View

### 1.1.1.9 Exercice\_1: cLimitCounter Object View diagram

Object diagram in package 'Exersice\_1: Class and Object'

Exersice\_1: cLimitCounter Object View  
Version 1.0

Thomas Batt created on 13.01.2022. Last modified 28.01.2022

Name: Exercice\_1: cLimitCounter Object  
Package: Exercice\_1: Class and Object  
Version: 1.0  
Author: Thomas Batt

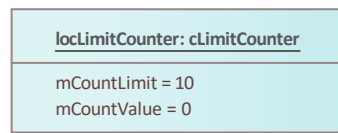


Figure 21: Exercice\_1: cLimitCounter Object View

### 1.1.1.10 Exercise\_2: cClock Class View diagram

Class diagram in package 'Exercise\_2: Relations between Classes'

Exercise\_2: cClock Class View

Version 1.0

Thomas Batt created on 13.01.2022. Last modified 09.02.2022

Name: Exercise\_2: cClock Class View  
 Package: Exercise\_2: Relations between Classes  
 Version: 1.0  
 Author: Thomas Batt

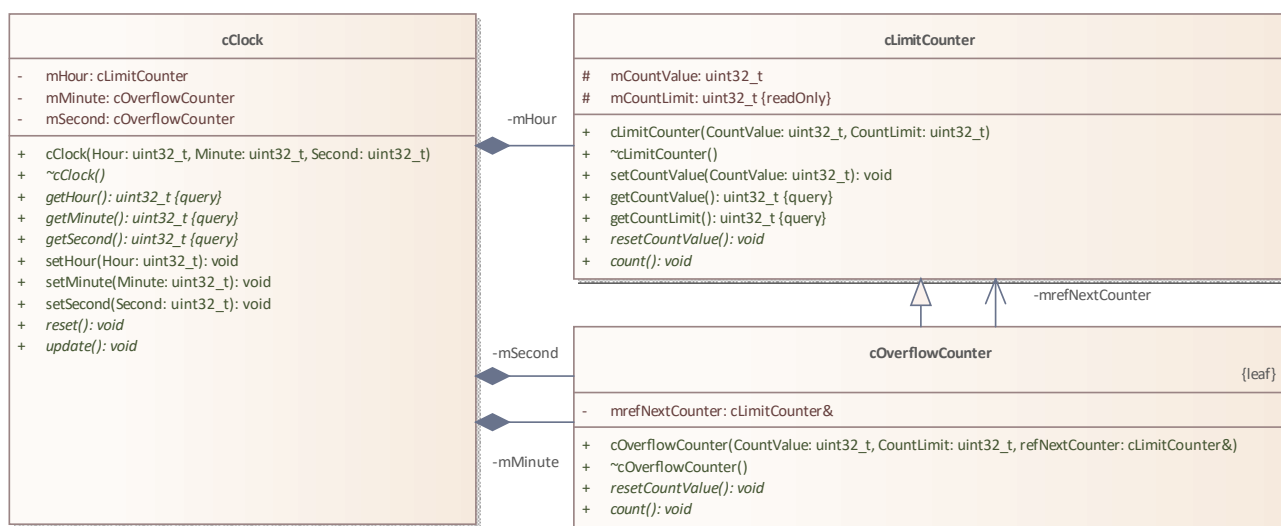


Figure 22: Exercise\_2: cClock Class View

### 1.1.1.11 Exercise\_2: cClock Object View diagram

Object diagram in package 'Exercise\_2: Relations between Classes'

Exercise\_2: cClock Object View  
Version 1.0

Thomas Batt created on 13.01.2022. Last modified 28.01.2022

Name: Exercise\_2: cClock Object View  
Package: Exercise\_2: Relations between Classes  
Version: 1.0  
Author: Thomas Batt

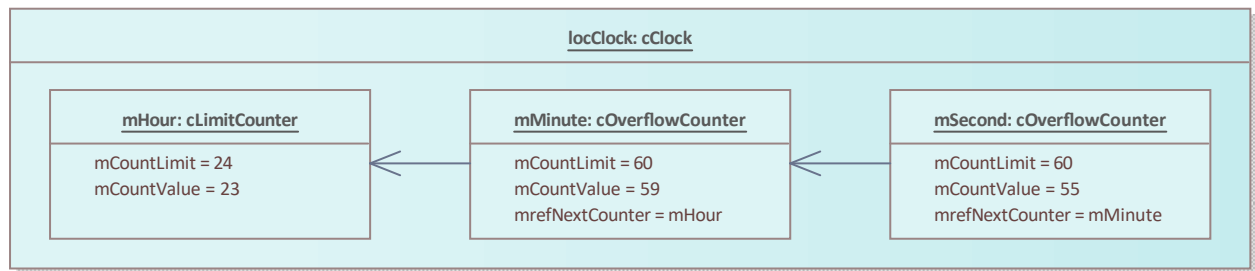


Figure 23: Exercise\_2: cClock Object View

### 1.1.1.12 Exercise\_3: icClock Realization Class View diagram

Class diagram in package 'Exercise\_3: Virtual Functions and Interface Class'

Exercise\_3: icClock Realization Class View

Version 1.0

Thomas Batt created on 13.01.2022. Last modified 28.01.2022

Name: Exercise\_3: icClock Realization Class View  
Package: Exercise\_3: Virtual Functions and Interface Class  
Version: 1.0  
Author: Thomas Batt

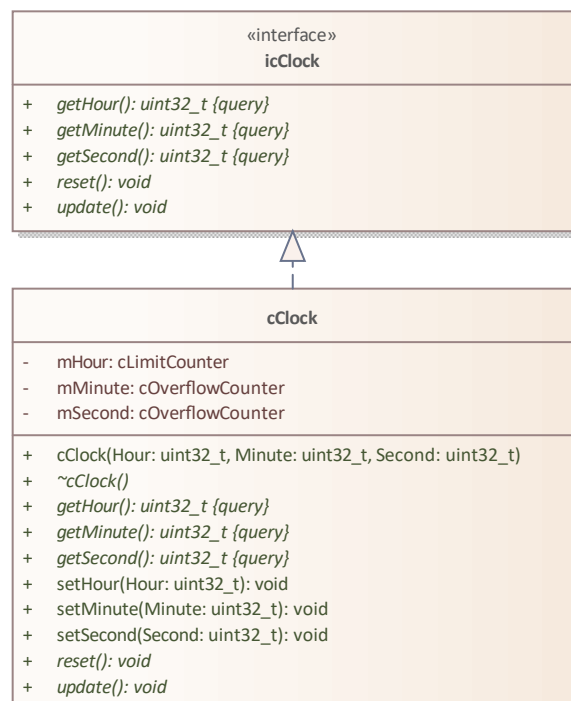


Figure 24: Exercise\_3: icClock Realization Class View

### 1.1.1.13 Exercise\_3: icClock Access Class\_View diagram

Class diagram in package 'Exercise\_3: Virtual Functions and Interface Class'

Exercise\_3: icClock Access Class\_View  
Version 1.0

Thomas Batt created on 27.01.2022. Last modified 26.09.2022

Name: Exercise\_3: icClock Access Class\_View  
Package: Exercise\_3: Virtual Functions and Interface Class  
Version: 1.0  
Author: Thomas Batt

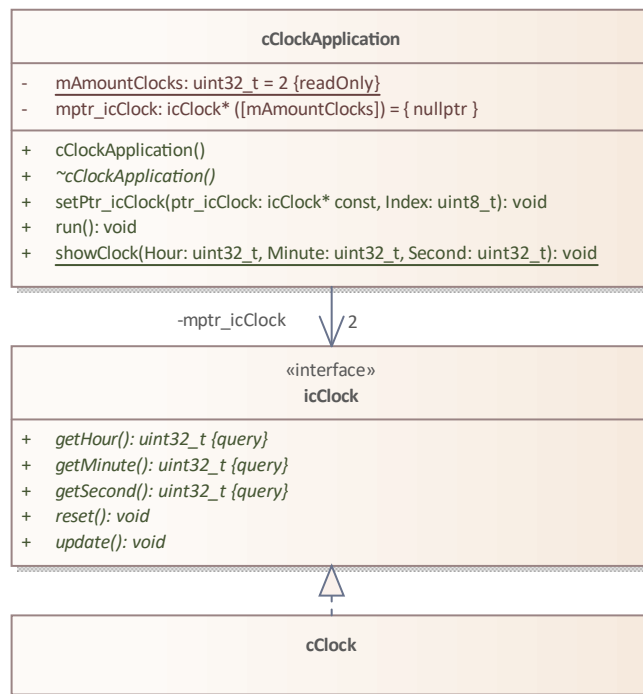


Figure 25: Exercise\_3: icClock Access Class\_View

### 1.1.1.14 Exercise\_3: cClockApplicationBuilder Class View diagram

Class diagram in package 'Exercise\_3: Virtual Functions and Interface Class'

Exercise\_3: cClockApplicationBuilder Class View

Version 1.0

Thomas Batt created on 27.01.2022. Last modified 28.01.2022

Name: Exercise\_3: cClockApplicationBuilder Class View  
Package: Exercise\_3: Virtual Functions and Interface Class  
Version: 1.0  
Author: Thomas Batt

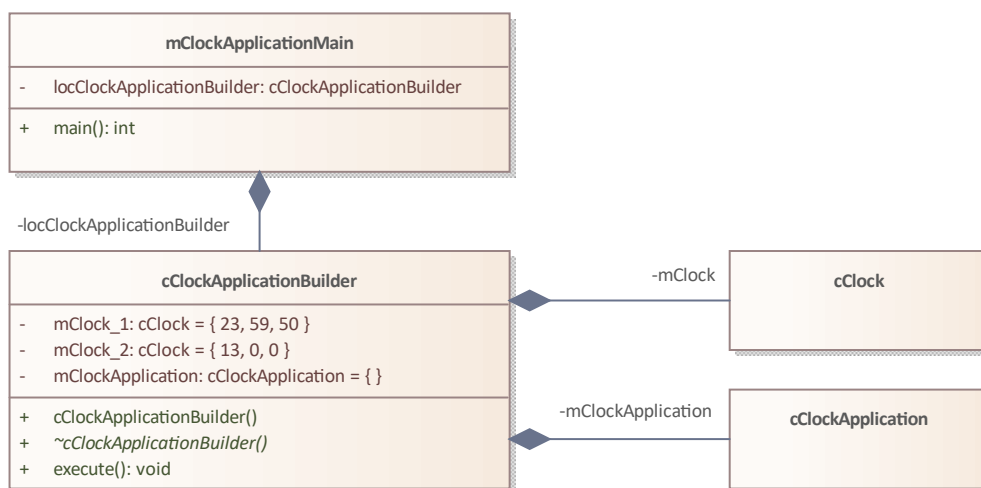


Figure 26: Exercise\_3: cClockApplicationBuilder Class View



### 1.1.1.15 Exercise\_3: cClockApplicationBuilder Object View diagram

Object diagram in package 'Exercise\_3: Virtual Functions and Interface Class'

Exercise\_3: cClockApplicationBuilder Object View  
Version 1.0

Thomas Batt created on 13.01.2022. Last modified 28.01.2022

Name: Exercise\_3: cClockApplicationBuilder Object View  
Package: Exercise\_3: Virtual Functions and Interface Class  
Version: 1.0  
Author: Thomas Batt

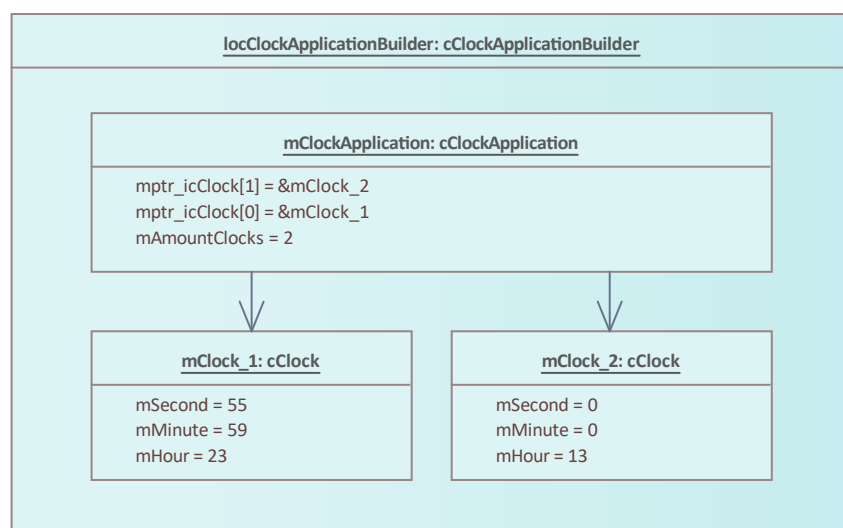


Figure 27: Exercise\_3: cClockApplicationBuilder Object View

### 1.1.1.16 Exercise\_4: Callback Interface Access and Realization Class View diagram

Class diagram in package 'Exercise\_4: Object Oriented Callback Structure'

Exercise\_4: Callback Interface Access and Realization Class View

Version 1.0

Thomas Batt created on 13.01.2022. Last modified 26.09.2022

Name: Exercise\_4: Callback Interface Access and Realization Class View

Package: Exercise\_4: Object Oriented Callback Structure

Version: 1.0

Author: Thomas Batt

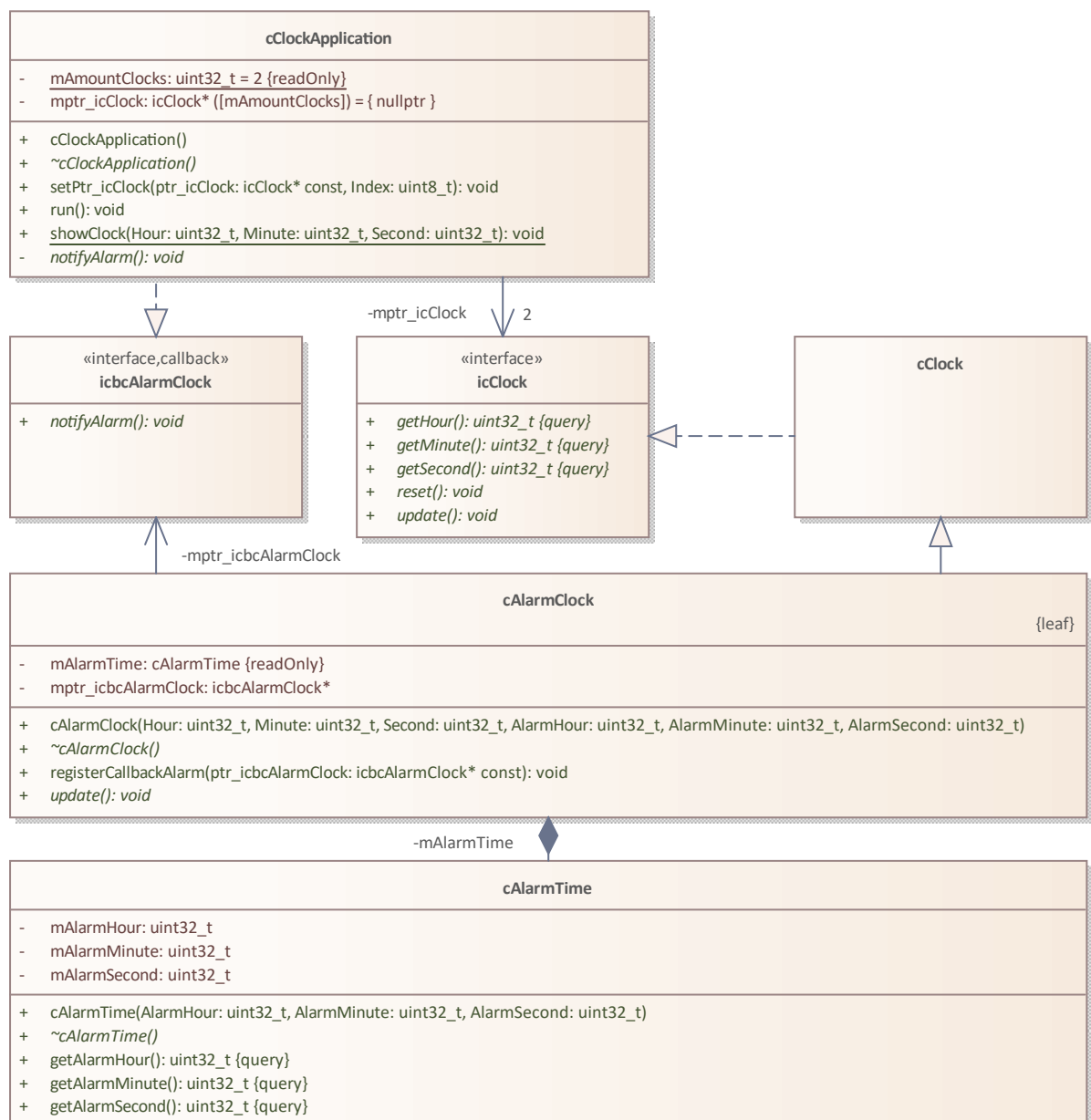


Figure 28: Exercise\_4: Callback Interface Access and Realization Class View

### 1.1.1.17 Exercise\_4: cClockApplicationBuilder Class\_View diagram

Class diagram in package 'Exercise\_4: Object Oriented Callback Structure'

Exercise\_4: cClockApplicationBuilder Class\_View  
Version 1.0

Thomas Batt created on 27.01.2022. Last modified 26.09.2022

Name: Exercise\_4: cClockApplicationBuilder Class\_View  
Package: Exercise\_4: Object Oriented Callback Structure  
Version: 1.0  
Author: Thomas Batt

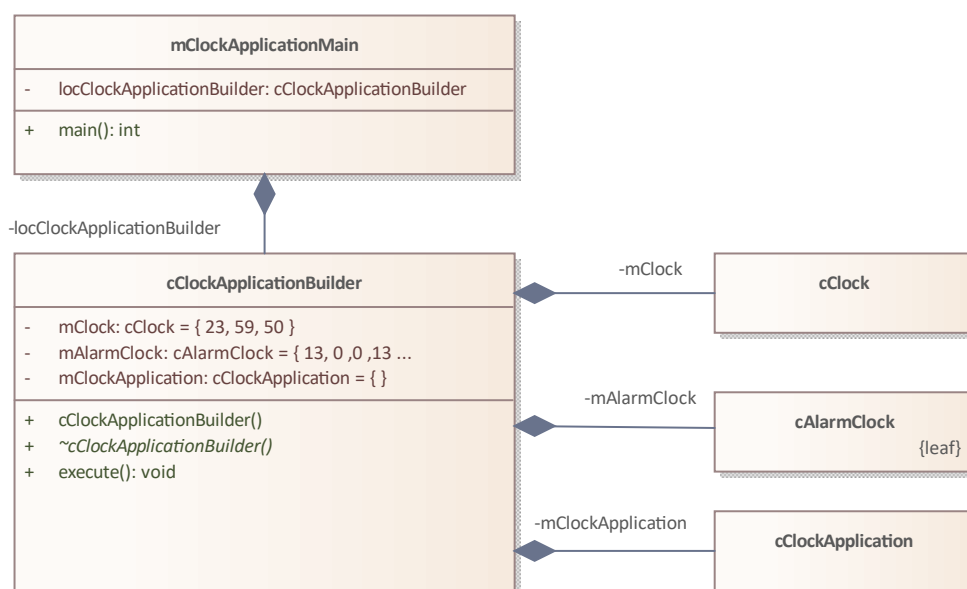


Figure 29: Exercise\_4: cClockApplicationBuilder Class\_View

### 1.1.1.18 Exercise\_4: cClockApplicationBuilder Object View diagram

Object diagram in package 'Exercise\_4: Object Oriented Callback Structure'

Exercise\_4: cClockApplicationBuilder Object View

Version 1.0

Thomas Batt created on 13.01.2022. Last modified 28.01.2022

Name: Exercise\_4: cClockApplicationBuilder Object View

Package: Exercise\_4: Object Oriented Callback Structure

Version: 1.0

Author: Thomas Batt

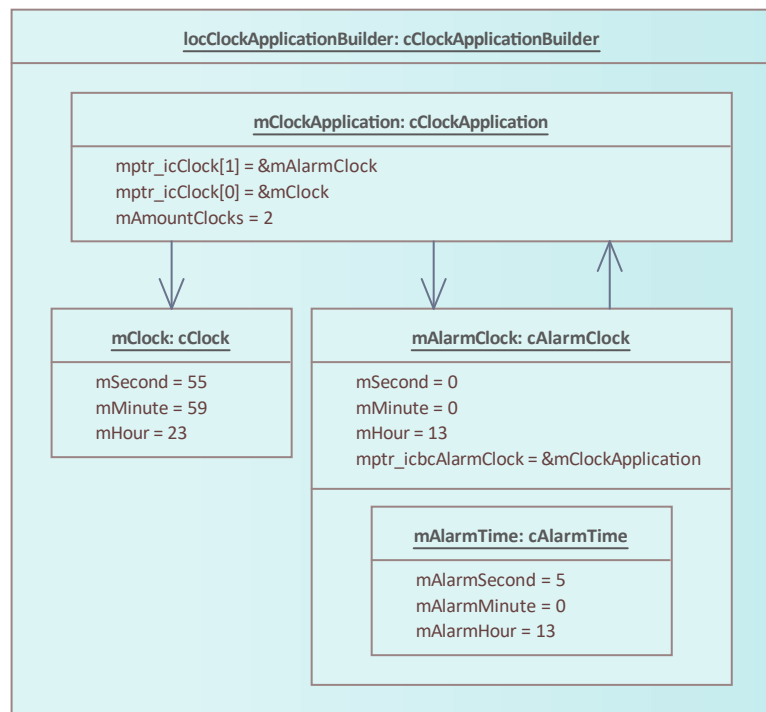


Figure 30: Exercise\_4: cClockApplicationBuilder Object View

### 1.1.1.19 Exercise\_5: icTimer Access and Realization Class View diagram

Class diagram in package 'Exercise\_5: Hardware Driver and Interrupt'

Exercise\_5: icTimer Access and Realization Class View

Version 1.0

Thomas Batt created on 13.01.2022. Last modified 07.02.2022

Name: Exercise\_5: icTimer Access and Realization Class View

Package: Exercise\_5: Hardware Driver and Interrupt

Version: 1.0

Author: Thomas Batt

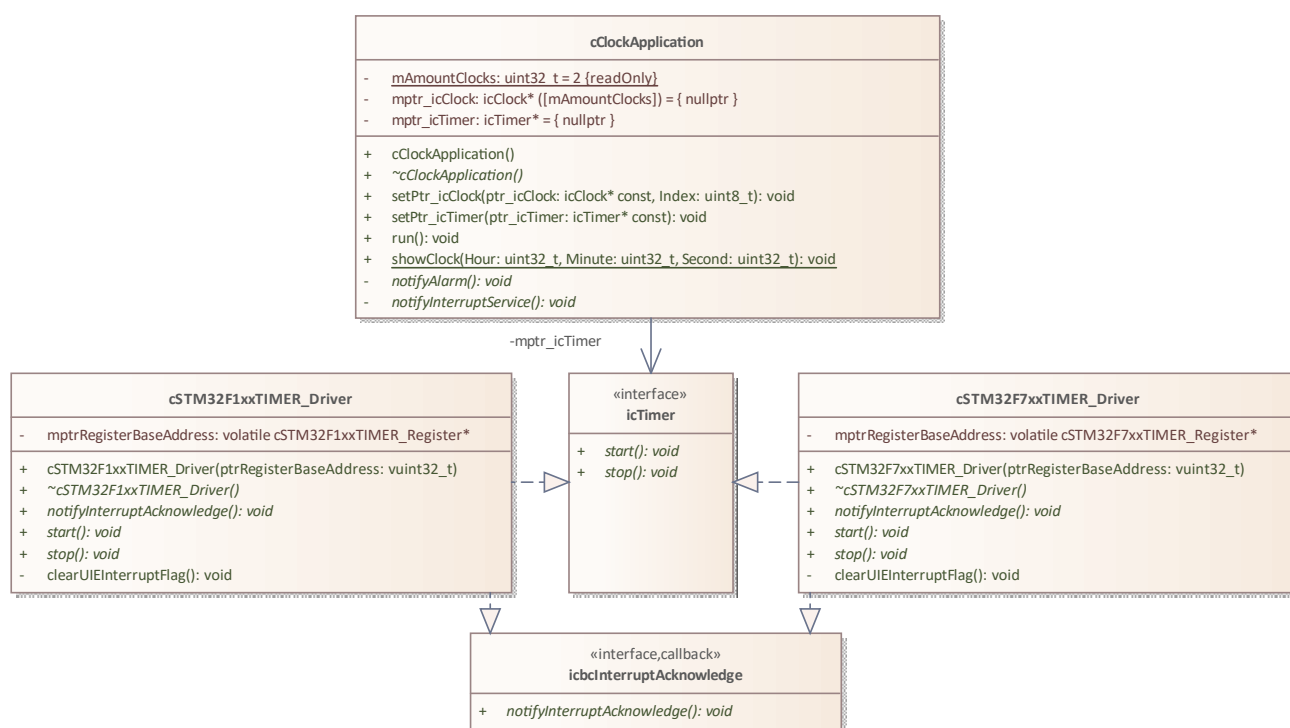


Figure 31: Exercise\_5: icTimer Access and Realization Class View

### 1.1.1.20 Exercise\_5: icbcInterruptDispatcher Access and Realization MCBSTM32 / Simulator Class View diagram

Class diagram in package 'Exercise\_5: Hardware Driver and Interrupt'

Exercise\_5: icbcInterruptDispatcher Access and Realization MCBSTM32 / Simulator Class View

Version 1.0

Thomas Batt created on 07.02.2022. Last modified 26.09.2022

Name: Exercise\_5: icbcInterruptDispatcher Access and Realization MCBSTM32 / Simulator Class View

Package: Exercise\_5: Hardware Driver and Interrupt

Version: 1.0

Author: Thomas Batt

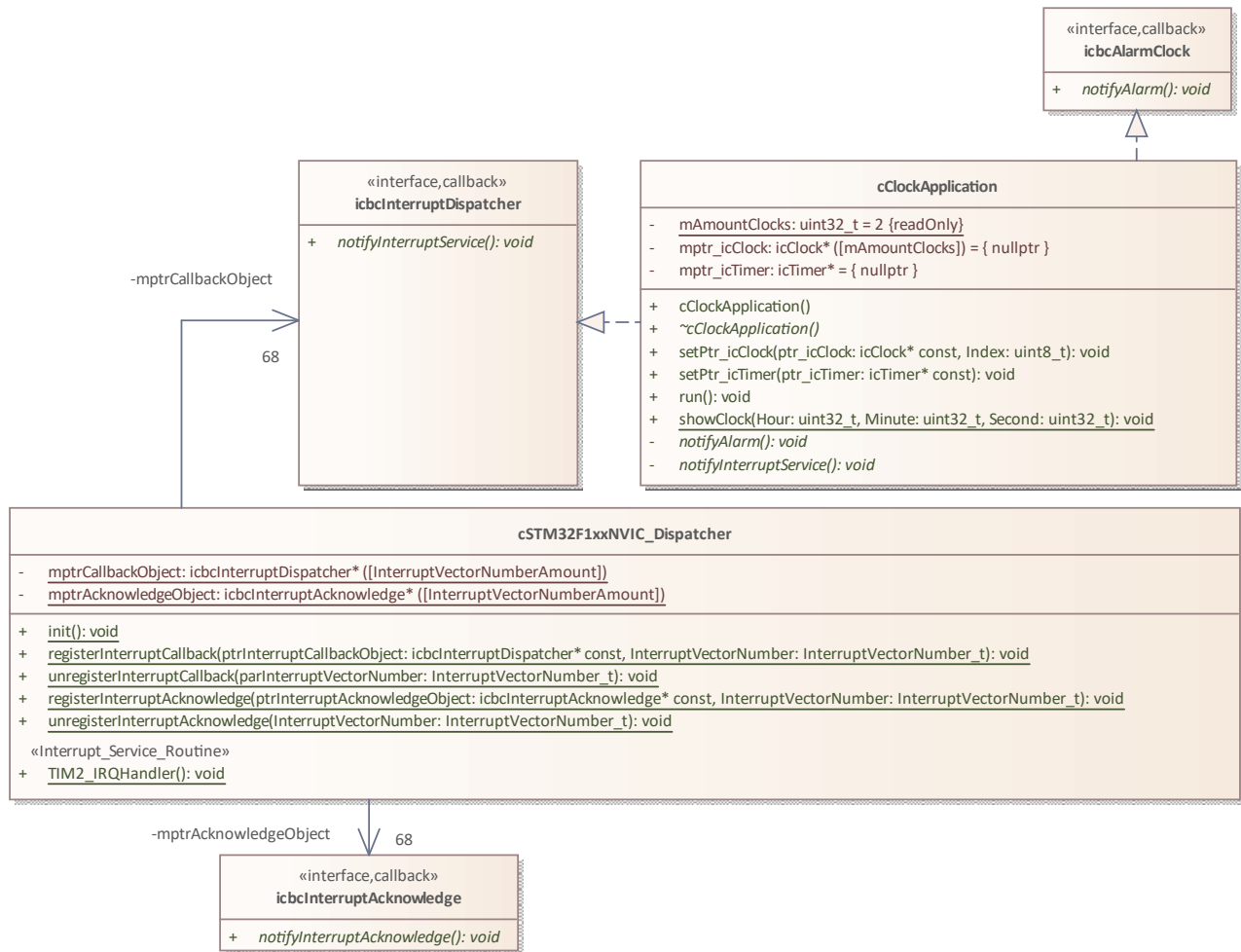


Figure 32: Exercise\_5: icbcInterruptDispatcher Access and Realization MCBSTM32 / Simulator Class View

### 1.1.1.21 Exercise\_5: cClockApplicationBuilder MCBSTM32\_Simulator Class View diagram

Class diagram in package 'Exercise\_5: Hardware Driver and Interrupt'

Exercise\_5: cClockApplicationBuilder MCBSTM32\_Simulator Class View

Version 1.0

Thomas Batt created on 28.01.2022. Last modified 07.02.2022

Name: Exercise\_5: cClockApplicationBuilder MCBSTM32\_Simulator Class View

Package: Exercise\_5: Hardware Driver and Interrupt

Version: 1.0

Author: Thomas Batt

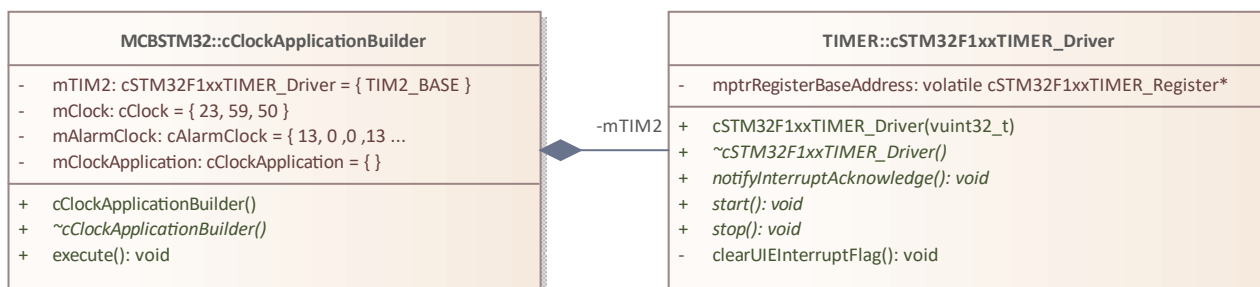


Figure 33: Exercise\_5: cClockApplicationBuilder MCBSTM32\_Simulator Class View

### 1.1.1.22 Exercise\_5: cClockApplicationBuilder MCBSTM32\_Simulator Object View diagram

Object diagram in package 'Exercise\_5: Hardware Driver and Interrupt'

Exercise\_5: cClockApplicationBuilder MCBSTM32\_Simulator Object View  
Version 1.0

Thomas Batt created on 13.01.2022. Last modified 28.01.2022

Name: Exercise\_5: cClockApplicationBuilder MCBSTM32\_Simulator Object View  
Package: Exercise\_5: Hardware Driver and Interrupt  
Version: 1.0  
Author: Thomas Batt

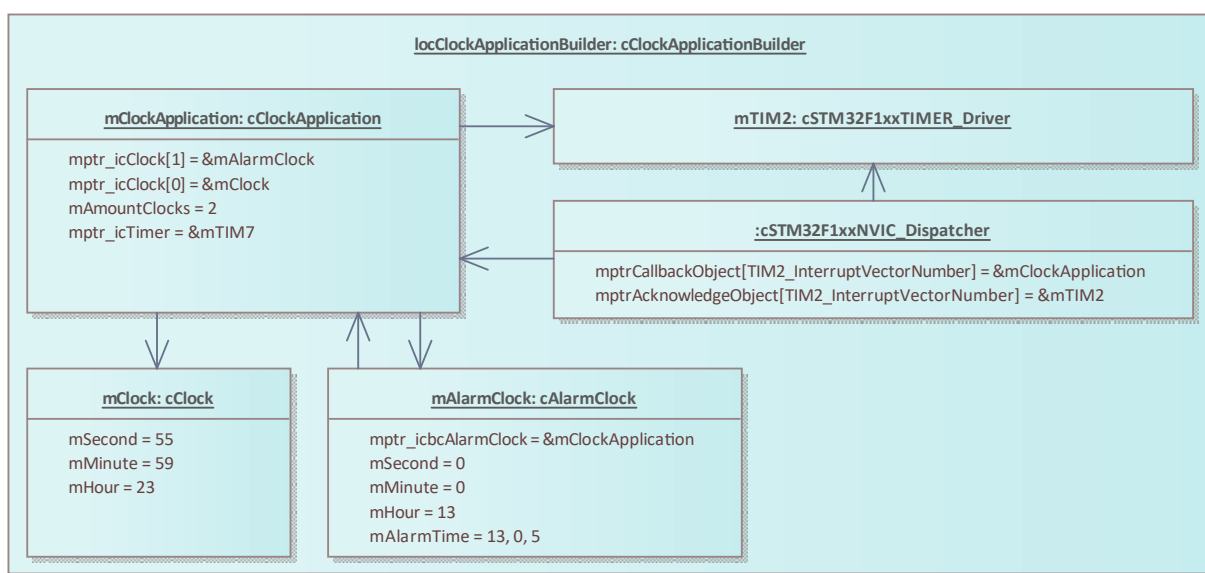


Figure 34: Exercise\_5: cClockApplicationBuilder MCBSTM32\_Simulator Object View



### 1.1.1.23 Exercise\_5: icbcInterruptDispatcher Access and Realization STM32NUCLEO-F746ZG Class View diagram

Class diagram in package 'Exercise\_5: Hardware Driver and Interrupt'

Exercise\_5: icbcInterruptDispatcher Access and Realization STM32NUCLEO-F746ZG Class View

Version 1.0

Thomas Batt created on 28.01.2022. Last modified 26.09.2022

Name: Exercise\_5: icbcInterruptDispatcher Access and Realization STM32NUCLEO-F746ZG Class View

Package: Exercise\_5: Hardware Driver and Interrupt

Version: 1.0

Author: Thomas Batt

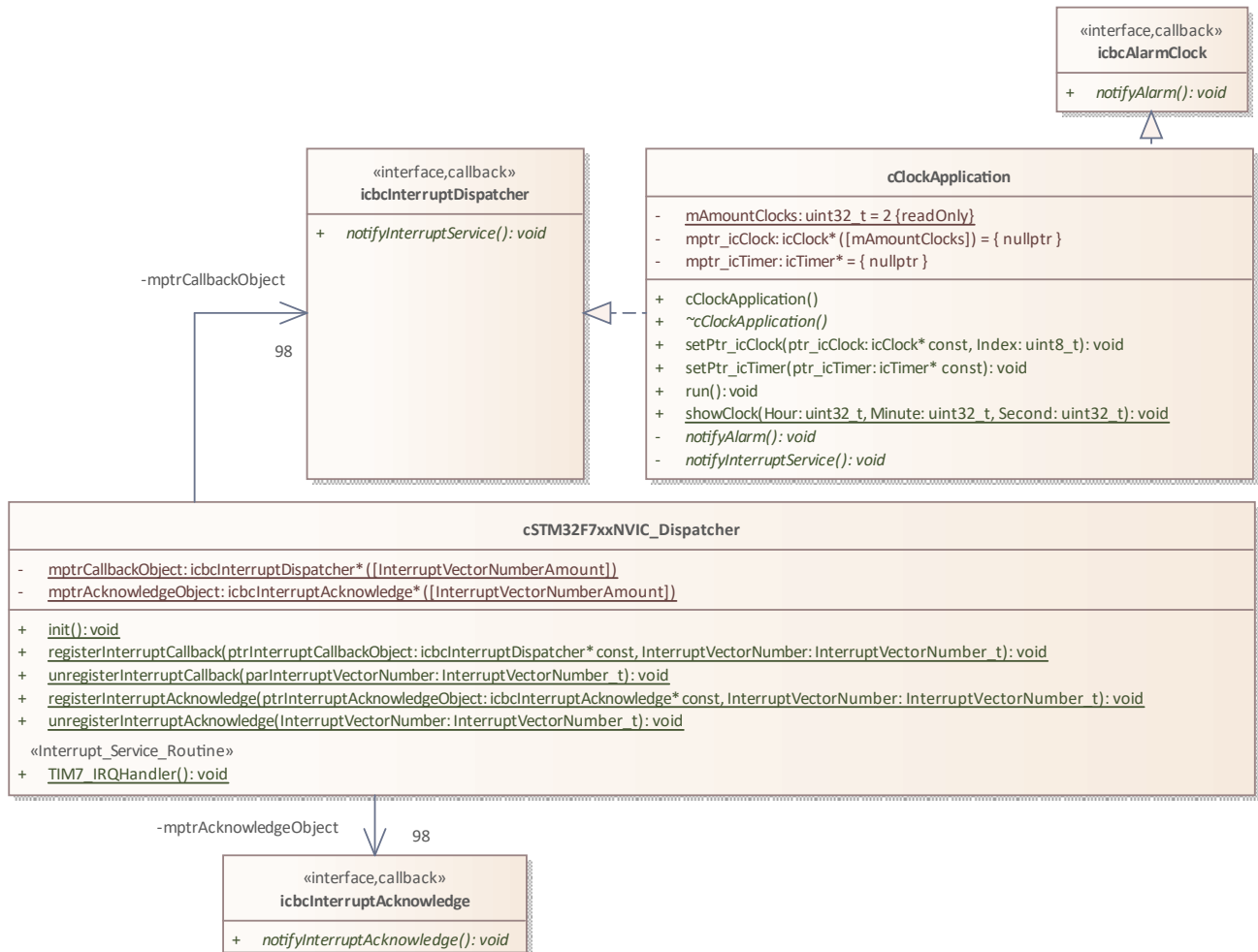


Figure 35: Exercise\_5: icbcInterruptDispatcher Access and Realization STM32NUCLEO-F746ZG Class View

### 1.1.1.24 Exercise\_5: cClockApplicationBuilder STM32NUCLEO-F746ZG Class

#### View diagram

Class diagram in package 'Exercise\_5: Hardware Driver and Interrupt'

Exercise\_5: cClockApplicationBuilder STM32NUCLEO-F746ZG Class View

Version 1.0

Thomas Batt created on 28.01.2022. Last modified 07.02.2022

Name: Exercise\_5: cClockApplicationBuilder STM32NUCLEO-F746ZG Class View

Package: Exercise\_5: Hardware Driver and Interrupt

Version: 1.0

Author: Thomas Batt

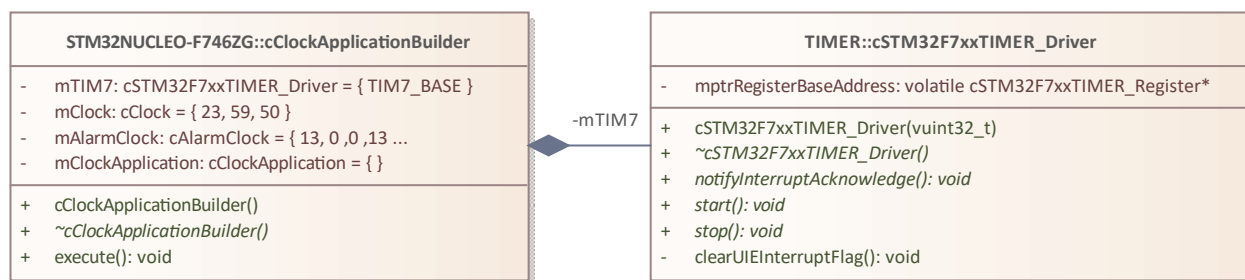


Figure 36: Exercise\_5: cClockApplicationBuilder STM32NUCLEO-F746ZG Class View

### 1.1.1.25 Exercise\_5: cClockApplicationBuilder STM32NUCLEO-F746ZG Object View diagram

Object diagram in package 'Exercise\_5: Hardware Driver and Interrupt'

Exercise\_5: cClockApplicationBuilder STM32NUCLEO-F746ZG Object View

Version 1.0

Thomas Batt created on 28.01.2022. Last modified 07.02.2022

Name: Exercise\_5: cClockApplicationBuilder STM32NUCLEO-F746ZG Object View

Package: Exercise\_5: Hardware Driver and Interrupt

Version: 1.0

Author: Thomas Batt

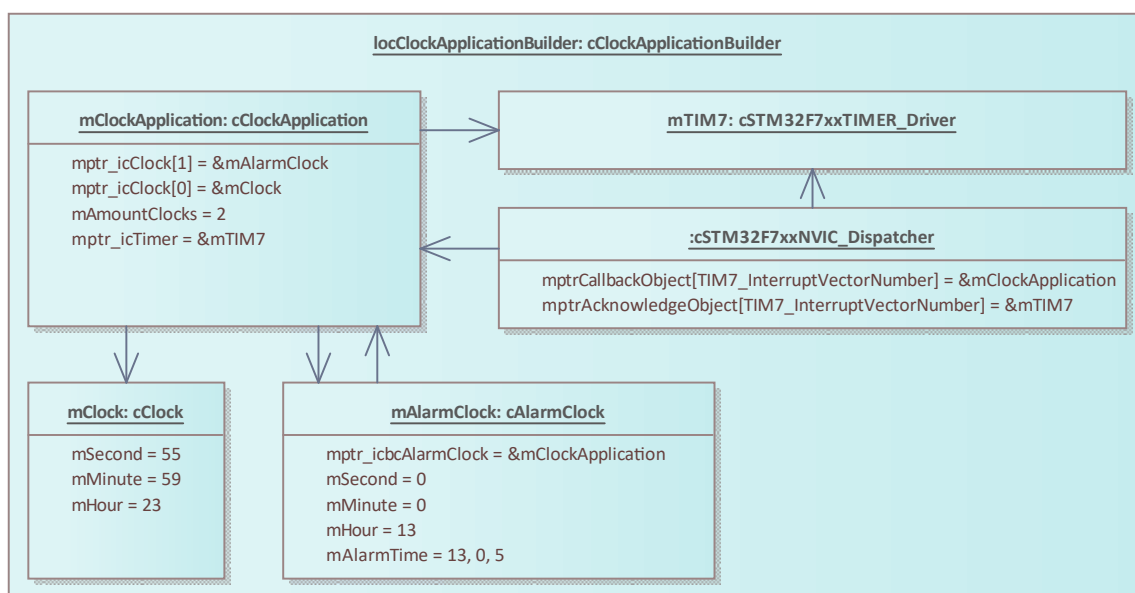


Figure 37: Exercise\_5: cClockApplicationBuilder STM32NUCLEO-F746ZG Object View

