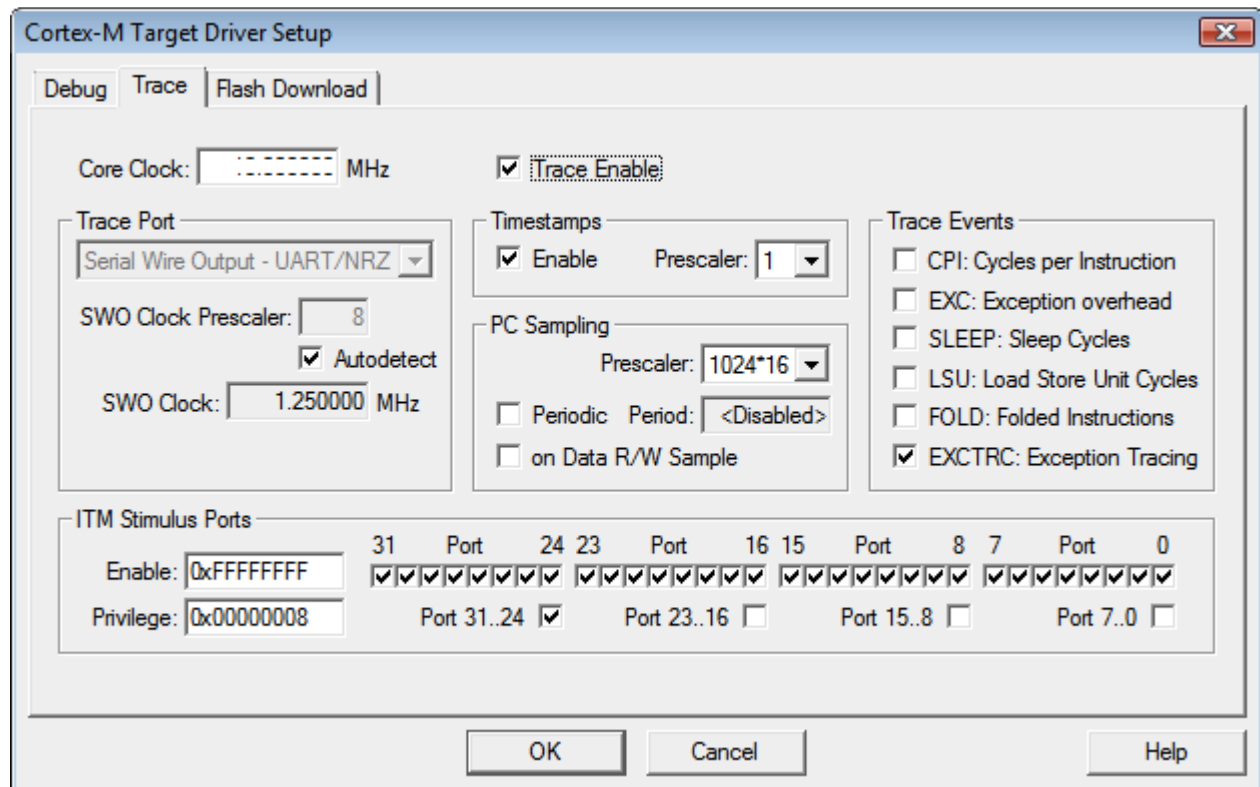


Trace Configuration

The dialog **Trace** configures the real-time trace operations. This dialog is supported for Cortex-M3 and Cortex-M4 processor-based devices.



Core Clock must be set equal to the CPU clock frequency to receive correct data streams. Some applications start from RESET with a lower CPU **Core Clock** (for example, devices with on-chip oscillators) and switch to a higher clock in the startup code. For such applications, stop program execution before the clock switches, and modify **Core Clock** to match the new clock rate. As an alternative, assign the new value to the VTREG **CORE_CLK**.

Trace Enable activates capturing real-time trace data.

The sections below determine the trace ports and the captured information:

- [Trace Port](#) configures the channel used for capturing trace.
- [ITM Stimulus Ports](#) activates various ports, which can be used from the program code, to obtain debugging or program-specific information.
- [Timestamps](#) sends time information along with trace events.
- [PC Sampling](#) enables PC information indicating the program location that triggered a trace event.
- [Trace Events](#) report specific CPU events.

Note

- [Configuring Devices for Tracing](#) explains the settings for various targets.