Alexander Eamons 12/6/2019 ECE 271 Design Project

Display Controller 1.

	Display Controller		4
50MHz-CIK	clk-50MHz	r[3.0]	r/'
25MHz.clk	Clk-25MHz	q[3.0]	9/
x-cursor,6	X_cursor[s.,0]	6[30]	b /
y-cursor, 6	y-cwsor[5.0]	h-sync	h-sync_
d	write	V_sync	V-synu
switch 240,3	r_write [2:0]		
switch 5,43,3	q_write[2:0]		
switch 8,7,6,3	b_write[2:0]		