ECE 271 Design Project 12/6/2019 Alexander Eamons Hordware Diagram hsync vsync RIH G H B H data FPGA NES Controller +5V VGA GND Seq 7 Seq 1,7 Seq 2,7 Seq 3,7 Seq 3,7 Seq 5,7 6× Zseament dato GND +SV CIK PS/2 Controller buttons 12 LEDS data SNES Controller 45V GND