12/6/2019

ECE 271 Design Project

	VGAAdapter		
clh gin H bin H	-> pixel-clock r-in[30] g-in[30] b-in[30]	h-sync v-sync r-out[s.0] g-out[s.0] b-out[s.0] x-fetch[q.0]	h-sync V-sync r-out /t g-out /t b-out /t x-fetch /10 y-fetch /10