Contents

Datasheet

Functional overview	15
- ARM® Cortex®-M4 with FPU core with embedded Flash ar SRAM	
– Adaptive real-time memory accelerator (ART Accelerator [™])	15
- Memory protection unit	15
- Embedded Flash memory	16
- CRC (cyclic redundancy check) calculation unit	16
- Embedded SRAM	16
- Multi-AHB bus matrix	16
- DMA controller (DMA)	17
- Nested vectored interrupt controller (NVIC)	18
- External interrupt/event controller (EXTI)	18
- Clocks and startup	18
- Boot modes	19
- Power supply schemes	19
- Power supply supervisor	20
- Internal reset ON	20
- Internal reset OFF	20
- Voltage regulator	21
- Regulator ON	21
- Regulator OFF	22
- Regulator ON/OFF and internal power supply supervisor availability 25	ty
- Real-time clock (RTC) and backup registers	25
- Low-power modes	26
- VBAT operation	26
- Timers and watchdogs	27
- Advanced-control timers (TIM1)	27
- General-purpose timers (TIMx)	28
- Independent watchdog	28
- Window watchdog	28
- SysTick timer	20

_	Inter-integrated circuit interface (I2C) $$	29
_	Universal synchronous/asynchronous receiver transmitters (USAR 29	(T)
_	Serial peripheral interface (SPI) \hdots	30
_	Inter-integrated sound (I2S)	30
_	Audio PLL (PLLI2S)	30
_	Secure digital input/output interface (SDIO) $$	31
_	Universal serial bus on-the-go full-speed (OTG_FS) \hdots	31
_	General-purpose input/outputs (GPIOs)	31
_	Analog-to-digital converter (ADC)	31
_	Temperature sensor	.32
_	Serial wire JTAG debug port (SWJ-DP)	32
_	Embedded Trace Macrocell $^{\!$.32
• Pino	uts and pin description	.33
• Mem	nory mapping	51
• Elect	trical characteristics	55
_	Parameter conditions	55
_	Minimum and maximum values	55
_	Typical values	55
_	Typical curves	55
_	Loading capacitor	55
_	Pin input voltage	56
_	Power supply scheme	57
_	Current consumption measurement	58
_	Absolute maximum ratings	58
_	Operating conditions	60
_	General operating conditions	60
_	VCAP1/VCAP2 external capacitors	62
_	Operating conditions at power-up/power-down (regulator ON) \dots	63
_	Operating conditions at power-up $/$ power-down (regulator OFF)	63
_	Embedded reset and power control block characteristics	64
_	Supply current characteristics	65
_	Wakeup time from low-power modes	75
_	External clock source characteristics	76

- Internal clock source characteristics	80
- PLL characteristics	82
- PLL spread spectrum clock generation (SSCG) characteristics	84
- Memory characteristics	85
- EMC characteristics	87
- Absolute maximum ratings (electrical sensitivity)	89
- I/O current injection characteristics	90
- I/O port characteristics	91
- NRST pin characteristics	96
- TIM timer characteristics	97
- Communications interfaces	98
- 12-bit ADC characteristics	.106
- Temperature sensor characteristics	.112
- VBAT monitoring characteristics	. 112
- Embedded reference voltage	. 112
$-$ SD/SDIO MMC card host interface (SDIO) characteristics \ldots	.113
- RTC characteristics	. 114
Package characteristics	. 115
- Package mechanical data	. 115
- WLCSP49, 3.06 x 3.06 mm, 0.4 mm pitch wafer level chip size p age	
- UFQFPN48, 7 x 7 mm, 0.5 mm pitch package	. 119
$-$ LQFP64, 10 x 10 mm, 64-pin low-profile quad flat package \ldots	.122
- LQFP100, 14 x 14 mm, 100-pin low-profile quad flat package $$. 125
- UFBGA100, 7 x 7 mm, 0.5 mm pitch package	. 128
- Thermal characteristics	. 131
- Reference document	. 131
• Part numbering	. 132
Ravisian history	13/