2025 Digital IC Design Homework 1

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| NAME | |  | | | | | | |
| Student ID | |  | | | | | | |
| **Functional Simulation Result** | | | | | | | | |
| Stage 1 | Pass/Fail | | Stage 2 | Pass/Fail | Stage 3 | Pass/Fail | Stage 4 | Pass/Fail |
| **Stage 1** | | | | | | | | |
| (your simulation result) | | | | | | | | |
| **Stage 2** | | | | | | | | |
| (your simulation result) | | | | | | | | |
| **Stage 3** | | | | | | | | |
| (your simulation result) | | | | | | | | |
| **Stage 4** | | | | | | | | |
| (your simulation result) | | | | | | | | |
| **Description of your design** | | | | | | | | |
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