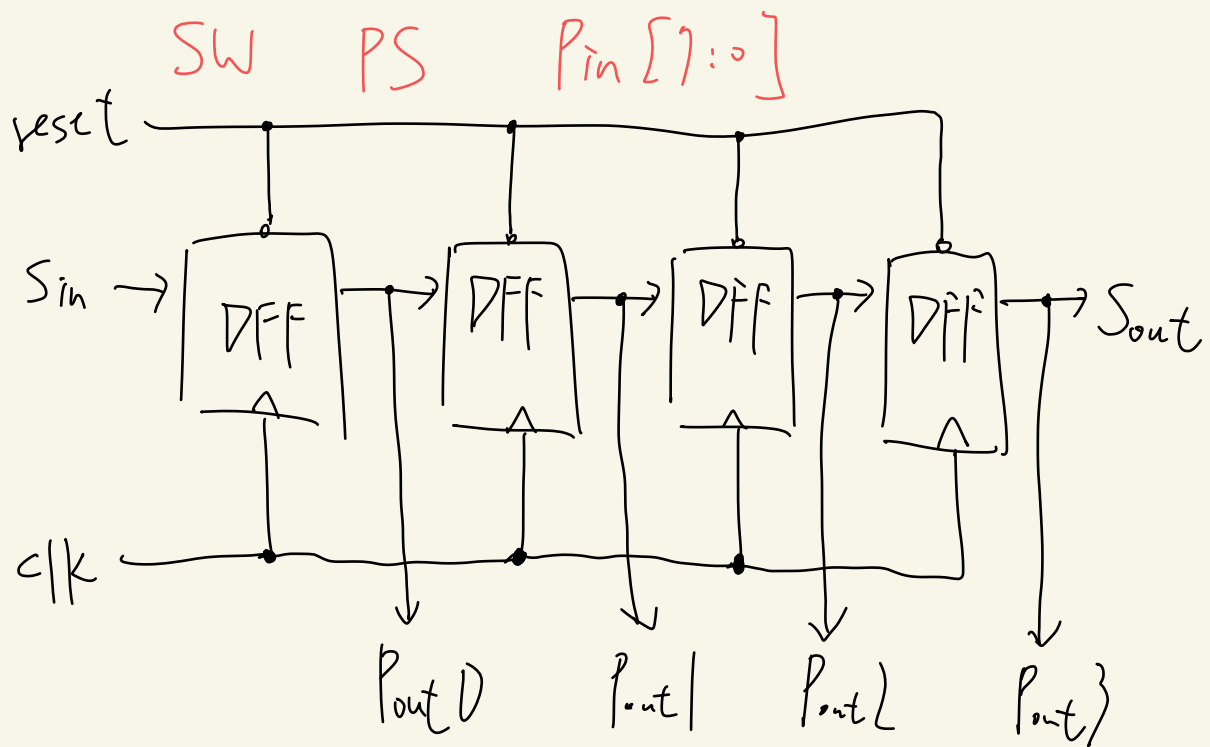


建立在 SISO, SIPO 的架構基礎上
做 PISO, PIPO 的設計



PS \ SW	SW == 1 (Shift mode)	SW == 0 (Write mode)
PS == 1 (Pin)	$SReg[7:0] =$ $\{SReg[6:0], SReg[7]\}$	$SReg[7:0] =$ $Pin[7:0]$
PS == 0 (Sin)	$SReg[7:0] =$ $\{SReg[6:0], SReg[7]\}$	$SReg[7:0] =$ $\{SReg[6:0], Sin\}$