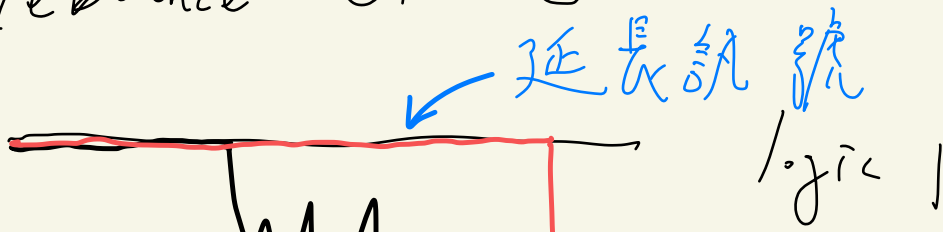


# Debounce Circuit (以 $1 \rightarrow 0$ 為例)



穩定後再輸出



State Program to T :

