

Q: Activate each of the inputs for several seconds and then press the stop button to stop the recording.

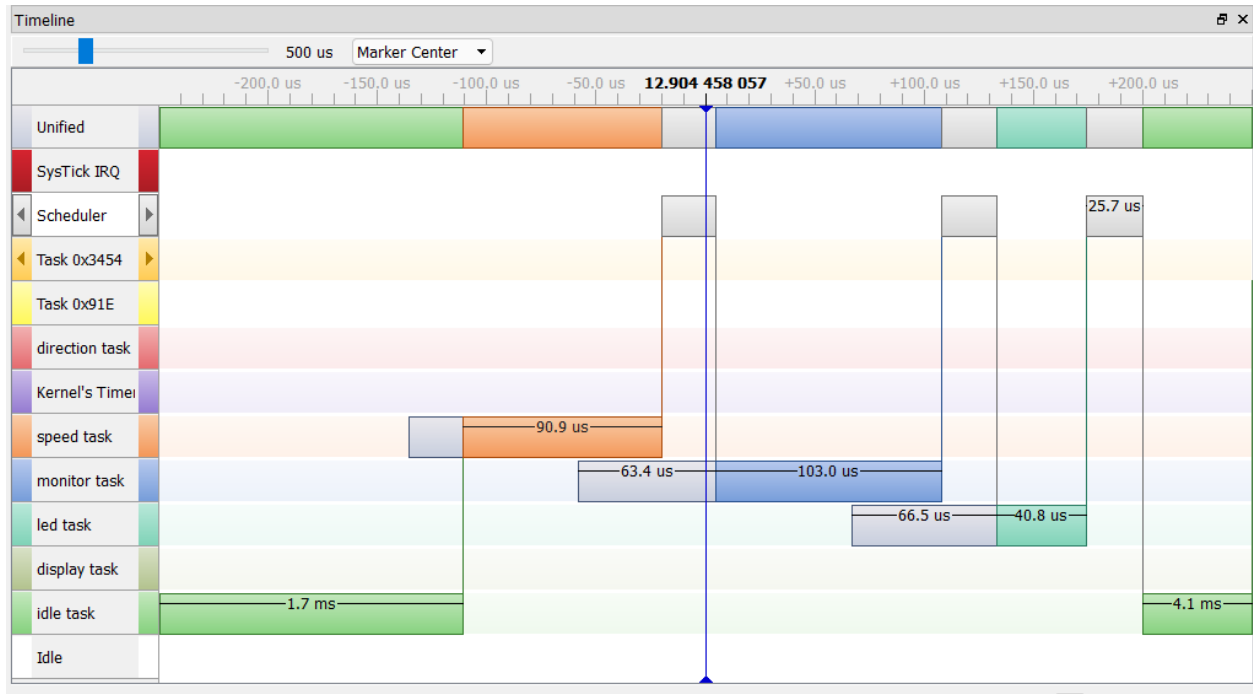


Fig. Speed Task

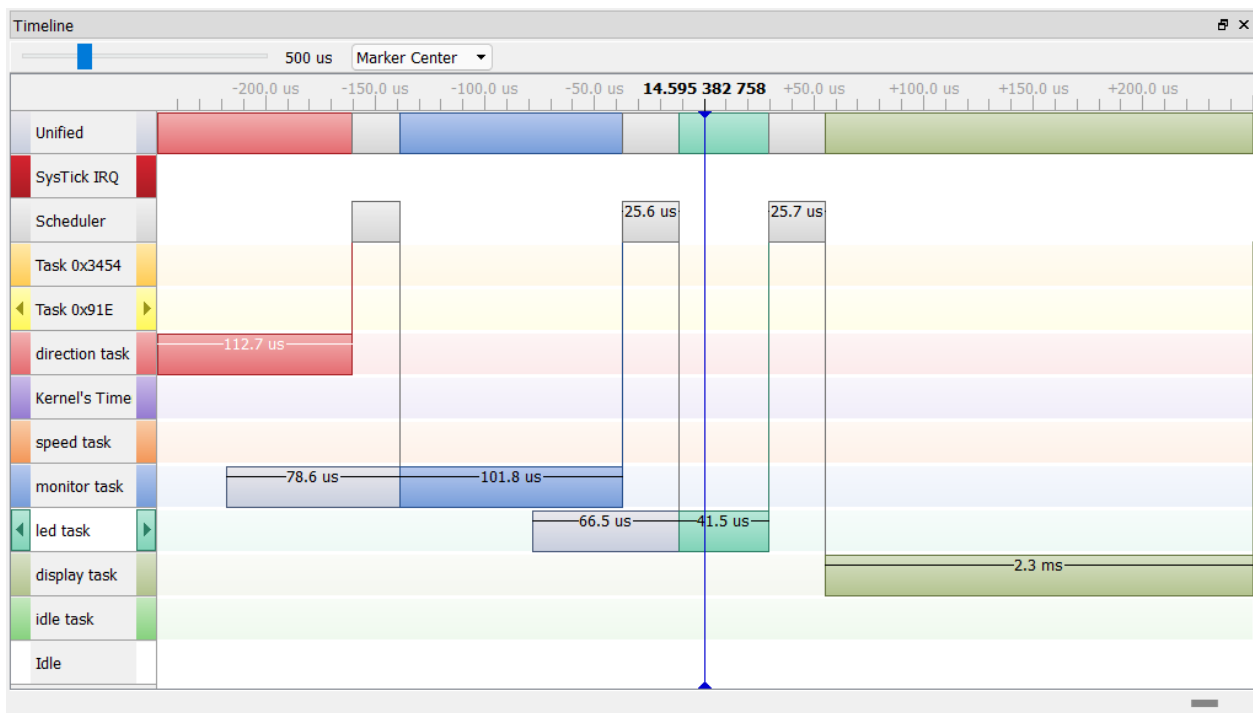


Fig. Direction Task

Q: Check the scheduling of each task. Is each task scheduled as expected? Explain

Yes, as seen in above, the monitor task, led task and display task occur after the execution of direction task or speed task. It is due to the scheduling. Monitor task is pending the event flag from speed task and direction task. And the LED task is pending the event flag from the monitor. So, as long as those tasks occur in sequence, then they are scheduled as expected.























Contexts				
Name	Type	Stack Information	Activations	Total Blocked Time
 SysTick IRQ	 #15		0	
 Scheduler			514	
 Task 0x3454	 @0	0 @ 0x00000000	33	0.006 851 C
 speed task	 @5	1024 @ 0x2000E...	0	0.000 000 C
 direction task	 @5	1024 @ 0x2000F...	319	0.094 002 E
 Kernel's Timer ...	 @5	256 @ 0x2000B6...	33	0.001 552 E
 monitor task	 @10	1024 @ 0x20010...	64	0.005 085 E
 display task	 @20	1024 @ 0x20012...	15	0.000 124 C
 led task	 @20	1024 @ 0x20011...	64	0.004 262 E
 idle task	 @30	1024 @ 0x20013...	372	0.000 000 C
 Idle			0	

Fig. Priority

And speed task and direction task have the highest priority, followed by the monitor task, display task and led task.