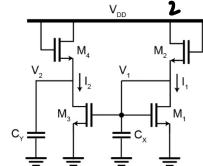




1. NMOS Bias Circuit Amplifier [20 pts]

Consider the circuit shown below. For all calculations, assume $k_n' = 125 \mu A/V^2$, $V_t = 0.3 V$, $W/L = 8$, $V_{DD} = 2 V$, $C_x = 250 \text{ pF}$, $C_V = 50 \text{ pF}$. Neglect channel length modulation, body effect, and all transistor capacitances.

- Calculate V_1 , I_1 , V_2 and I_2 .
- Re-calculate V_2 and I_2 with $(W/L)_{M4} = 20$.
- Calculate the zero-value time constants for C_x and C_V . Use $(W/L)_{M3,4} = 20$.
- Which time constant limits the bandwidth of the circuit?
- Briefly qualitatively explain how your answers for (a)-(d) would change (decrease, no change, or increase) if you accounted for the body effect. (Hint: Consider where the bulk must be tied for all transistors.)



$$k_n' = 125 \frac{\mu A}{V^2}$$

$$V_t = 0.3 V$$

$$\frac{W}{L} = 8$$

$$V_{DD} = 2$$

$$C_x = 250 \text{ pF} \quad C_V = 50 \text{ pF}$$

a)

M_1 , M_2 , M_4 are diode connected transistors. (Sat)
 C_V and C_x are open circuit

$$M_4: I_2 = \frac{1}{2} k_n' \frac{w}{l} \cdot (V_{DD} - V_2 - V_t)^2 \quad (1) \quad V_{DD} - V_2 - V_t = V_1 - V_t$$

Assuming M_3 in Sat region

$$I_2 = \frac{1}{2} k_n' \frac{w}{l} (V_1 - V_t)^2 \quad (2)$$

$$V_{DS} = V_2 > V_1 - V_t$$

$$M_2: I_1 = \frac{1}{2} k_n' \frac{w}{l} \cdot (V_{DD} - V_1 - V_t)^2 \quad (3) \quad 1.7 - V_1 = V_1 - 0.3$$

$$M_1: I_1 = \frac{1}{2} k_n' \frac{w}{l} \cdot (V_1 - V_t)^2 \quad (4) \quad V_1 = 1$$

$$(3) = (4)$$

$$V_{DD} - V_1 - V_t = V_1 - V_t$$

$$I_1 = \frac{1}{2} \cdot 125 \mu A \cdot 8 \cdot 0.7^2$$

$$V_{DD} = 2V_1$$

$$V_1 = 1 V$$

$$= 245 \mu A$$

$$(1) = (2)$$

$$V_{DD} - V_2 - V_t = 1 - 0.3$$

$$2 - V_2 = 1$$

$$V_2 = 1 V$$

Then $(2) = (4)$

$$I_2 = I_1 = 245 \mu A$$

$$V_2 = 1 V$$

For M_3

$$V_{DS} = V_2 - 0 = 1 V$$

$$V_{GS} = 1 V$$

$$V_{DS} > V_{GS} - V_t = 0.7$$

so M_3 in Sat

b)

Assume M_3 in Sat

$$M_3: I_2 = \frac{1}{2} k_n' \cdot 20 \cdot (V_t - V_2)^2$$

$$= \frac{1}{2} \cdot 12.5 \mu A \cdot 20 \cdot (1.7 - 0.7)^2$$

$$= 612.5 \mu A$$

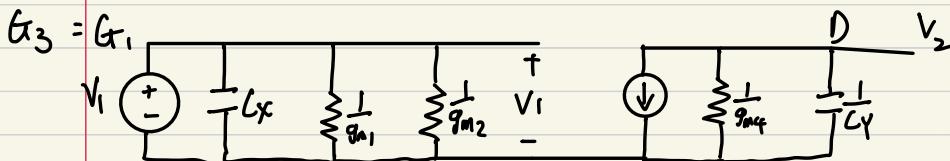
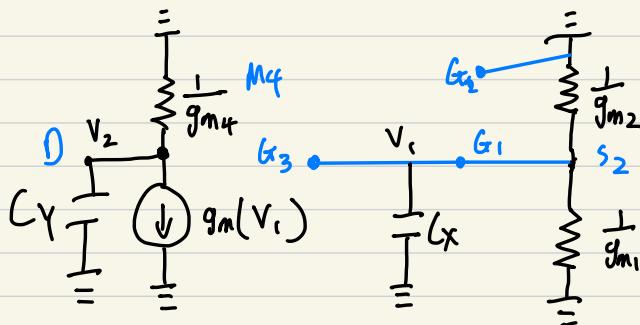
$$M_4: I_2 = \frac{1}{2} k_n' \cdot 20 \cdot (V_{DD} - V_2 - V_t)^2$$

$$612.5 \mu A = \frac{1}{2} \cdot 12.5 \mu A \cdot 20 \cdot (1.7 - V_2)^2$$

$$\sqrt{0.49} = 1.7 - V_2$$

$$V_2 = 1 V$$

c)



$$I_X = \left(\frac{1}{g_{m_1} + g_{m_2}} \right) \cdot I_X$$

$$I_Y = \frac{1}{g_{m_4}} \cdot I_Y =$$

$$g_{m_2} = g_{m_1} = k'n \left(\frac{w}{\sum_{i=1}^n w_i} \right) V_{OV} = 125u \cdot 8 \cdot 0.7 = 700u \frac{A}{V}$$

$$g_{m3} = g_{m4} = k' \ln \left(\frac{w}{c} \right)_{(3,4)} Vov = 125 u \cdot 20 \cdot 0.7 = 1750 u$$

$$T_x = \frac{1}{g_{m_1} + g_{m_2}} \cdot C_x = \frac{g_{m_1} \cdot g_{m_2}}{g_{m_2} + g_{m_1}} \cdot C_x \geq 714 \cdot 250 \mu s = 1.786 \cdot 10^{-7} s$$

$$T_Y = \frac{1}{q_{mY}} \cdot C_Y = \frac{1}{1750\mu} \cdot 50\mu = 28.6 \text{ n s}$$

$$d) \quad \frac{1}{T_y} = 35 \text{ MHz} \quad \frac{1}{T_x} = 5.6 \text{ MHz}$$

So I_x limits the bandwidth of the circuit

e) a) b) Body effect $\uparrow V_t$, \rightarrow I₁ and I₂ ↓
 ↳ increase Threshold.

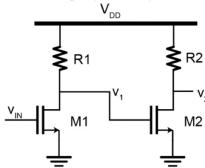
c) Body effect $\uparrow V_t \rightarrow g_m \downarrow$
 \downarrow
 $I_x, I_y \uparrow$

d) One of the pole is dominant,
so no change

2. NMOS Cascade Amplifier [20 pts]

Consider (again) the circuit shown below. Assume that (magically) this circuit is biased with V_{IN} and V_t such that node V_2 is at $V_{DD}/2$. Further, assume that M_1 and M_2 have the same W/L , bias current, negligible channel length modulation, and are in saturation.

- Symbolically derive an expression for R_2 with respect to V_{IN} satisfying the above conditions.
- Draw the small-signal model of this circuit. Include only C_{GS} and C_{GD} capacitors.
- Using only KCL/KVL, derive an expression for the transfer function $v_2(s)/v_{in}(s)$. Simplify your expression as much as possible and pull out the gain term, which has no frequency dependency.
- Briefly explain why node V_{IN} will likely not matter in ZVTC calculations.
- Using ZVTCs, derive an expression to estimate the dominant pole.
- Instead of using ZVTCs, estimate the bandwidth using Miller's Theorem. (Hint: Consider where the dominant pole is in this circuit based on the driving impedance.)



$R_2(V_{IN})$

$$M_1 : I_D = \frac{1}{2} k_n' \frac{w}{l} (V_{GS} - V_t)^2 \\ = \frac{1}{2} k_n' \frac{w}{l} (V_{IN} - V_t)^2$$

$$M_2 : I_D = \frac{1}{2} k_n' \frac{w}{l} (V_1 - V_t)^2$$

$$V_2 = V_{DD} - I \cdot R_2 \Rightarrow IR_2 = \frac{V_{DD}}{2} \\ I = \frac{V_{DD}}{2R_2}$$

$$\frac{V_{DD}}{R_2} = \frac{1}{2} k_n' \frac{w}{l} (V_{IN} - V_t)^2$$

$$R_2 = \frac{V_{DD}}{2 \cdot \frac{1}{2} k_n' \frac{w}{l} (V_{IN} - V_t)^2}$$

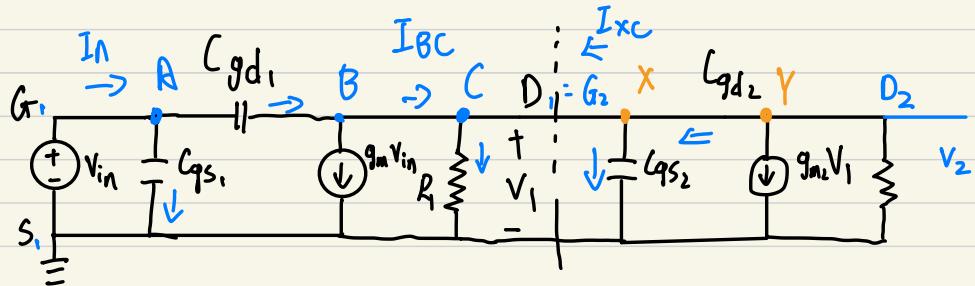
$$V_2 = V_{DD}/2$$

$$V_2 = V_{DD} - I \cdot R_2$$

$$IR_2 = \frac{V_{DD}}{2}$$

$$R_2 = \frac{V_{DD}}{2I}$$

b)



$$c) \frac{V_2(s)}{V_{in}(s)} = \frac{V_1(s)}{V_{in}(s)} \cdot \frac{V_2(s)}{V_1(s)}$$

① ② A

$$I_n - sQ_{gs1}V_{in} - sQ_{gd1}(V_{in} - V_1) = 0$$

③ ④ B

$$sQ_{gd1}(V_{in} - V_1) - g_m V_{in} = I_{BC}$$

⑤ ⑥ C

$$I_{BL} - \frac{V_1}{R_1} + I_{XC}$$

⑦ ⑧ D

$$-sQ_{gs2}V_{in} + sQ_{gd2}(V_2 - V_1) = I_{XC}$$

⑨ ⑩ Y

$$-sQ_{gd2}(V_2 - V_1) - g_{m2}V_1 = \frac{V_2}{R_2}$$

Solve ③

$$-R_2 s \left(g_{d_2} (V_2 - V_1) \right) - g_{m_2} R_2 V_1 = V_2$$

$$-R_2 s \left(g_{d_2} V_2 + R_2 s \left(g_{d_2} V_1 - g_{m_2} R_2 V_1 \right) \right) = V_2$$

$$V_1 \left(R_2 s \left(g_{d_2} - g_{m_2} R_2 \right) \right) = V_2 \left(1 + R_2 s \left(g_{d_2} \right) \right)$$

$$V_2 = \frac{V_1 \left(R_2 s \left(g_{d_2} - g_{m_2} R_2 \right) \right)}{\left(1 + R_2 s \left(g_{d_2} \right) \right)}$$

Solve ②

$$\left(s \left(g_{d_1} (V_{in} - V_1) \right) - g_{m_1} V_{in} \right) - \frac{V_1}{R_1} + \left(-s \left(g_{s_2} \cdot V_{in} + s \left(g_{d_2} V_2 - s \left(g_{d_2} V_1 \right) \right) \right) \right)$$

$$\underline{s \left(g_{d_1} V_{in} \right)} - \underline{s \left(g_{d_1} V_1 \right)} - \underline{g_{m_1} V_{in}} - \underline{\frac{V_1}{R_1}} - \underline{s \left(g_{s_2} V_{in} \right)} + \underline{s \left(g_{d_2} V_2 \right)} - \underline{s \left(g_{d_2} V_1 \right)}$$

$$V_{in} : V_{in} \left(s \left(g_{d_1} - g_{m_1} - s \left(g_{s_2} \right) \right) + \right.$$

$$V_1 : V_1 \left(-s \left(g_{d_1} - \frac{1}{R_1} \right) + s \left(g_{d_2} \cdot \frac{\left(R_2 s \left(g_{d_2} - g_{m_2} R_2 \right) \right)}{\left(1 + R_2 s \left(g_{d_2} \right) \right)} - s \left(g_{d_2} \right) \right) = 0$$

$$V_{in} \left(s \left(g_{d_1} - g_{m_1} - s \left(g_{s_2} \right) \right) = V_1 \left(s \left(g_{d_1} + \frac{1}{R_1} - s \left(g_{d_2} \frac{R_2 s \left(g_{d_2} - g_{m_2} R_2 \right)}{1 + R_2 s \left(g_{d_2} \right)} + s \left(g_{d_2} \right) \right) \right)$$

$$\frac{V_1}{V_{in}} = \frac{s \left(g_{d_1} - g_{m_1} - s \left(g_{s_2} \right) \right)}{s \left(g_{d_1} + \frac{1}{R_1} - s \left(g_{d_2} \cdot \frac{R_2 s \left(g_{d_2} - g_{m_2} R_2 \right)}{1 + R_2 s \left(g_{d_2} \right)} + s \left(g_{d_2} \right) \right)}$$

$$\frac{V_2(s)}{V_{in}(s)} = \frac{V_2}{V_1} \cdot \frac{V_1}{V_{in}}$$

$$\frac{R_2 s L_{gd_2} - g_{m_2} R_2}{1 + R_2 s L_{gd_2}} \cdot \frac{s(L_{gd_1} - g_{m_1} - sL_{gs_2})}{s(L_{gd_1} + \frac{1}{R_1} - sL_{gd_2} \cdot \frac{R_2 s L_{gd_2} - g_{m_2} R_2}{1 + R_2 s L_{gd_2}}) + sL_{gd_2}}$$

Denominator $(1 + R_2 s L_{gd_2})(s(L_{gd_1} + \frac{1}{R_1}) + sL_{gd_2}) - sL_{gd_2} \cdot (R_2 s L_{gd_2} - g_{m_2} R_2)$

$$\begin{aligned} & s(L_{gd_1} + \frac{1}{R_1} + sL_{gd_2} + s^2 R_2 L_{gd_2} L_{gd_1} + \frac{s R_1 L_{gd_2}}{R_1} + s^2 R_2^2 L_{gd_2} \\ & \quad - s^2 R_2^2 L_{gd_2}^2 + s g_{m_2} R_2 L_{gd_2}) \\ &= s(L_{gd_1} + L_{gd_2} + \frac{R_2(L_{gd_2})}{R_1} + g_{m_2} R_2 L_{gd_2}) + \frac{1}{R_1} + s^2 R_2 L_{gd_2} L_{gd_1} \end{aligned}$$

Numerator $(R_2 s L_{gd_2} - g_{m_2} R_2) \cdot (s(L_{gd_1} - g_{m_1} - sL_{gs_2})$

$$\begin{aligned} &= s^2 R_2 L_{gd_1} L_{gd_2} - s g_{m_2} R_2 L_{gd_1} - s g_{m_1} R_2 L_{gd_2} + g_{m_1} g_{m_2} R_2 + s g_{m_2} R_2 L_{gs_2} \\ &= s^2 (R_2 L_{gd_1} L_{gd_2}) + s(g_{m_2} R_2 L_{gs_2} - g_{m_2} R_2 L_{gd_1} - g_{m_1} R_2 L_{gd_2}) \\ & \quad + g_{m_1 m_2} R_2 \end{aligned}$$

$$\frac{V_L}{V_{in}} = \frac{g_{m_1 m_2} R_2 + s(g_{m_2} R_2 L_{gs_2} - g_{m_2} R_2 L_{gd_1} - g_{m_1} R_2 L_{gd_2}) + s^2 (R_2 L_{gd_1} L_{gd_2})}{s(L_{gd_1} + L_{gd_2} + \frac{R_2(L_{gd_2})}{R_1} + g_{m_2} R_2 L_{gd_2}) + \frac{1}{R_1} + s^2 R_2 L_{gd_2} L_{gd_1}}$$

Multiply $\frac{R_1}{R_1}$

$$\frac{V_L}{V_{in}} = \frac{g_m R_1 R_2 + s(g_m R_1 R_2 C_{gs2} - g_m R_1 R_2 C_{gd1} - g_m R_1 R_2 C_{gd2}) + s^2 (R_1 R_2 C_{gd1} C_{gd2})}{1 + s(R_1 C_{gd1} + R_1 C_{gd2} + R_2 C_{gd2} + g_m R_1 R_2 C_{gd2}) + s^2 R_1 R_2 C_{gd2} C_{gd1}}$$

$$If \quad g_{m1} = g_{m2} \quad C_{ds1} = C_{ds2} \quad C_{gs1} = C_{gs2}$$

$$\text{Numerator} = g_m^2 R_1 R_2 + s(g_m R_1 R_2 C_{gs} - g_m R_1 R_2 C_{gd} - g_m R_1 R_2 C_{gd}) + s^2 (R_1 R_2 C_{gd}^2)$$

$$= g_m^2 R_1 R_2 \left(1 + s \left(\frac{C_{gs}}{g_m} - 2 \frac{C_{gd}}{g_m} \right) + s^2 \frac{C_{gd}^2}{g_m^2} \right)$$

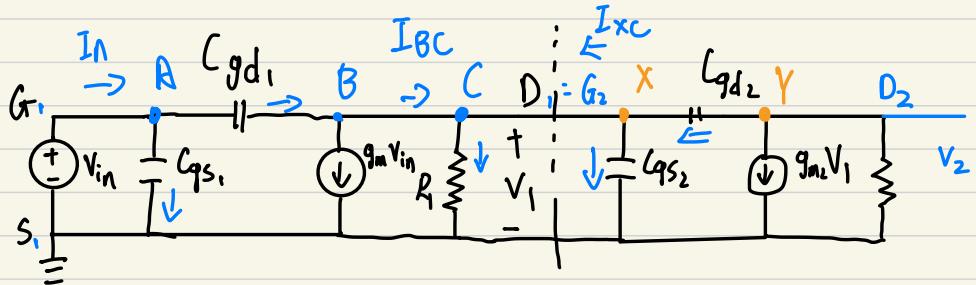
$$\text{Denominator} \quad 1 + s(2R_1 C_{gd} + R_2 C_{gd} + g_m R_1 R_2 C_{gd}) + s^2 R_1 R_2 C_{gd}^2$$

$$\frac{V_{2(s)}}{V(s)} = \frac{g_m^2 R_1 R_2 \left(1 + s \left(\frac{C_{gs}}{g_m} - 2 \frac{C_{gd}}{g_m} \right) + s^2 \frac{C_{gd}^2}{g_m^2} \right)}{1 + s(2R_1 C_{gd} + R_2 C_{gd} + g_m R_1 R_2 C_{gd}) + s^2 R_1 R_2 C_{gd}^2}$$

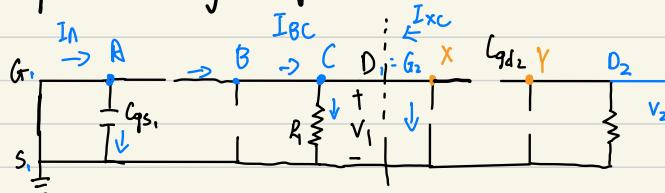
$g_m^2 R_1 R_2$ is the gain

d) In the ZVTC, V_{in} is grounded, which means it won't affect the time constant $b_1 = \sum T_i$

e)

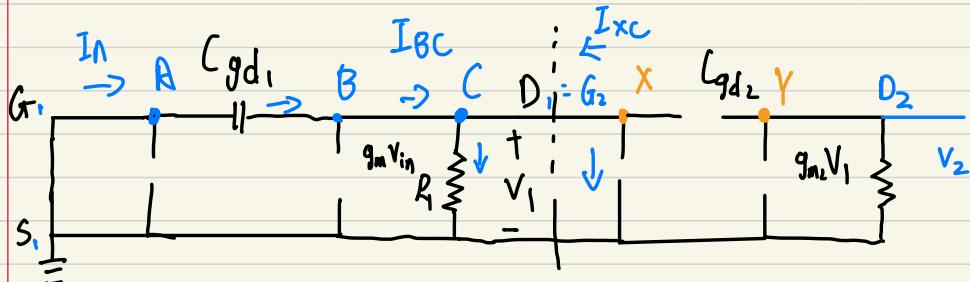


Step 1) Find T_{qsi}



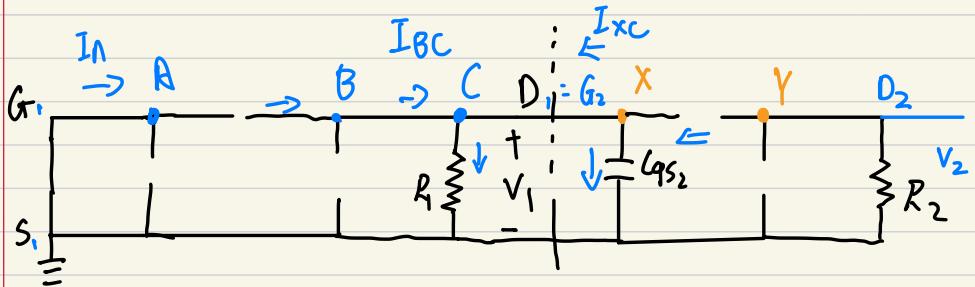
$$T_{qsi} = 0$$

Step 2) Find T_{qd}



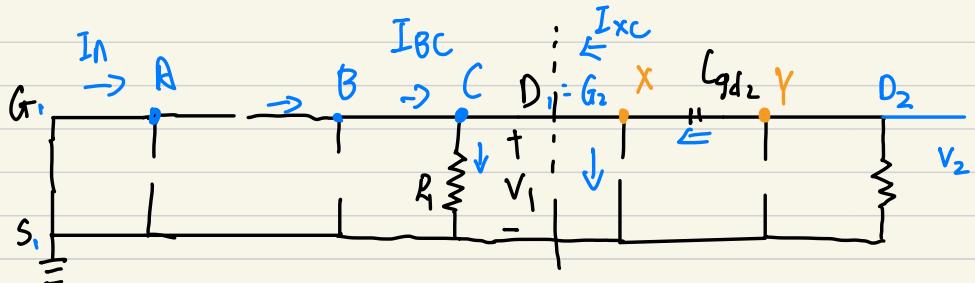
$$T_{qd} = C_{gd1} \cdot R_1$$

Step 3) Find C_{qs_2}



$$I_{Cqs_2} = R_1 C_{qs_2}$$

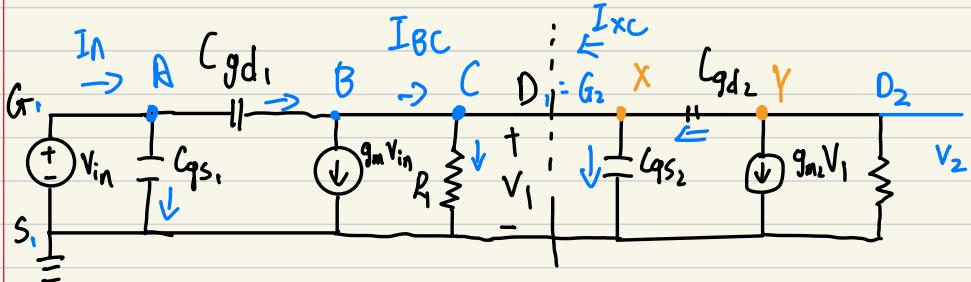
Step 4) Find I_{Cqd_2}



$$I_{Cqd_2} = (R_1 + R_2) C_{qd_2}$$

$$\begin{aligned} b_1 &= T_{qs_1} + T_{gd_1} + T_{qs_2} + T_{gd_2} \\ &= D + L_{gd_1} \cdot R_1 + L_{qs_2} \cdot R_1 + (R_1 + R_2) L_{gd_2} \\ &= R_1 L_{gd_1} + R_1 L_{qs_2} + (R_1 + R_2) L_{gd_2} \end{aligned}$$

t)



$$k_1(s)$$

$$\frac{V_1(s)}{V_{gs}(s)} = \frac{\frac{1}{R_1} + \frac{C_{gs}}{s} + \frac{C_{gd}[1-k_1(s)]}{s}}{\frac{1}{R_1} + \frac{C_{gd}[1-\frac{1}{k_1(s)}]}{s}}$$

$$k_1(s) = \frac{V_1(s)}{V_{gs}(s)}$$

$$KCL @ B: s(C_{gd1}(V_{gs} - V_1)) - g_m V_{gs} = I_{BC}$$

$$KCL @ C$$

$$I_{BC} - \frac{V_1}{R_1} = 0$$

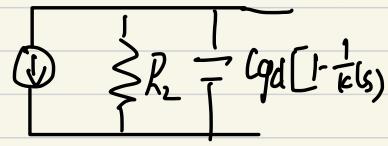
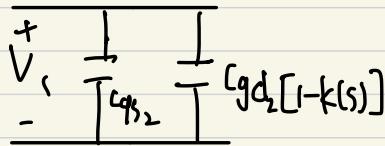
$$s(C_{gd1}V_{gs} - s(C_{gd1}V_1 - g_m V_{gs}) = \frac{V_1}{R_1}$$

$$V_{gs} (s(C_{gd1} - g_m) R_1 = V_1 (1 + sR_1 C_{gd1})$$

$$\frac{V_1(s)}{V_{gs}(s)} = \frac{-R_1 g_m + sR_1 C_{gd1}}{1 + sR_1 C_{gd1}}$$

$$k_1(s) = -R_1 g_m \left(\frac{1 - s \frac{C_{gd1}}{g_m}}{1 + sR_1 C_{gd1}} \right) \approx -g_m R_1$$

$$k_2(s) = \frac{V_2(s)}{V_1(s)}$$



KCL @ Y

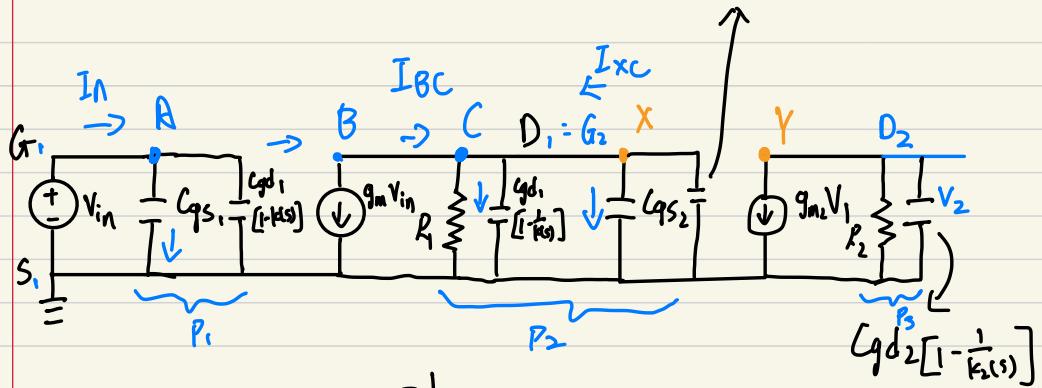
$$s(gpd_2(V_1 - V_2) - q_m V_1) = \frac{V_2}{R_2}$$

$$s(gpd_2 V_1 - s(gpd_2 V_2 - q_m V_1) = \frac{V_2}{R_2}$$

$$V_1 R_2 (s(gpd_2 - q_m)) = V_2 (1 + s R_2 gpd_2)$$

$$\frac{V_2}{V_1} = \frac{-q_m R_2 (1 - \frac{s(gpd_2)}{q_m})}{1 + s R_2 gpd_2} \approx -q_m R_2$$

$$k_1(s) = -g_m R_1 \quad k_2(s) = -g_m R_2 \quad C_{gd2} [1 - k_2(s)]$$



$$P_1 = D = \frac{D \cdot (C_{qs1} + C_{gd1} [1 - k_1(s)])}{-1}$$

$$P_2 = R_1 \left(C_{qd1} [1 - \frac{1}{k_1(s)}] + C_{gs2} + C_{gd2} [1 - k_2(s)] \right)$$

$$P_3 = \frac{-1}{R_2 \left(C_{gd2} [1 - \frac{1}{k_2(s)}] \right)} = \frac{-1}{R_2 (C_{gd2} (1 + \frac{1}{g_m R_2}))}$$

P_2 will be the dominant pole

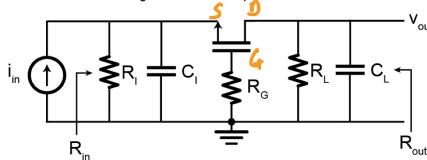
$$P_2 = \frac{-1}{R_1 \left(C_{qd1} \left(1 + \frac{1}{g_m R_1} \right) + C_{gs2} + C_{gd2} \left(1 + g_m R_2 \right) \right)}$$

Q3

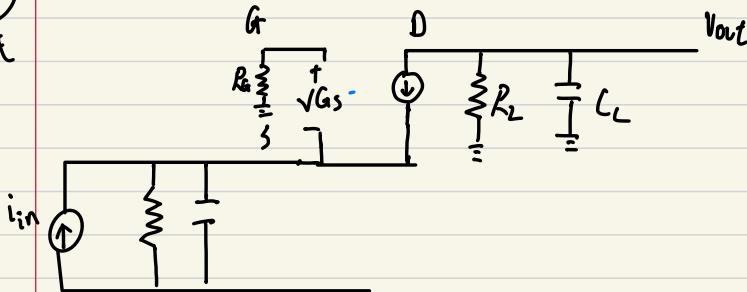
3. Transimpedance Amplifier [20 points]

Consider the circuit shown below. For all calculations, use $g_m = 4 \text{ mS}$, $C_{gs} = 120 \text{ fF}$, $R_i = 10 \text{ k}\Omega$, $C_i = 20 \text{ fF}$, $R_o = 100 \text{ }\Omega$, $C_L = 150 \text{ fF}$, and $R_L = 5 \text{ k}\Omega$. Neglect the body effect, channel length modulation, and all other capacitances. Assume that the transistor is in saturation.

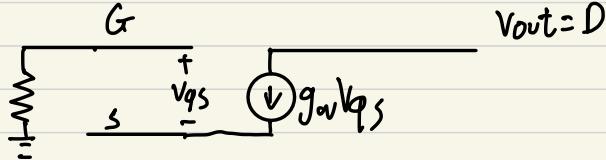
- Draw the simplified small-signal equivalent circuit, including the small-signal model of the transistor.
- Calculate the low-frequency transimpedance gain (V_{out}/i_{in}). (Hint: Neglect capacitive effects.)
- Calculate the low-frequency input impedance, R_{in} .
- Calculate the low-frequency output impedance, R_{out} .
- Estimate the -3dB bandwidth of the gain. Write out expressions for each of the time constants.



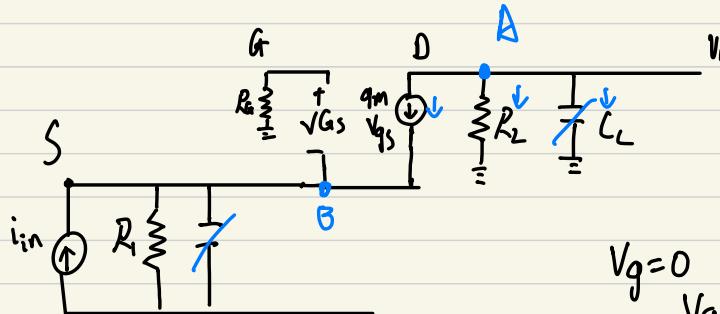
a)
Circuit



Transistor



b)



$$V_g = 0 \\ V_{gs} = -V_s$$

KCL @ A

$$-g_m V_{gs} - \frac{V_{out}}{R_L} = 0 \Rightarrow V_{out} = -g_m V_{gs} R_L$$

KCL @ B

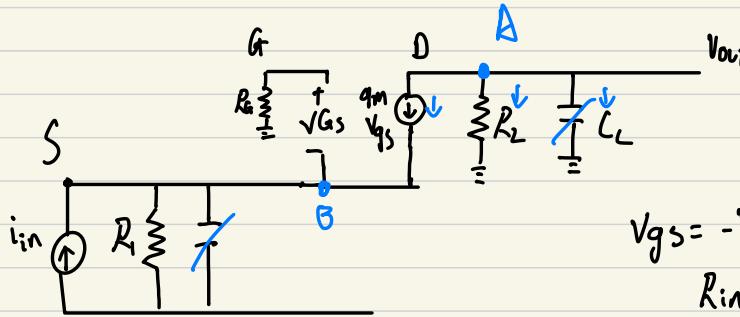
$$i_{in} + g_m V_{gs} = \frac{V_s}{R_I}$$

$$i_{in} g_m (-V_s) = V_s \\ i_{in} R_I + g_m V_s \cdot R_I = V_s \Rightarrow i_{in} = \frac{V_s (1 + g_m R_I)}{R_I}$$

$$\frac{V_{out}}{i_{in}} = \frac{-g_m - V_s \cdot R_L \cdot R_I}{V_s (1 + g_m R_I)} = \frac{R_L R_I g_m}{k g_m R_I} =$$

$$4878 \quad \frac{V}{A}$$

c)



$$V_{gs} = -V_s$$

$$R_{in} = \frac{V_{in}}{i_{in}}$$

$$V_{in} = (i_{in} + g_m V_{gs}) - R_I = V_s$$

$$= R_I i_{in} + g_m V_s - R_I = V_s$$

$$R_{in} i_{in} = V_s (1 + g_m R_I)$$

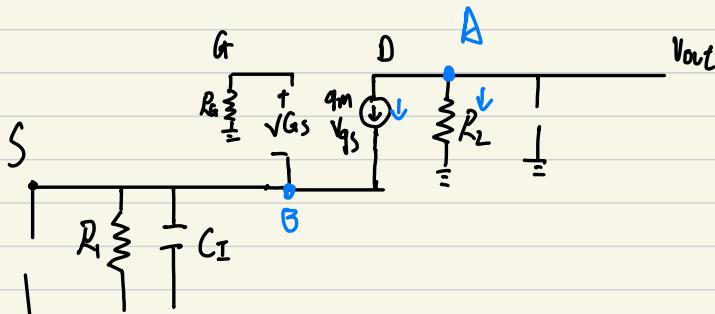
$$R_{in} = \frac{V_{in}}{i_{in}} = \frac{R_I}{1 + g_m R_I} = 243.9 \Omega$$

d) $R_o = \frac{V_{out}}{i_{out}}$ $i_{out} = -g_m V_{qs}$

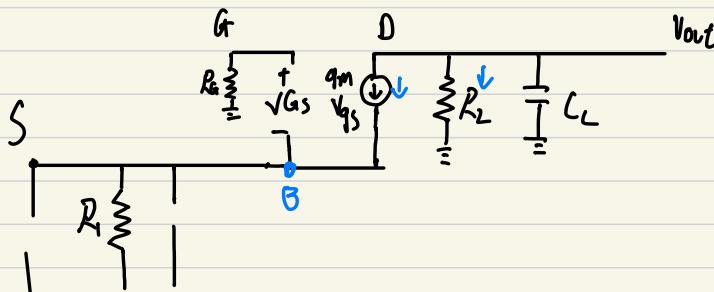
$$V_{out} = -g_m V_{qs} R_L$$

$$R_o = \frac{-g_m V_{qs} R_L}{-g_m V_{qs}} = 5 k\Omega$$

e)

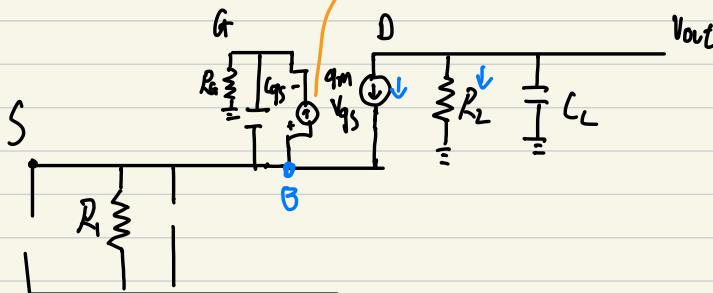
step 1) Find T_{CI}

$$T_{CI} = R_{in} \cdot C_I = 2 \cdot 10^{-10} \text{ s}$$

step 2) Find T_{CL} 

$$T_{CL} = R_L \cdot C_L = 7.5 \cdot 10^{-10} \text{ s}$$

Step 3 find C_{GS} V_t



KCL @ B

$$-i_t - \frac{V_s}{R_i} + g_m V_t = 0$$

$$V_{GS} = V_t = i_t \cdot R_G - V_s$$

$$V_s = i_t \cdot R_G - V_t$$

$$g_m V_t = \frac{i_t R_G - V_t}{R_i} + i_t$$

$$g_m R_i V_t = i_t R_G + i_t R_i - V_t$$

$$V_t (1 + g_m R_i) = i_t (R_i + R_G)$$

$$\frac{V_t}{i_t} = \frac{R_i + R_G}{1 + g_m R_i} = R_t$$

$$T_{C_{GS}} = \frac{R_i + R_G}{1 + g_m R_i} \cdot C_{GS} = 2.956 \cdot 10^{-11} \text{ s}$$

Step 4) find $f_{W_{-3dB}}$

$$f_{-3dB} = \left| \frac{1}{b_1} \right| = \frac{1}{T_{CQS} + T_{Lc} + T_{Cl}} = 1.02 \text{ GHz}$$

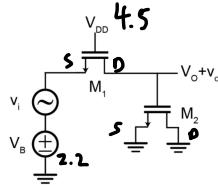
$$w_3 = \frac{1}{2\pi f} = 0.16 \text{ GHz}$$

Q4

4. All NMOS Filter [10 pts]

Consider the circuit shown below. Use $W_1 = 15 \mu\text{m}$, $L_1 = 5 \mu\text{m}$, $W_2 = 12 \mu\text{m}$, $L_2 = 15 \mu\text{m}$, $V_B = 2.2 \text{ V}$, $V_0 = 350 \text{ mV}$, $V_{DD} = 4.5 \text{ V}$, $\lambda = 0.1 \text{ 1/V}$, $k'_n = 120 \mu\text{A/V}^2$, and $C_{ox} = 4 \text{ fF}/\mu\text{m}^2$.

- Analyze the DC operating point of this circuit (all node voltages, branch currents, and region of operation for each transistor.)
- Sketch the small-signal model, including capacitors. Neglect all extrinsic capacitors.
- Calculate the transfer function $V_o(s)/V_i(s)$.
- Sketch the magnitude and phase of the transfer function.
- Briefly, qualitatively explain what happens if V_B is increased.



$$V_0 = 350 \text{ mV} \quad \lambda = 0.1 \\ V_i = 0 \quad k'n't = 120 \mu\text{A} \\ V_o = 0 \quad C_{ox} = 4 \text{ fF}$$

$$M_1 \frac{W}{L} = 3$$

$$M_2 \frac{W}{L} = \frac{12}{15} = \frac{4}{5}$$

a)

$$M_1: V_{GS} = V_{DD} - V_B = 4.5 - 2.2 = 2.3 \text{ V} \\ V_{DS} = V_{out} - V_B$$

$$V_{OV} = 2.3 - 350 \text{ mV} \\ = 1.95 \text{ V}$$

Assuming M_1 in Saturation

$$I_{D1} = \frac{1}{2} k'n' \left(\frac{W}{L} \right)_1 \cdot V_{OV}^2 \cdot (1 + \lambda V_{DS})$$

$$M_2: V_{DS} = 0$$

$$V_{GS} = V_{out}$$

$$V_{OV} = V_{out} - V_t$$

$$V_{DS} \leq V_{OV}$$

Assuming M_2 in Triode region

$$I_{D2} = k'n' \left(\frac{W}{L} \right)_2 (V_{DS} V_{OV} - \frac{1}{2} V_{DS}^2) = 0$$

And $I_{D1} = I_{D2} = 0 \text{ A}$

So M_1 is in cutoff or Triode region

$$I_{D1} = k'n' \left(\frac{W}{L} \right)_1 (V_{DS} V_{OV} - \frac{1}{2} V_{DS}^2) = 0$$

$$V_{DS} = 0 = V_{out} - V_B$$

$$V_{out} = V_B = 2.2 \text{ V}$$

$$M_2: V_{DS} = 2.2 - 350mV = 1.85V$$

$$V_{DS} < V_{OV} \quad \& \quad V_{OV} > 0$$

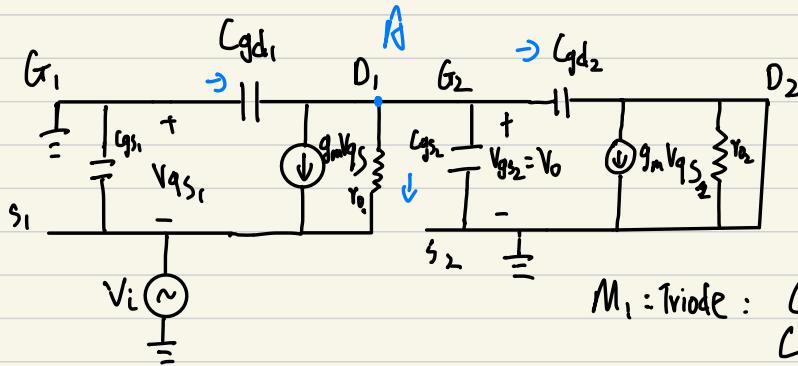
so M_2 is in Triode Region

$$M_1: (V_{DS} = 0) < (V_{OV} = 1.95V)$$

$$V_{DS} < V_{OV} \quad \& \quad V_{OV} > 0$$

so M_1 is in Triode Region

b)



$$M_1: \text{Triode : } C_{gd1} = \frac{1}{2}WL\text{Cox}$$

$$C_{gs1} = \frac{1}{2}WL\text{Cox}$$

$$V_{gs1} = D - V_i = -V_i$$

$$M_2: \text{Triode : } C_{gd2} = \frac{1}{2}WL\text{Cox}$$

c)

@ A

$$s C_{gd1} (D - V_o) + g_m V_i - \frac{V_o}{r_o} - s C_{gs2} V_o - s C_{gd2} (V_o - D) = 0$$

$$-s C_{gd1} V_o + g_m V_i - \frac{V_o}{r_o} - s C_{gs2} V_o - s C_{gd2} V_o = 0$$

$$\underline{-s r_o C_{gd1} V_o} + g_m V_i - \underline{V_o} - \underline{s r_o C_{gs2} V_o} - \underline{s r_o C_{gd2} V_o} = 0$$

$$g_m V_i = V_o (1 + s r_o C_{gd1} + s r_o C_{gs2} + s r_o C_{gd2})$$

$$\frac{V_o}{V_i} = \frac{g_m}{1 + s r_o C_{gd1} + s r_o C_{gs2} + s r_o C_{gd2}}$$

$$= \frac{g_m}{1 + \underbrace{s r_o (C_{gd1} + C_{gs2} + C_{gd2})}_{P_1}}$$

$$M_1: \text{Triode} : \begin{aligned} C_{gd_1} &= \frac{1}{2} W_1 L_{ox} = 150 \text{ fF} \\ C_{gs_1} &= \frac{1}{2} W_1 L_{ox} = 150 \text{ fF} \end{aligned}$$

$$M_2: \text{Triode} : \begin{aligned} C_{gd_2} &= \frac{1}{2} W_2 L_{ox} = 360 \text{ fF} \\ C_{gs_2} &= \frac{1}{2} W_2 L_{ox} = 360 \text{ fF} \end{aligned}$$

$$r_o = \frac{1}{g_{ds}}$$

$$\begin{aligned} g_{ds} &= \frac{dI_d}{dV_{ds}} \left(k'_n \frac{w}{L} \left(V_{ov} V_{ds} - \frac{1}{2} V_{ds}^2 \right) \right) \Big|_{V_{ds}} \\ &= \left(k'_n \frac{w}{L} (V_{ov} - V_{ds}) \right) \end{aligned}$$

$$\begin{aligned} g_{ds_1} &= k'_n \left(\frac{w}{L} \right)_1 (V_{ov_1} - V_{ds_1}) \\ &= 120 \mu \cdot 3 \cdot 1.95 = 762 \mu \end{aligned}$$

$$r_{o1} = 1424.5 \Omega$$

$$g_{ds_2} = k'_n \left(\frac{w}{L} \right)_2 (V_{ov} - V_{ds})$$

$$= 120 \mu \cdot \frac{12}{15} \cdot 1.85 = 177.6 \mu \quad r_{o2} = 5630.6 \Omega$$

$$\begin{aligned} g_m &= \frac{dI_d}{dV_{gs}} \left(k'_n \frac{w}{L} ((V_{gs} - V_t) V_{ds} - \frac{1}{2} V_{ds}^2) \right) \\ &= k'_n \frac{w}{L} (V_{ds} - \frac{1}{2} V_{ds}^2) = 0 \end{aligned}$$

due to
 $V_{ds} = 0$
in M_1, M_2

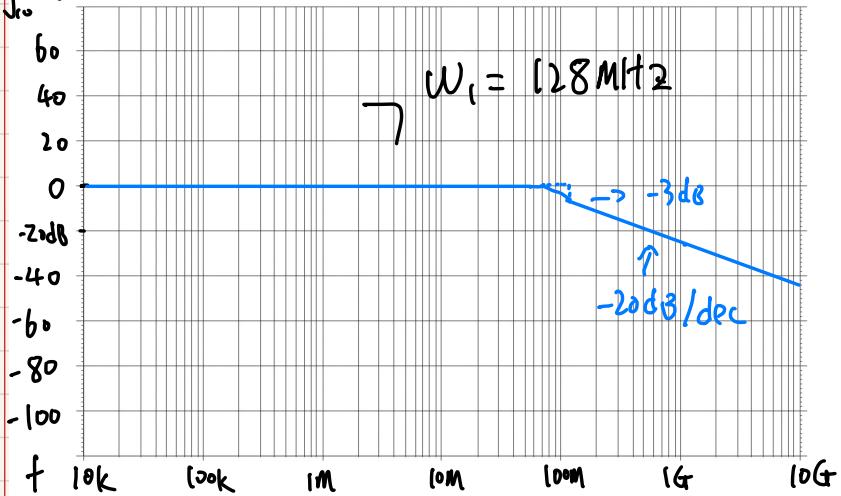
$$H(s) = \frac{g_m}{1 + s r_{o1} \underbrace{(C_{gd_1} + C_{gs_2} + C_{gd_2})}_{P_1}} \quad P_1 = \frac{1}{2\pi r_{o1} (C_{gd_1} + C_{gs_2} + C_{gd_2})}$$

$$\begin{aligned} &= 0.128 \text{ GHz} \\ &= 128 \text{ MHz} \end{aligned}$$

d

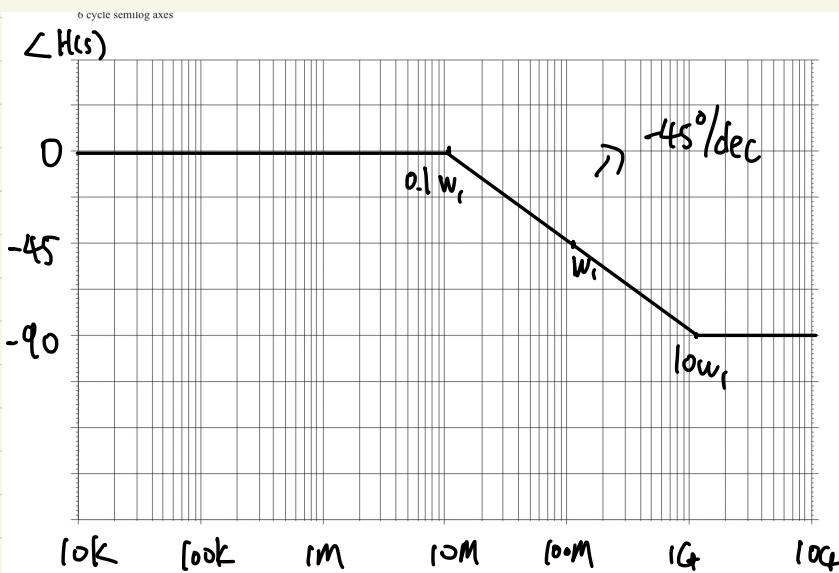
$$20 \log \left| \frac{V_o}{V_i} \right|$$

6 cycle semilog axes



$$\omega_c = 128 \text{ rad/s}$$

7

 $\rightarrow -3 \text{ dB}$ \uparrow
 -20 dB/dec 

c) From part a

$$V_{out} = V_B$$

① so $V_B \uparrow \rightarrow V_{out} \uparrow$

② $V_{GS} = V_{DD} - V_B$ $V_{ov} = (V_{DD} - V_B) - V_t$

$$\therefore V_B \uparrow \rightarrow V_{GS} \downarrow$$

if $V_B \uparrow\uparrow$ until $V_B = V_{DD} \rightarrow V_{ov} < 0$

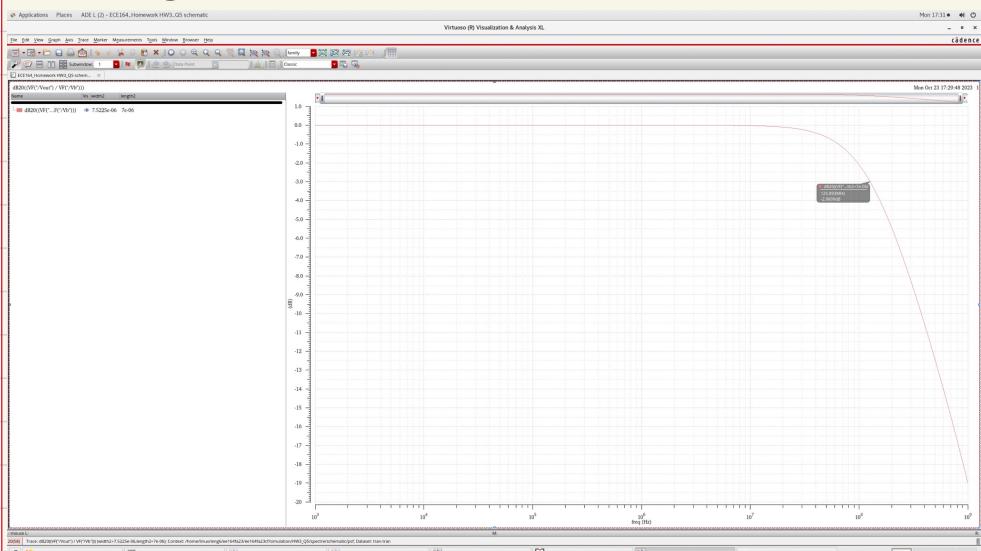
M₁ will be in cut-off region

5. Filter Design [30 pts]

You are off on your own! Redesign the circuit shown in question 4 for our technology. The specifications for this circuit are: $f_c = 125$ MHz and be tunable $\pm 15\%$ by changing V_b . (Remember, $V_{DD} = 1.8$ V). Submit a schematic annotated with the DC operating point and a plot of the AC response showing that the circuit can meet the tuning specification. Lastly, briefly describe your design methodology. (Hint: If you are unsure where to start, I would recommend connecting an NMOS device like the device connected in M_2 and sweeping the L to extract C_{ox} . It is probably a good idea to make the area large enough not to be determined by parasitic/extrinsic capacitance. From there, you can appropriately size M_1 .)

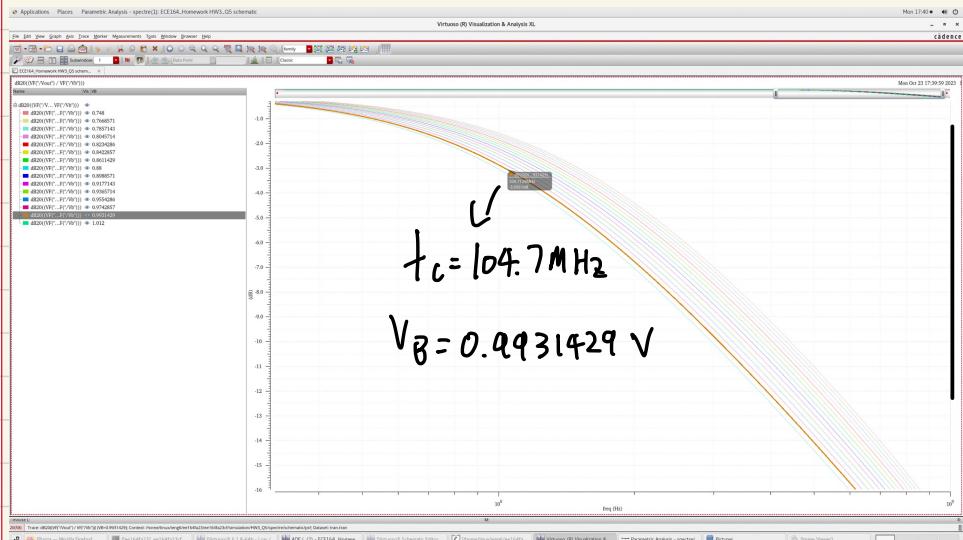
Q5

f_c

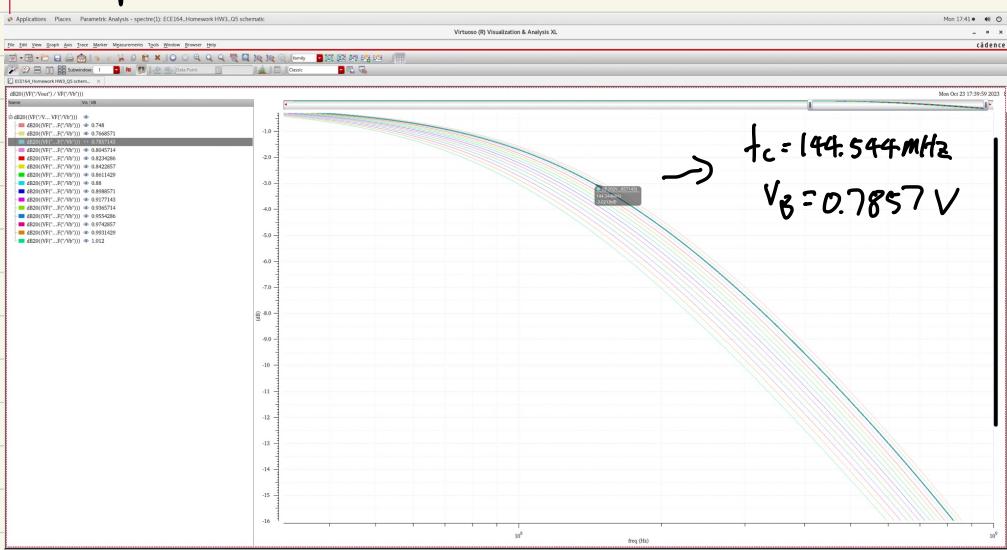


$$f_c = 125.893 \text{ MHz} \quad |H(s)| \approx -3 \text{ dB}$$

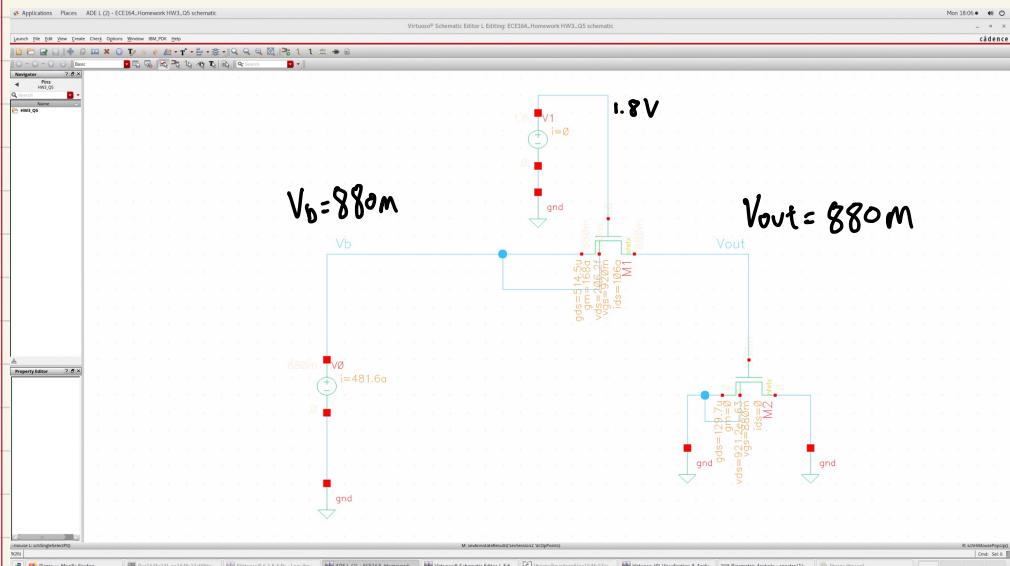
Lower Bound



Higher Bound



DC Operating Point



Setup

Applications Places

Launch Session Settings Analysis

27

Design Variables

length 1

WADSWELL

VB
VDO

infinity

87

1

Sizing

$$w_1 = 15 \text{ u}$$

$$W_2 = 7.522u$$

$$L_1 = 5u$$

$$L_2 = 7u$$

$$f_c = 125 \text{ MHz}$$

$$V_B = 0.88 V$$

$$V_{DD} = 1.8$$



Design Methodology

① Using $W_1 = 15\mu$ and $L_1 = 5\mu$ from Q4. keep them constant

② Calculate $r_o = \frac{1}{k'_n (L_1) (V_{DD} - V_B - V_t)}$ set
 k'_n and V_t derived from HW1, $V_B = 0.88V$
 $262.7 \frac{\mu A}{V^2}$ $0.307V$

③ $t_c = \frac{1}{2\pi r_o (C_{gd1} + C_{gs2} + C_{gd2})}$ $C_{gd1} = \frac{1}{2} W_1 L_1 C_{ox}$
 $C_{gs2} + C_{gd2} = W_2 L_2 C_{ox}$

④ Find C_{ox} by plotting C_{gg1} vs. L_1

$$C_{gg1} = W_1 \cdot \underbrace{L_1}_x \cdot C_{ox}$$

$$C_{gg1} = \underbrace{W_1 \cdot C_{ox}}_k \cdot L_1 = kx$$

After finding k , we know W_1 , then C_{ox} can be solved

$$C_{ox} = 7.56 \cdot 10^{-3}$$

⑤ Since we know γ_0 , $L_{gd_1} = \frac{1}{2} W_1 L_1 C_{ox}$
I assume $W_2 = L_2 = x$

so

$$125\text{MHz} = \frac{1}{2\pi \cdot \gamma_0 \cdot (L_{gd_1} + x^2 \cdot C_{ox})}$$
$$x = 9.09\mu$$

⑥ Then try to stimulate this
 $f_c = 125\text{MHz}$ is not found

⑦ keep adjusting W_2 and L_2 simultaneously
until find $f_c = 125\text{MHz} |H(s)| = -3\text{dB}$

⑧ Varying V_B to find upper and lower bound.
to get $\pm 15\%$ f_c