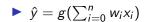
UO 631 Parallel Processing: Multi-layer Perceptron Classification

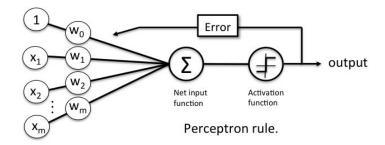
Luis Guzman & Steven Walton University of Oregon

December 3, 2020

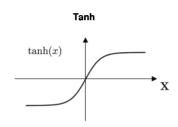
- ► What are Multi-Layer Perceptrons
- ► Gradient Descent
- Code Profiling
- CPU Parallelization
- GPU Parallelization
- Performance Results
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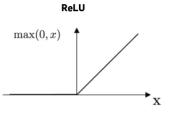
Perceptron



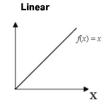


Activation Functions

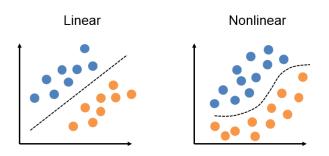




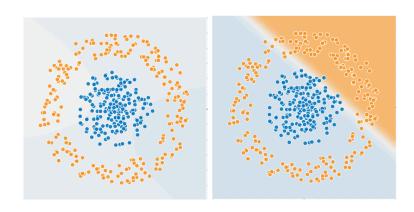
Sigmoid $\sigma(x) = \frac{1}{1+e^{-x}} \ \ \, \bigwedge_{\mathbf{X}}$



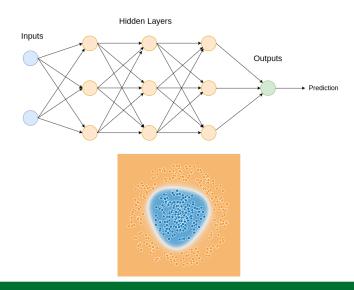
Decision Boundary



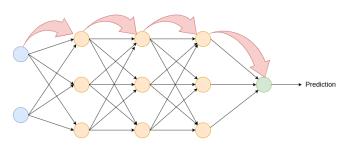
Limitations



Multi-Layer Perceptron



Making predictions: Forward pass



- $h_{1_{act}} = Inputs \cdot W_{h_1}$

- $\hat{y} = h_{3_{act}} \cdot W_O$
- ► How do we improve the predictions?

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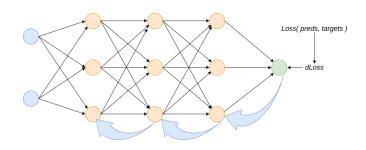
Gradient Descent

- Once a prediction has been made, find out how wrong its with a Loss function.
- Binary Cross Entropy Loss:

L =
$$y * log(\hat{y}) + (1 - y) * log(1 - \hat{y})$$

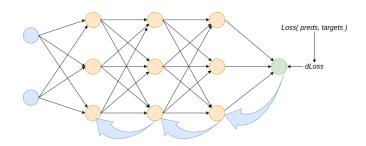
- Gradient descent step:
 - $\mathbf{v} = \mathbf{w} \gamma \frac{d}{d_w} \mathbf{L}$
 - $ightharpoonup \gamma$ learning rate parameter

Backward Pass: Back-propagating the error



- $ightharpoonup O_{error} = dLoss$
- $\blacktriangleright h_{2_{error}} = h_{3_{error}} \cdot (W_{h_3})^T$
- $h_{1_{error}} = h_{2_{error}} \cdot (W_{h_2})^T$

Backward Pass: Updating the weights



$$W_{h_1} = W_{h_1} - \gamma((Inputs)^T \cdot h_{1_{error}})$$

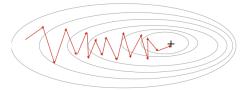
$$\qquad \qquad W_{h_2} = W_{h_2} - \gamma((h_{1_{act}})^T \cdot h_{2_{error}})$$

$$\qquad \qquad \mathbf{W}_{h_3} = \mathbf{W}_{h_3} - \gamma ((h_{2_{act}})^T \cdot h_{3_{error}})$$

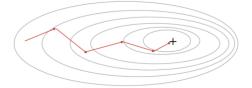
$$\blacktriangleright W_O = W_O - \gamma((h_{3_{act}})^T \cdot O_{error})$$

Stochastic GD vs (Mini) Batch GD

Stochastic Gradient Descent



Mini-Batch Gradient Descent



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Code Profiling

- Used GProf to profile code.
- Found slowest function we wrote was our matrix multiply.
- Found that allocating data took more time.

```
Fach sample counts as 0.01 seconds.
     cumulative
                   self
                                     self
                                              total
       seconds
                             calls
                                   ms/call ms/call name
                  seconds
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                     0.63 232252000
                                                      std::vector<std::vector<float, std::allocator<float> >, st
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           9.96
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 4.33
           1.17
                     0.21 130682022
                                        0.00
 3.92
           1.36
                     0.19 156813500
                                        0.00
                                                      std::vector<float, std::allocator<float> >::operator[](uns
 2.68
           1.49
                     0.13
                           170500
                                       0.00
                                                0.00 math_funcs::matrix_mult(std::vector<std::vector<float, std:
 <std::vector<float, std::allocator<float> > >, std::vector<std::vector<float,
                                                                                  std::allocator<float> >, std::al
```

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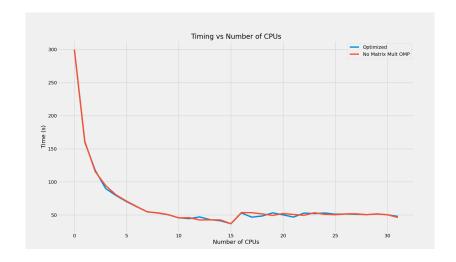
CPU Optimization/Parallelization

- Started with gpof to see what was slowing things down.
- Optimized serial version looking to make sure it was memory efficient (loops).
- ► Found the matrix multiplication and transpose were the most heavy computation.

Optimizing Matrix Multiplication

- Original implementation was not great for parallelization.
- Introduced batching.
- Found that if the problem was too small parallelization made things worse (actually need DNN).
- Tried multiple versions of matrix multiply (2 naive and blocking)

Performance



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CUDA

- Intended to get entire network into GPU but did not have enough time.
- Implemented matrix multiplication.
- Struggled more with getting CUDA configured than writing actual code :(
- Problem being small still resulted in not enough work.

CUDA

OMP (μs)	CUDA (μ s)	Kernel (μ s)
1.213	121.734	4.98

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What Next?

- Get full model into GPU.
- Test with cuda optimized functions and compare to ours.
- Introduce optimized CPU libraries.
- Flatten 2D arrays and vectors to decrease cache misses.