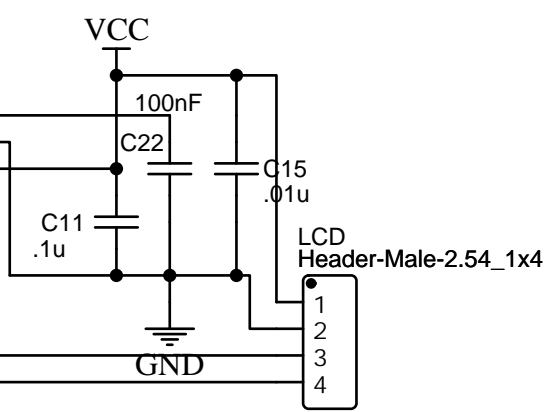
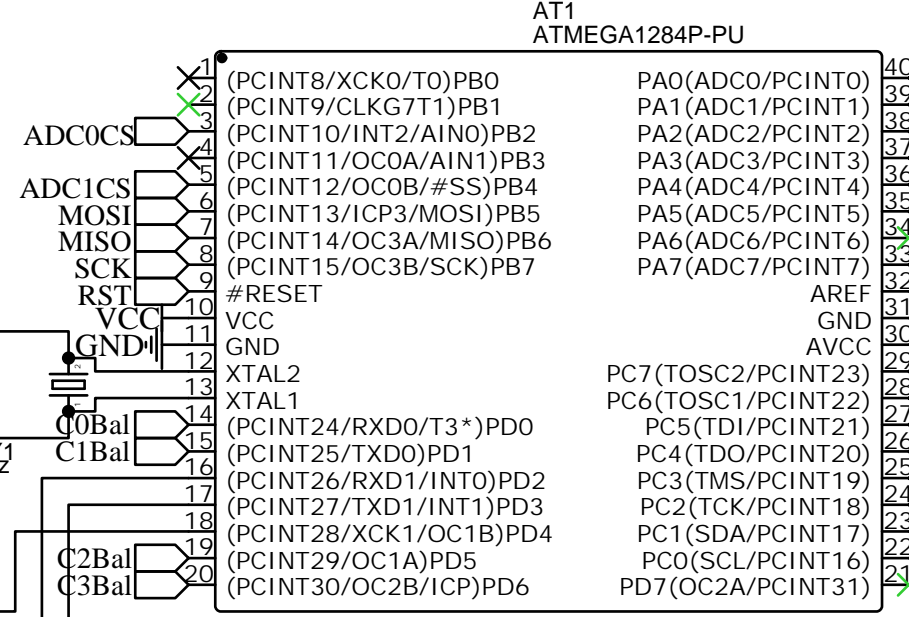
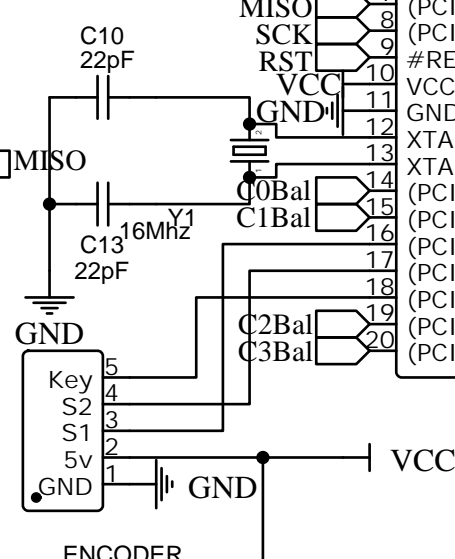
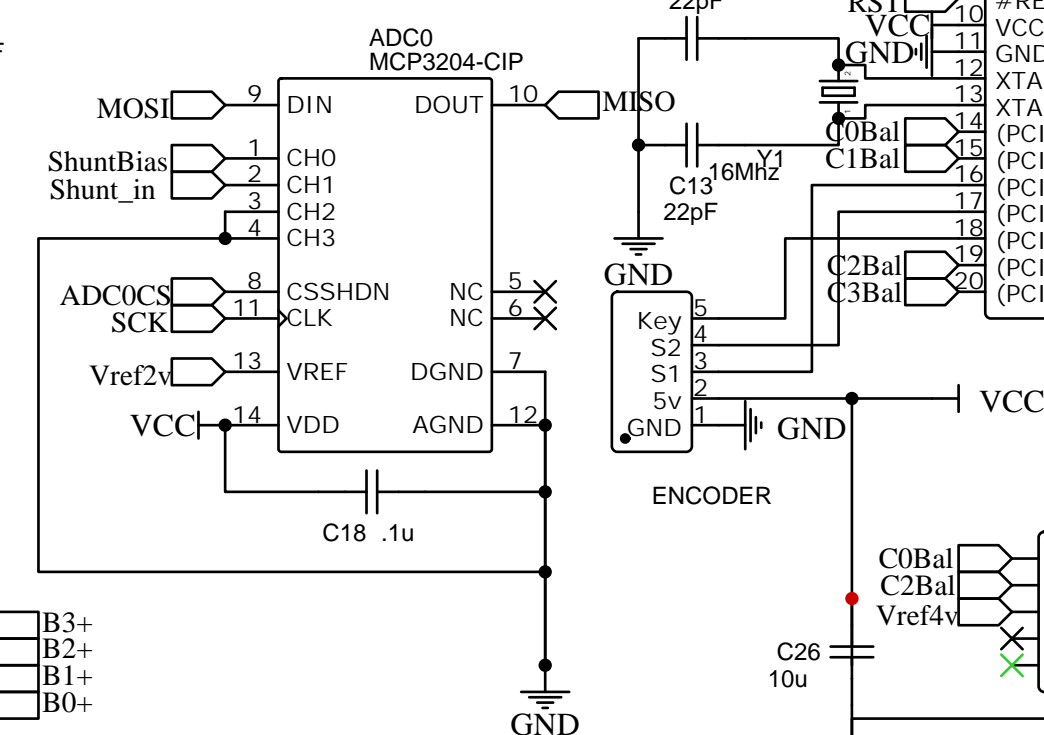
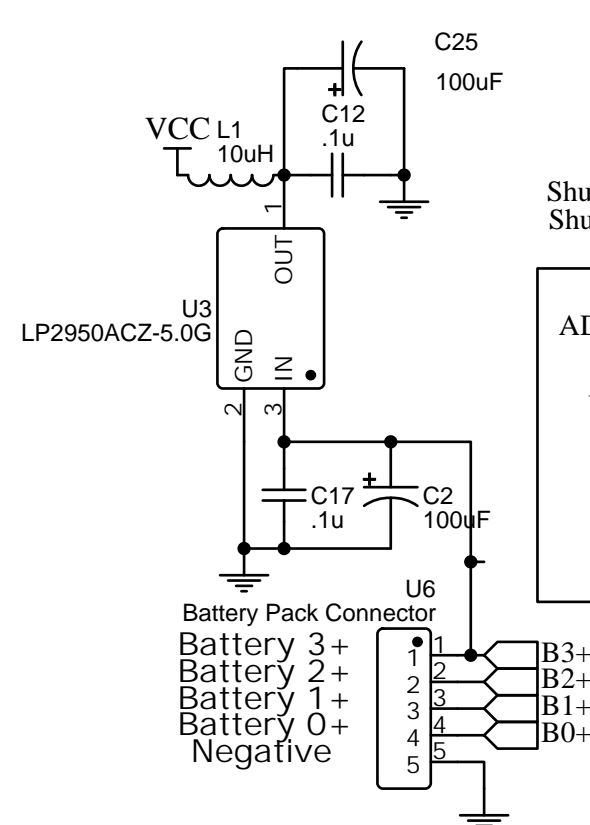


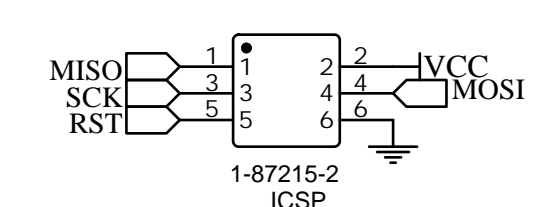
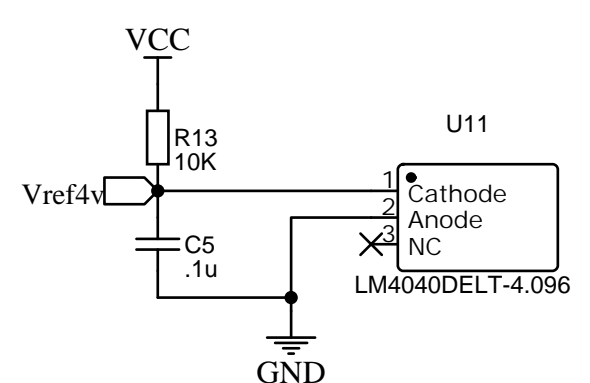
Ordered boards 4/22/24 BMS/LoadtestV1.02R(b)  
R13 should be 1K or less for total 3 sense  
R4 should be 1K  
C17 should be at pin 10 MPU  
"loadtest" should be dropped

Notes printed on board are incorrect

USE	MPU	ID
LoadOFF	40	21/A7
LoadON	39	20/A6
ShOFF	38	19/A5
ShON	37	18/A4
SolOFF	36	17/A3
SolON	35	16/A2
AltOFF	29	29
AltON	28	28
AUX1	27	27
AUX2	26	26
AUX3	25	25
AUX4	24	24
AUX5	33	25



Need  
add presence for balance board  
C17 should be at CPU pin10  
bal cable reversed  
QD for temp sense  
fuses in bal board  
delete C1 on shunt board



Cabl	MPU	ID
C0 -	1	14
C1 -	2	15
C2 -	3	19
C3 -	4	20