Important TEBF0808 CPLD Update to REV07 is needed to use this RGPIO definition!!!

Enter Comments here

BASE

I2S

SC0808BF_0

TEBF0808 Base Control (Beta)

RGPIO

RGPIO_M_RESET_N

axis_live_audio_0

AXI4-Stream Live Audio v1.0 (Pre-Production)

proc_sys_reset_0

Processor System Reset

vio_general

VIO (Virtual Input/Output)

zynq_ultra_ps_e_0

Zynq UltraScale+ MPSoC