

Defines which component is required for which version of the board (X=populate)

Refers to Ethernet Development Board v1.0

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Part	FPGA	MCU	Use
R1	X	X	USB
R2	X	X	USB
R3	X	X	USB
R4	X	X	USB
R5	X	X	USB
R6	X	X	USB
R7	X	X	USB
R8	X	X	USB
R9	X		RJ45 FPGA
R10	X		RJ45 FPGA
R11	X		PHY
R12	X		PHY
R13		X	ETH CNTRL
R14		X	RJ45 MCU
R15		X	RJ45 MCU
R16		X	ETH CNTRL
R17		X	ETH CNTRL
R18		X	ETH CNTRL
R19		X	ETH CNTRL
R20		X	RJ45 MCU
R21		X	ETH CNTRL
R22		X	DISPLAY
R23		X	DISPLAY
R24		X	DISPLAY
R25		X	DISPLAY
R26		X	DISPLAY
R27		X	DISPLAY
R28		X	DISPLAY
R29		X	DISPLAY
R30	X	X	BUTTON
C1	X		FPGA
C2		X	POWER
C3		X	POWER
C4	X		FPGA
C5	X	X	USB
C6	X	X	USB
C7	X	X	USB
C8	X	X	USB
C9	X	X	USB
C10	X		FPGA

C11	X		FPGA
C12	X		RJ45 FPGA
C13	X		RJ45 FPGA
C14	X		PHY
C15	X		PHY
C16	X		PHY
C17	X		PHY
C18	X		PHY
C19		X	RJ45 MCU
C20		X	ETH CNTRL
C21		X	ETH CNTRL
C22		X	RJ45 MCU
C23		X	RJ45 MCU
C24		X	RJ45 MCU
C25		X	ETH CNTRL
C26		X	ETH CNTRL
C27		X	ETH CNTRL
C28		X	ETH CNTRL
C29		X	ETH CNTRL
C30		X	ETH CNTRL
C31		X	ETH CNTRL
C32		X	ETH CNTRL
C33		X	ETH CNTRL
C34		X	MCU
C35		X	MCU
X1	X		FPGA
X2	X	X	USB
X3	X		RJ45 FPGA
X4	X		FPGA
X5		X	RJ45 MCU
X6		X	ETH CNTRL
X7		X	MCU
D1	X	X	USB
D2	X	X	USB
D3	X		PHY
D4	X		PHY
IC1	X	X	USB
IC2	X		PHY
IC3		X	ETH CNTRL
IC4		X	MCU
J1		X	POWER
LED1		X	DISPLAY
LED2		X	DISPLAY
LED3		X	DISPLAY
Q1		X	DISPLAY
Q2		X	DISPLAY

Q3		X	DISPLAY
SW1	X	X	BUTTON
88	40	67	