
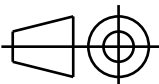


Note missing passives needed only for FPGA version.

DESIGNED BY: STEFAN GLOOR		VERSION DIFFERENCES  ETHERNET DEVELOPMENT BOARD v1.0		I	—		
DATE: 19.01.2019				H	—		
CHECKED BY:				G	—		
DATE:		<div><div>F</div><div></div></div>		F	—		
SIZE <div>A3</div>				E	—		
				D	—		
SCALE	WEIGHT (kg)			DRAWING NUMBER	SHEET 1/1	C	—
						B	—
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