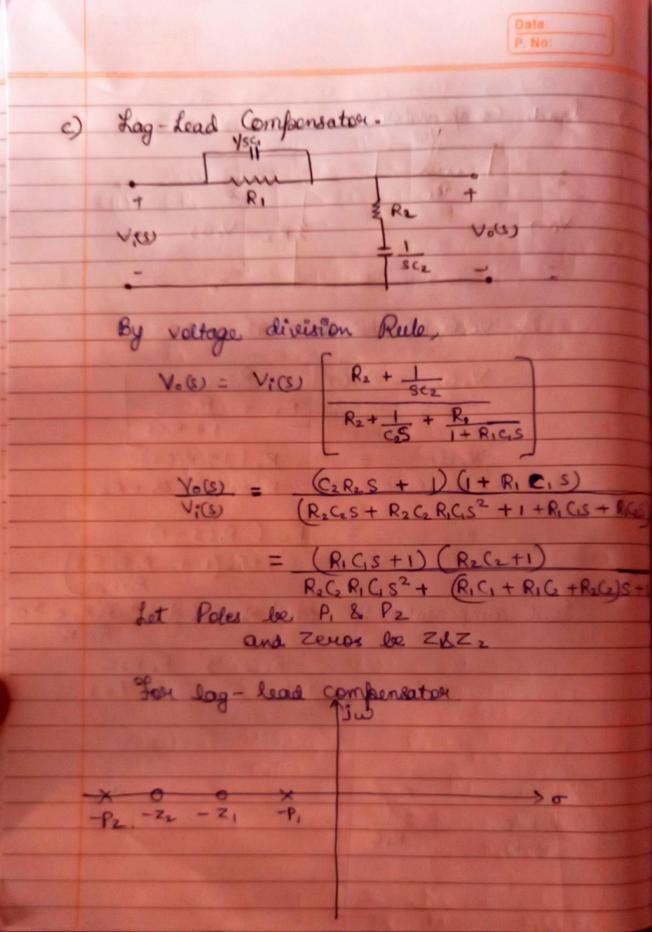
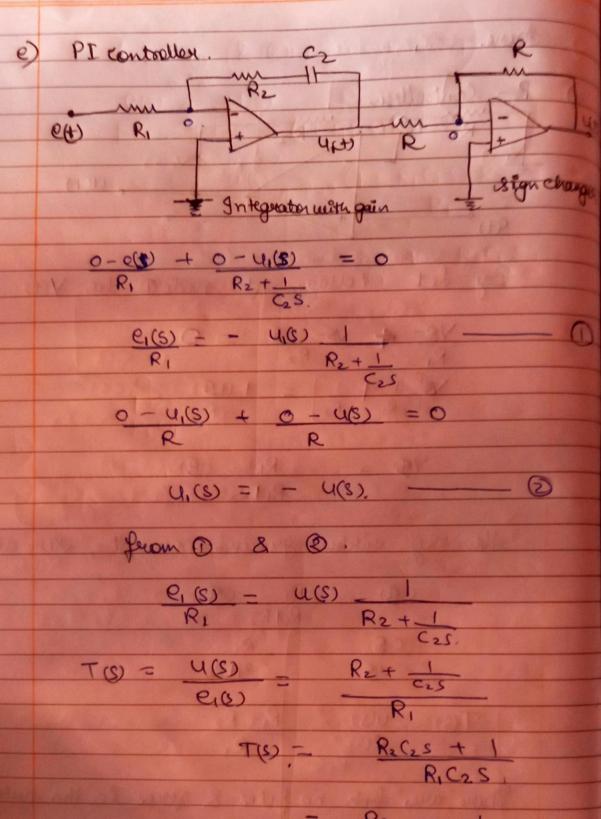


Lead Compensator. By voltage division Rule. V. (3) = Vi(5) Yo (S) R2 (1+ RICS) Vice R, + R, R, CS + R2 R2 (R,C3+1) RI+R2(I+ RIR2CS)
RI+R2 Lead compensator: - tan' R. C - tan' RIR2 C >0 RIC > RIRZC RI +RZ 4jw R, R2C

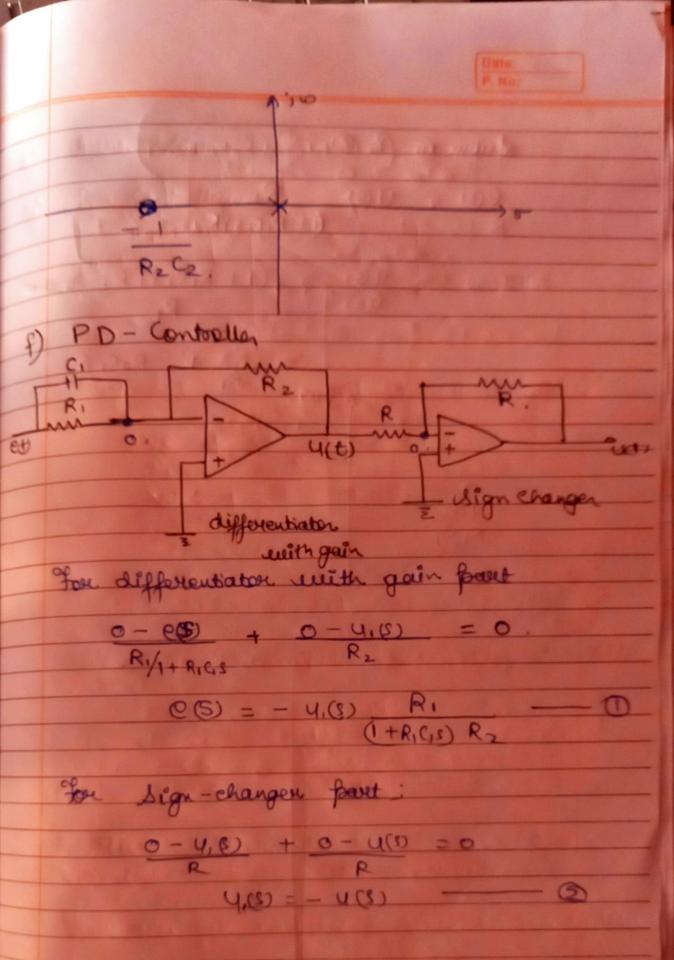


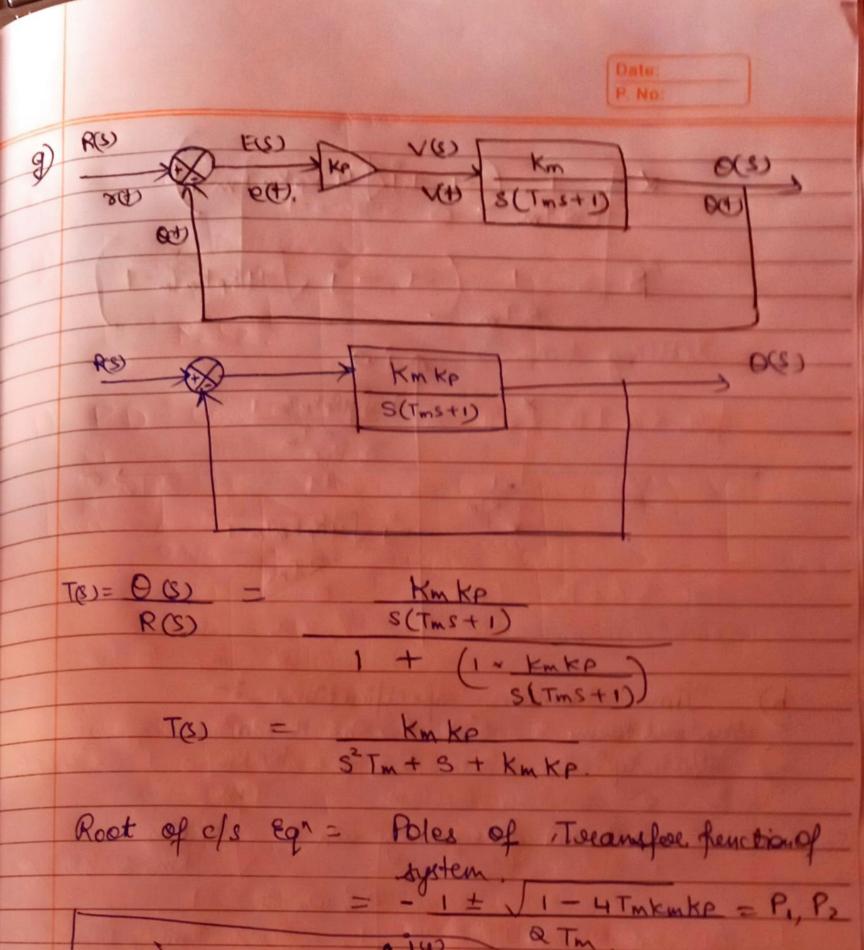
P - Controller. U(t) By Let Voltage at node 10 be V(t) V67 + V6- U6) = 0  $\frac{V(G)}{R_1+R_2} = \frac{R_1}{R_1+R_2}$ also by virtual ground concept, NH = at-C6) = R1 us  $T(s) = \frac{u(s)}{c(s)} = \frac{R_1 + R_2}{R_1}$ There are food no pooles & zeros in the tystem

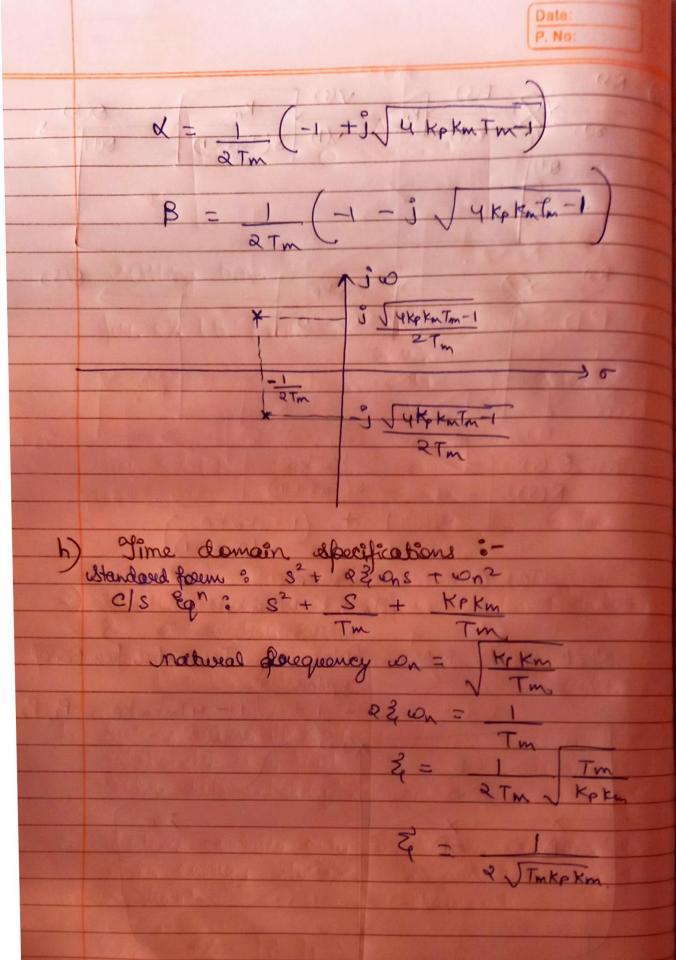
Date: P. No:

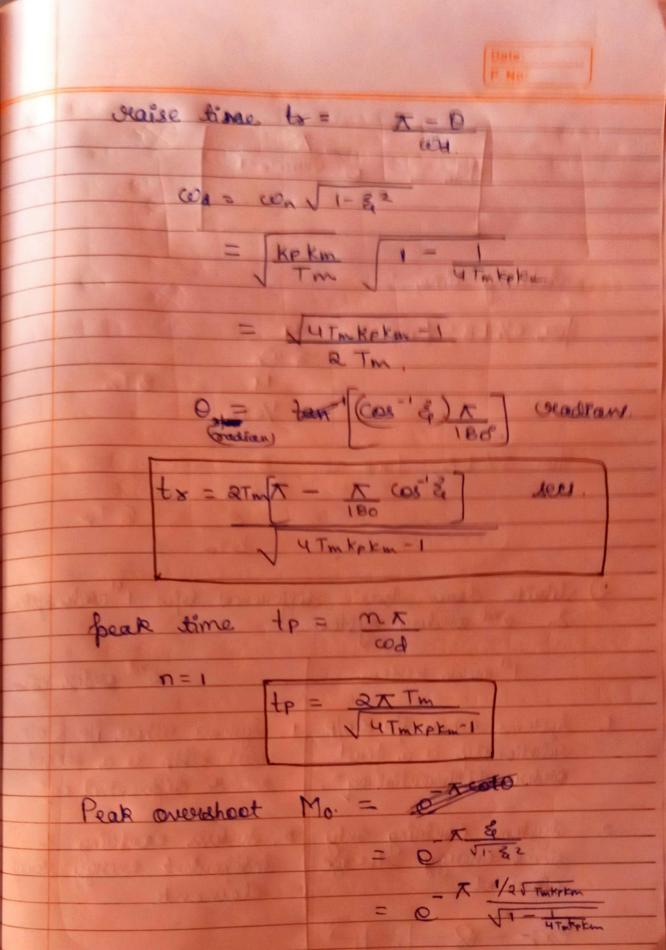


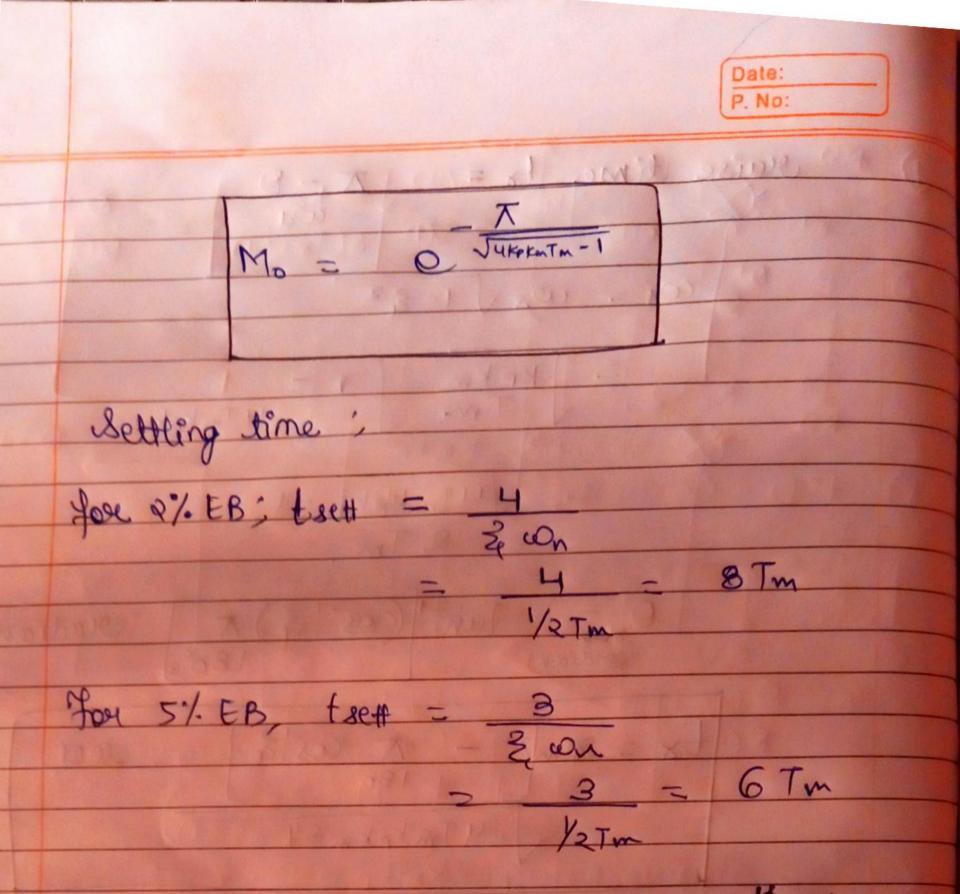
- R2 + 1











Delay time, Ta = 1+ 0.73 1 + 0.7 ( ) RVKpKmTm) = Tm Tm = Tm + 0.37 Kmkp Kmkp = 0.35 + KMKPTM KMKP

