

ADD_LATCH_sky130_rhbd_tt_1P8_25C.ccs Library

Cell Groups
DLATCHN
DLATCH
FA
HA

DLATCHN

ADD_LATCH_sky130_rhbd_tt_1P8_25C.ccs Cell
Library: Process , Voltage 1.80, Temp 25.00

Truth Table

INPUT	OUTPUT
D	Q
x	1

Footprint

Cell Name	Area
DLATCHN	0.00000

Pin Capacitance Information

Cell Name	Pin Cap(pf)	Max Cap(pf)
	D	Q
DLATCHN	0.00000	2.75752

Leakage Information

Cell Name	Leakage(nW)		
	Min.	Avg	Max.
DLATCHN	0.00000	0.00000	0.00000

Delay Information

Delay(ns) to Q rising :

Cell Name	Timing Arc(Dir)	Delay(ns)		
		First	Mid	Last
DLATCHN	D->Q (RR)	0.00000	0.00000	0.00000

Delay(ns) to Q falling :

Cell Name	Timing Arc(Dir)	Delay(ns)		
		First	Mid	Last
DLATCHN	D->Q (FF)	0.00000	0.00000	0.00000

DLATCH

*ADD_LATCH_sky130_rhbd_tt_1P8_25C.ccs Cell Library:
Process , Voltage 1.80, Temp 25.00*

Truth Table

INPUT	OUTPUT
D	Q
x	-

Footprint

Cell Name	Area
DLATCH	0.00000

Pin Capacitance Information

Cell Name	Pin Cap(pf)	Max Cap(pf)
	D	Q
DLATCH	0.00000	3.31413

Leakage Information

Cell Name	Leakage(nW)		
	Min.	Avg	Max.
DLATCH	0.00000	0.00000	0.00000

FA

*ADD_LATCH_sky130_rhbd_tt_1P8_25C.ccs Cell Library: Process , Voltage
1.80, Temp 25.00*

Truth Table

INPUT	
A	B
x	x

Footprint

Cell Name	Area
FA	0.00000

Pin Capacitance Information

Cell Name	Pin Cap(pf)	
	A	B
FA	0.00000	0.00000

Leakage Information

Cell Name	Leakage(nW)		
	Min.	Avg	Max.
FA	0.00000	0.00000	0.00000

HA

*ADD_LATCH_sky130_rhbd_tt_1P8_25C.ccs Cell Library: Process , Voltage
1.80, Temp 25.00*

Truth Table

INPUT	
A	B
x	x

Footprint

Cell Name	Area
HA	0.00000

Pin Capacitance Information

Cell Name	Pin Cap(pf)	
	A	B
HA	0.00000	0.00000

Leakage Information

Cell Name	Leakage(nW)		
	Min.	Avg	Max.
HA	0.00000	0.00000	0.00000