

TMR_SNQN_sky130_rhbd_tt_1P8_25C.ccs Library

Cell Groups
TMRDFFSNQNX1

TMRDFFSNQNX1

TMR_SNQ_N_sky130_rhbd_tt_1P8_25C.ccs
Cell Library: Process , Voltage 1.80, Temp
25.00

Truth Table

INPUT			OUTPUT
D	SN	CLK	QN
x	x	x	0

Footprint

Cell Name	Area
TMRDFFSNQNX1	0.00000

Pin Capacitance Information

Cell Name	Pin Cap(pf)			Max Cap(pf)
	D	SN	CLK	QN
TMRDFFSNQNX1	0.02916	0.04562	0.04556	1.80000

Leakage Information

Cell Name	Leakage(nW)		
	Min.	Avg	Max.
TMRDFFSNQNX1	0.00000	0.00000	0.00000

Passive Power Information

Passive power(pJ) for D rising :

Cell Name	Power(pJ)		
	first	mid	last
TMRDFFSNQNX1	0.00000	0.00000	0.00000
	-0.00446	-0.00452	-0.00454

Passive power(pJ) for D falling :

Cell Name	Power(pJ)		
	first	mid	last
TMRDFFSNQNX1	0.00000	0.00000	0.00000
	0.00446	0.00452	0.00454

Passive power(pJ) for SN rising :

Cell Name	Power(pJ)		
	first	mid	last
TMRDFFSNQNX1	0.00000	0.00000	0.00000
	-0.00676	-0.00683	-0.00685

Passive power(pJ) for SN falling :

Cell Name	Power(pJ)		
	first	mid	last
TMRDFFSNQNX1	0.00000	0.00000	0.00000
	0.00676	0.00683	0.00685

Passive power(pJ) for CLK rising :

Cell Name	Power(pJ)		
	first	mid	last
TMRDFFSNQNX1	0.00000	0.00000	0.00000
	-0.00900	-0.00914	-0.00916

Passive power(pJ) for CLK falling :

Cell Name	Power(pJ)		
	first	mid	last
TMRDFFSNQNX1	0.00000	0.00000	0.00000
	0.00900	0.00914	0.00916