

```
sub_4015C4 proc near

var_1C= dword ptr -1Ch
var_18= dword ptr -18h
var_14= dword ptr -14h

push    ebx
push    esi
push    edi
push    ebp
add    esp, 0FFFFFFF4h
mov    [esp+1Ch+var_18], ecx
mov    [esp+1Ch+var_1C], edx
mov    edx, eax
mov    ebp, edx
and    ebp, 0FFFFF000h
add    edx, [esp+1Ch+var_1C]
and    edx, 0FFFh
and    edx, 0FFFFF000h
mov    [esp+1Ch+var_14], edx
mov    eax, [esp+1Ch+var_18]
mov    [eax], ebp
mov    eax, [esp+1Ch+var_14]
sub    eax, ebp
mov    edx, [esp+1Ch+var_18]
mov    [edx+4], eax
mov    esi, ds:dword_40C43C
jmp    short loc_401646
```

```
loc_401646:
cmp    esi, offset dword_40C43C
jnz    short loc_40160A
```

```
loc_40160A:
mov    ebx, [esi+8]
mov    edi, [esi+0Ch]
add    edi, ebx
cmp    ebp, ebx
jbe    short loc_401618
```

```
        mov    ebx, ebp
```

```
loc_401618:
cmp    edi, [esp+1Ch+var_14]
jbe    short loc_401622
```

```
        mov    edi, [esp+1Ch+var_14]
```

```
loc_401622:
cmp    edi, ebx
jbe    short loc_401644
```

```
push    4           ; flProtect
push    1000h       ; flAllocationType
sub    edi, ebx
push    edi         ; dwSize
push    ebx         ; lpAddress
call    VirtualAlloc
test    eax, eax
jnz    short loc_401644
```

```
        mov    eax, [esp+1Ch+var_18]
        xor    edx, edx
        mov    [eax], edx
        jmp    short loc_40164E
```

```
loc_401644:
        mov    esi, [esil]
```

```
loc_40164E:
        add    esp, 0Ch
        pop    ebp
        pop    edi
        pop    esi
        pop    ebx
        ret
sub_4015C4 endp
```