

STMod+ with Motion and Optical Sensors

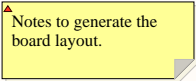
MB1540

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- Sheet 8: IRA-S210ST01
- Sheet 9: LV0104CS-TLM-H
- Sheet 10: VEML6070

Legend

- General comment such as function title, configuration, ...
- Text to be added to silkscreen.
- Warning text.



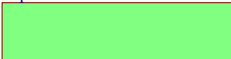
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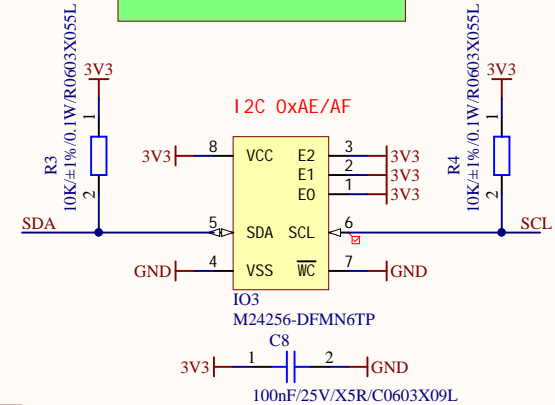
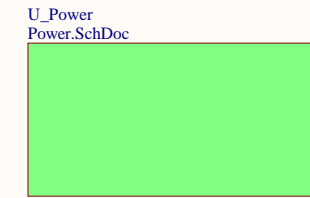
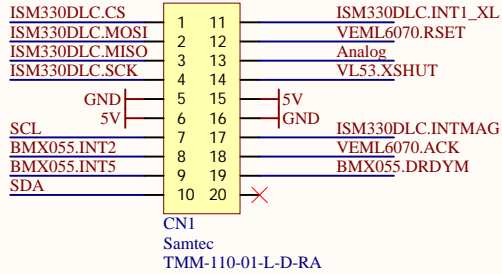
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U_Top
Top.SchDoc



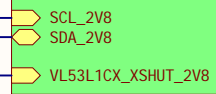
Title: Project overview		
Project: STMod+ with Motion and Optical Sensors		
Variant: Default		
Revision: A-01		Reference: MB1540
Size: A4	Date: 06-FEB-19	Sheet: 1 of 10



U_Level Shifters
Level Shifters.SchDoc

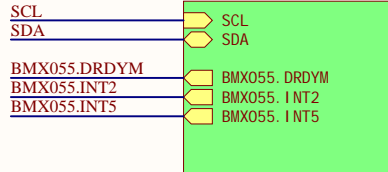


U_VL53L1X
VL53L1X.SchDoc



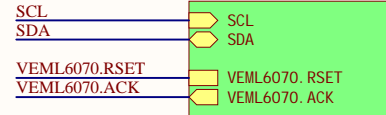
I2C 0x52/53

U_BMX055
BMX055.SchDoc



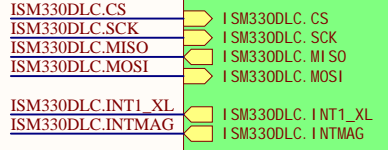
I2C 0x32/33 (acc)
I2C 0xD2/D3 (gyr)
I2C 0x22/23 (mag)

U_VEML6070
VEML6070.SchDoc

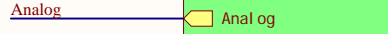


I2C 0x70/71/73/31

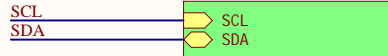
U_ISM330DLC
ISM330DLC.SchDoc



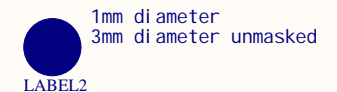
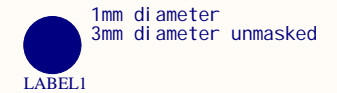
U_IRA-S210ST01
IRA-S210ST01.SchDoc

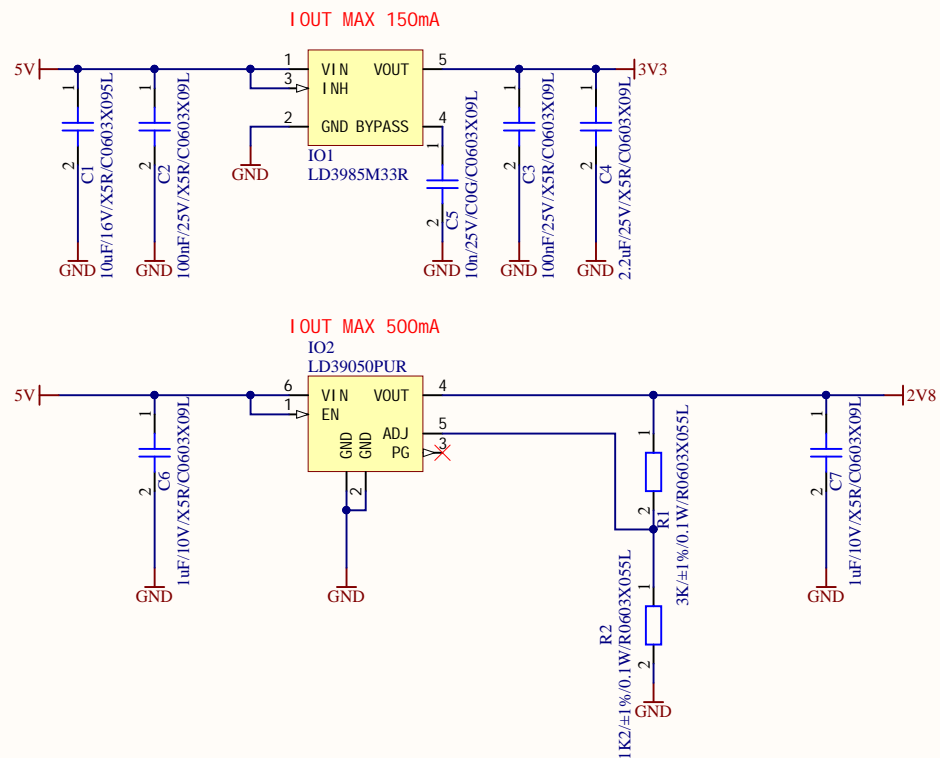


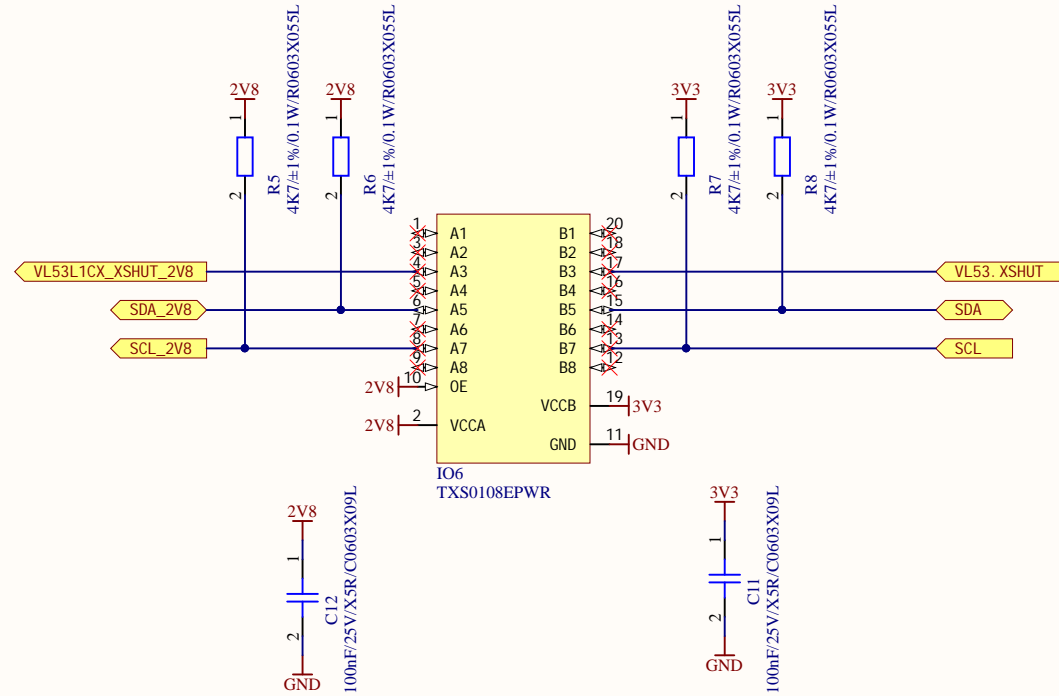
U_LV0104CS-TLM-H
LV0104CS-TLM-H.SchDoc

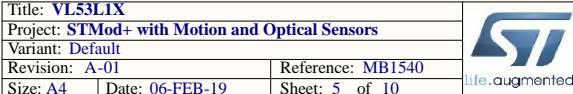


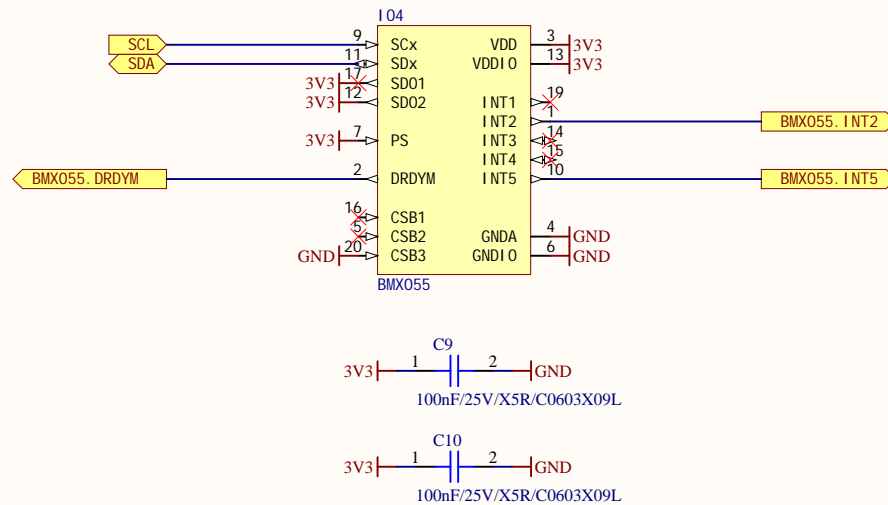
I2C 0x26/27

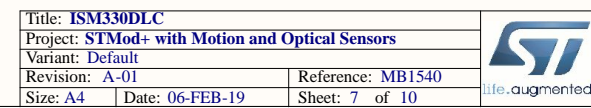


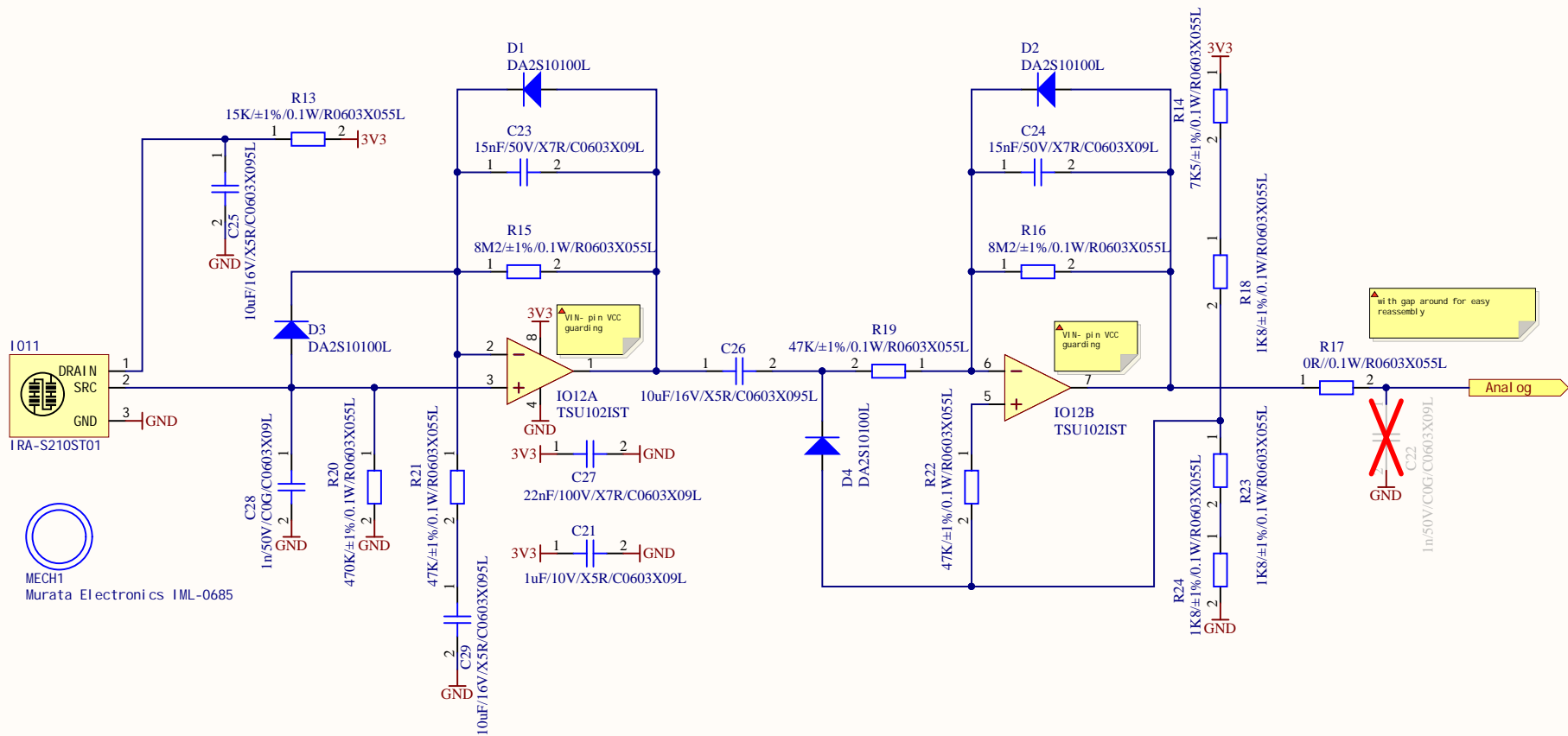


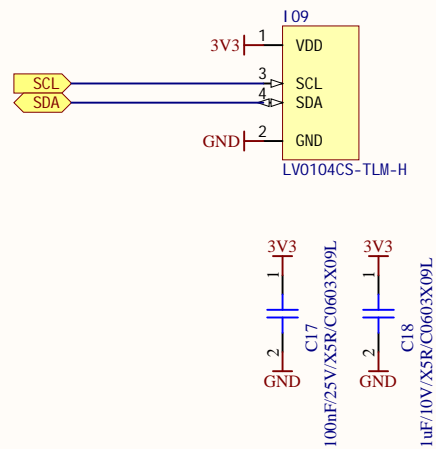


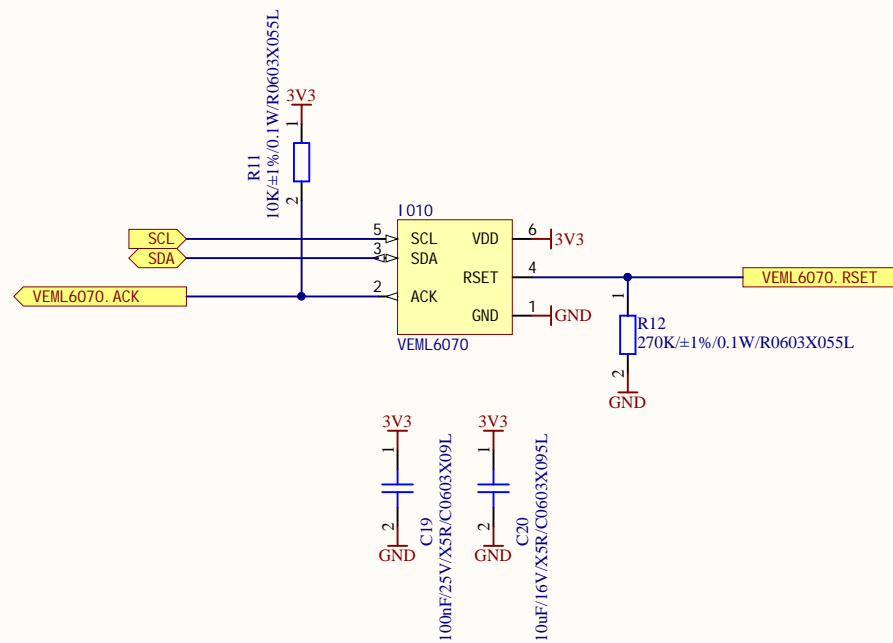












Design Rules Verification Report

Filename : D:\Users\coppervia\AltiumProjects\Projects\STM\MB1540\MB1540.PcbDoc

Warnings 0
Rule Violations 0

Warnings	
Total	0

Rule Violations	
Clearance Constraint (Gap=0.25mm) (All),(All)	0
Clearance Constraint (Gap=0.15mm) (InComponent('IO8')),(All)	0
Clearance Constraint (Gap=0.2mm) (InComponent('IO10') or InComponent('IO12')),(All)	0
Short-Circuit Constraint (Allowed=No) (All),(All)	0
Un-Routed Net Constraint (All)	0
Modified Polygon (Allow modified: No), (Allow shelved: No)	0
Width Constraint (Min=0.25mm) (Max=1mm) (Preferred=0.25mm) (All)	0
Routing Layers(All)	0
Routing Via (MinHoleWidth=0.3mm) (MaxHoleWidth=0.3mm) (PreferredHoleWidth=0.3mm) (MinWidth=0.8mm)	0
Differential Pairs Uncoupled Length using the Gap Constraints (Min=0.254mm) (Max=0.254mm) (Preferred=0.254mm)	0
Power Plane Connect Rule(Relief Connect)(Expansion=0.508mm) (Conductor Width=0.21mm) (Air Gap=0.21mm)	0
Acute Angle Constraint (Minimum=60.000) (All)	0
Hole Size Constraint (Min=0.025mm) (Max=10mm) (All)	0
Pads and Vias to follow the Drill pairs settings	0
Hole To Hole Clearance (Gap=0.254mm) (All),(All)	0
Minimum Solder Mask Sliver (Gap=0.1mm) (IsPad),(IsVia)	0
Silk To Solder Mask (Clearance=0.2mm) (IsPad),(All)	0
Silk to Silk (Clearance=0.2mm) (All),(All)	0
Net Antennae (Tolerance=0mm) (All)	0
Room U_Top (Bounding Region = (263mm, 51.5mm, 470.075mm, 253.175mm) (InComponentClass('U_Top'))	0
Room U_ISM330DLC (Bounding Region = (0mm, 0mm, 455.425mm, 299.65mm) (InComponentClass('U_ISM330DLC'))	0
Room U_VEML6070 (Bounding Region = (0mm, 0mm, 515.95mm, 324mm) (InComponentClass('U_VEML6070'))	0
Room U_LV0104CS-TLM-H (Bounding Region = (0mm, 0mm, 533.475mm, 395.225mm)	0
Room U_Level_Shifters (Bounding Region = (0mm, 0mm, 482.075mm, 282.5mm)	0
Room U_VL53L1X (Bounding Region = (226.1mm, 9.225mm, 530.15mm, 246.85mm)	0
Room U_Power (Bounding Region = (277.495mm, 83.625mm, 459.075mm, 237.975mm)	0
Room U_BMX055 (Bounding Region = (0mm, 0mm, 658.35mm, 357.9mm) (InComponentClass('U_BMX055'))	0
Room U_IRA-S210ST01 (Bounding Region = (248.125mm, 28.45mm, 487.575mm, 261.75mm)	0
Component Clearance Constraint (Horizontal Gap = 0.2mm, Vertical Gap = 0.2mm) (All),(All)	0
Component Clearance Constraint (Horizontal Gap = -2mm, Vertical Gap = -2mm) (InComponent('MECH1')),(All)	0
Height Constraint (Min=0mm) (Max=25.4mm) (Preferred=12.7mm) (All)	0
Total	0

Electrical Rules Check Report

Class	Document	Message
		Successful Compile for MB1540.PrjPcb

60.0mm

0 0

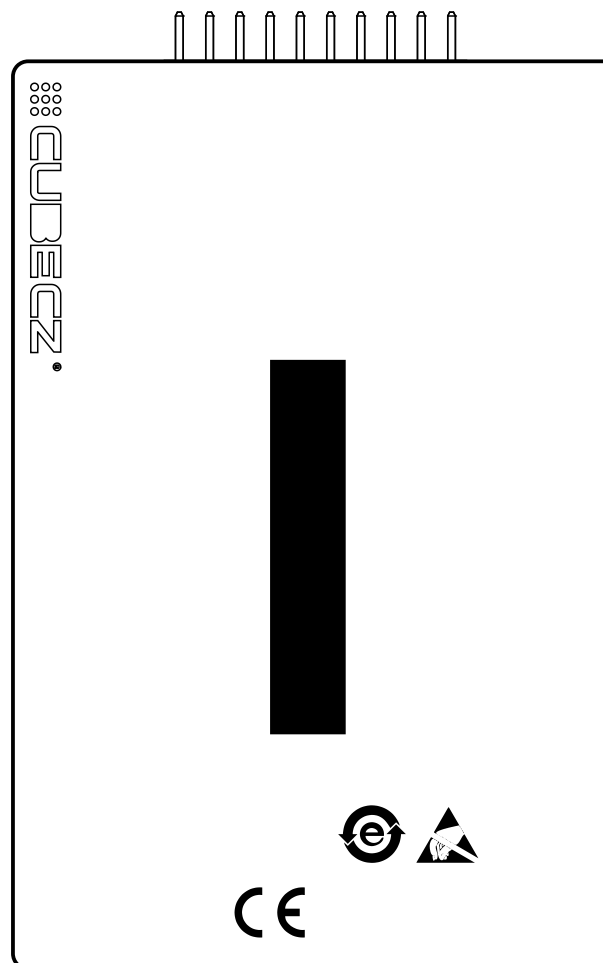
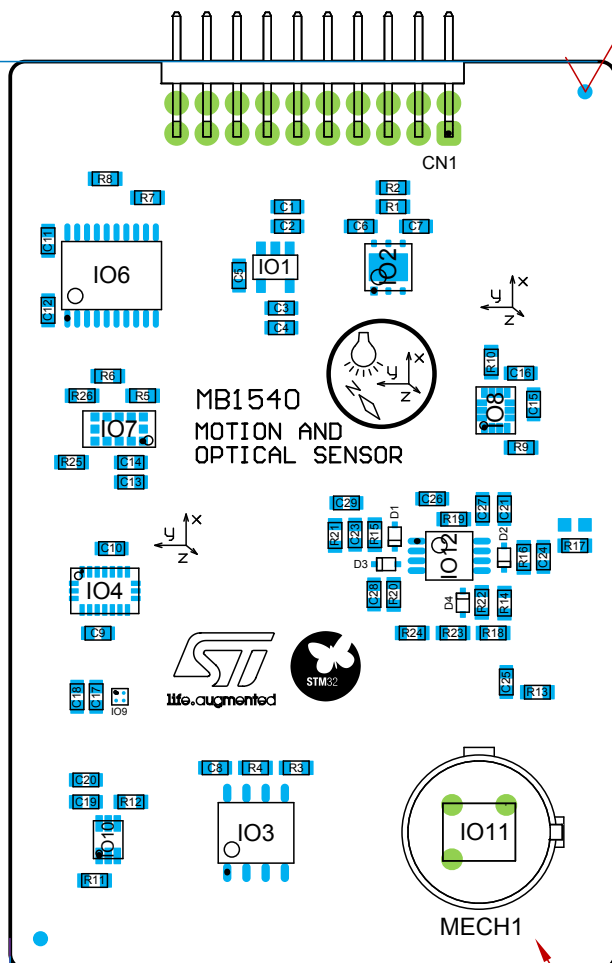
40.0mm

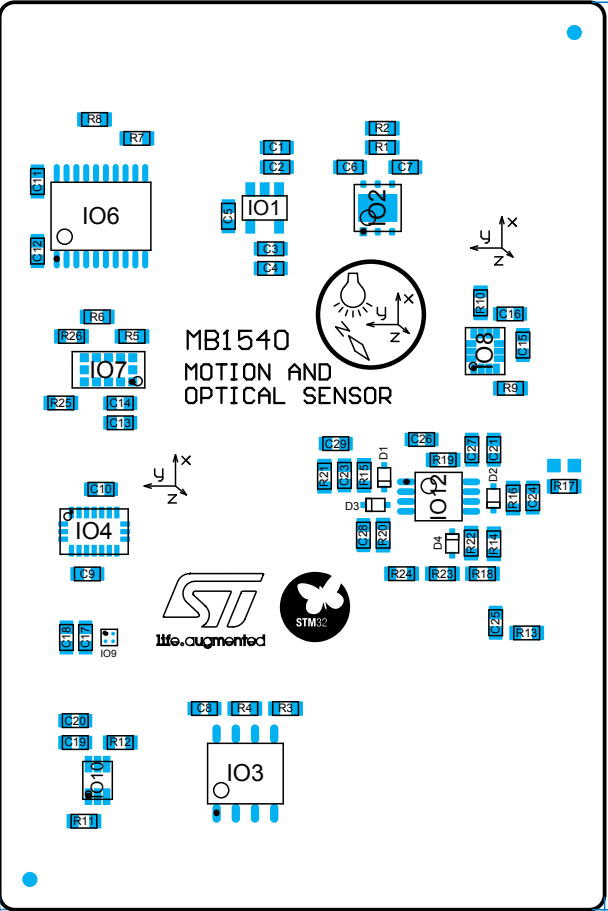
ENiG

View from Top side (Scale 2:1)

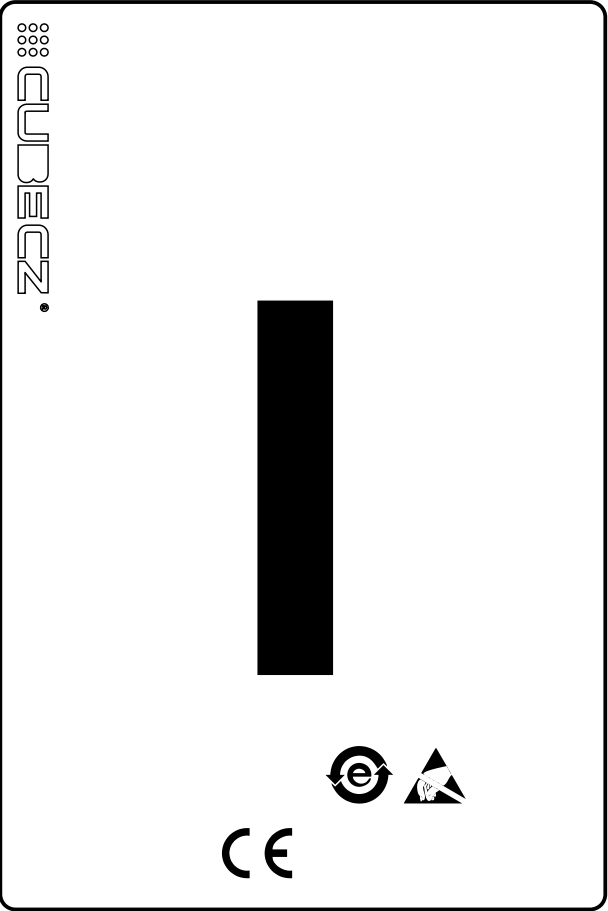
4x R1.0mm

when IO11 assembled, cover it with
MECH1 Sensor Accessory Lens
Murata Electronics North America
IML-0685



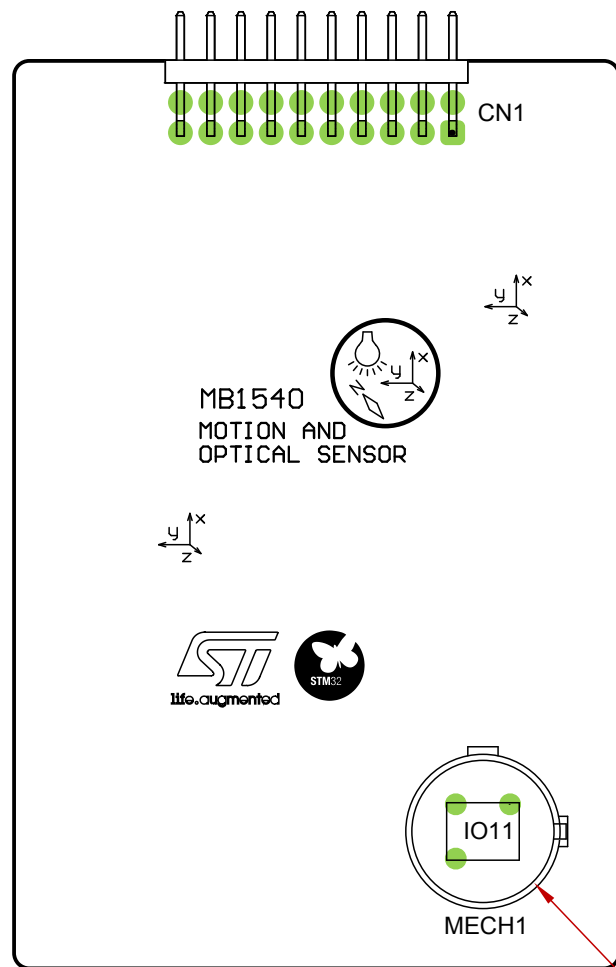


View from Top side (Scale 2:1)



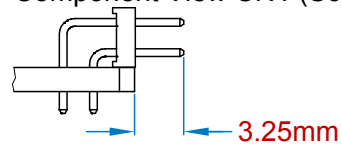
View from Bottom side (Scale 2:1)

\oplus 0 0

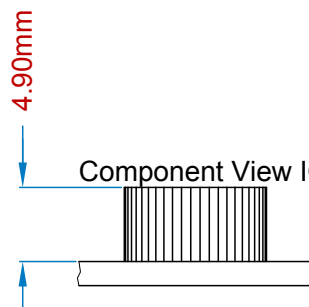


View from Top side (Scale 2:1)

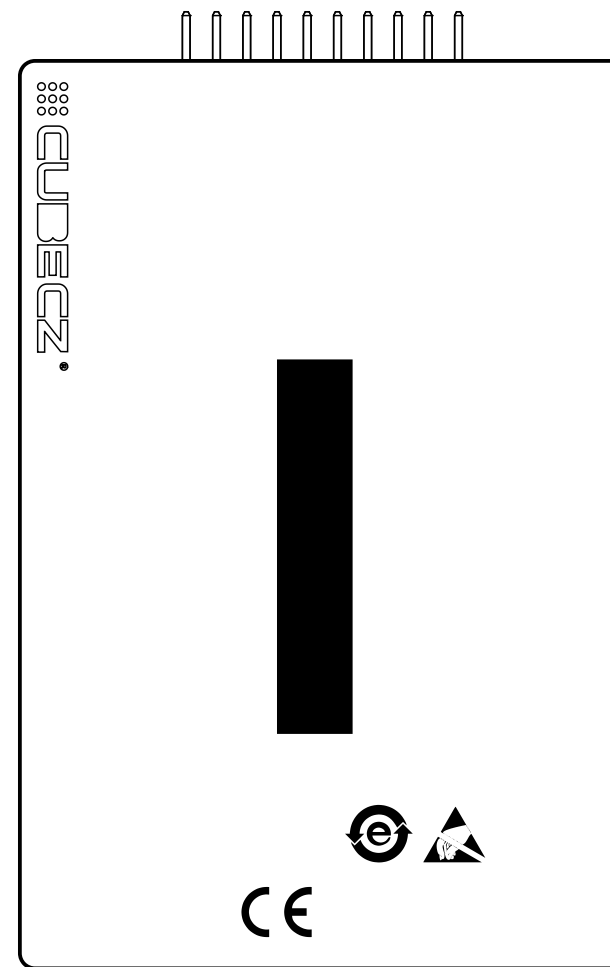
Component View CN1 (Scale 2:1)



Component View IO11(Scale 2:1)

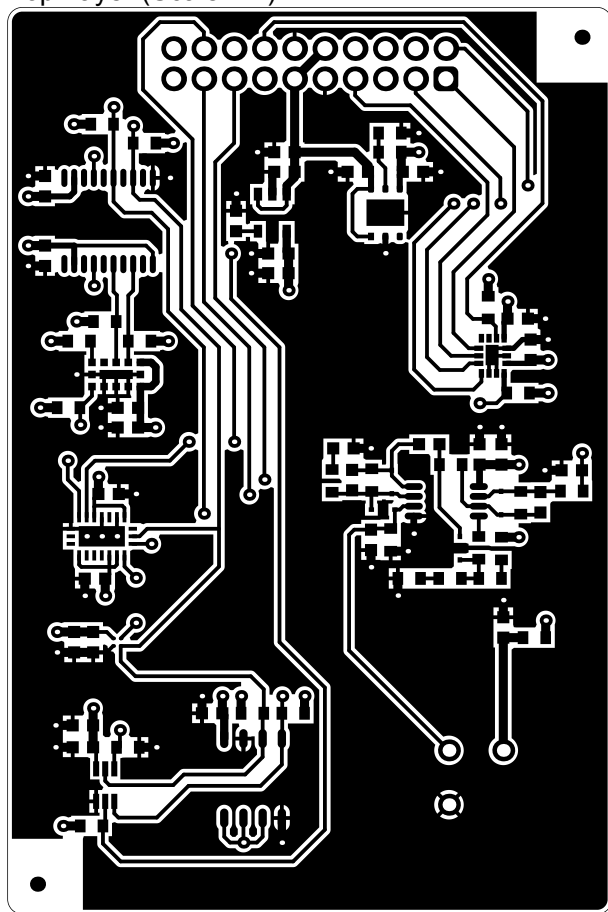


when IO11 assembled, cover it with
MECH1 Sensor Accessory Lens
Murata Electronics North America
IML-0685

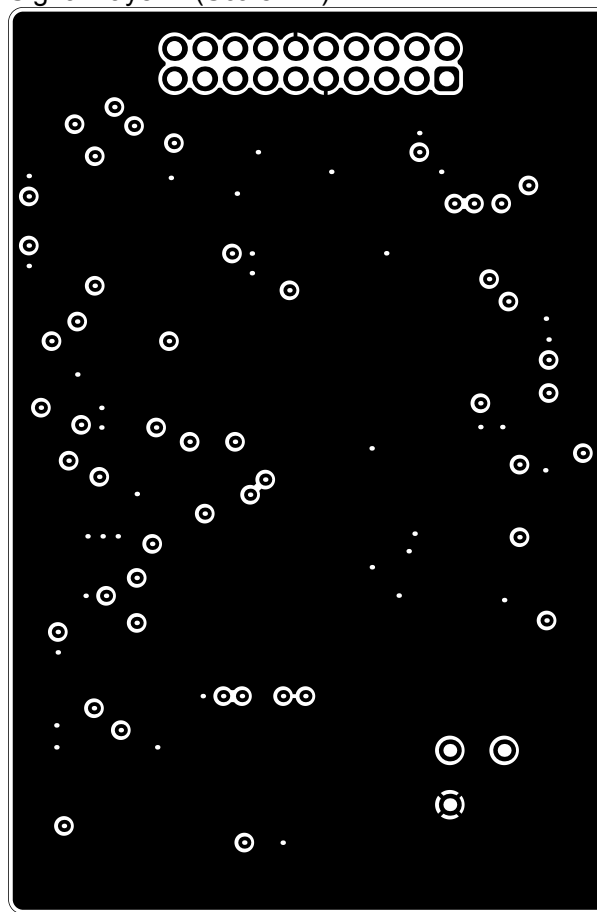


View from Bottom side (Scale 2:1)

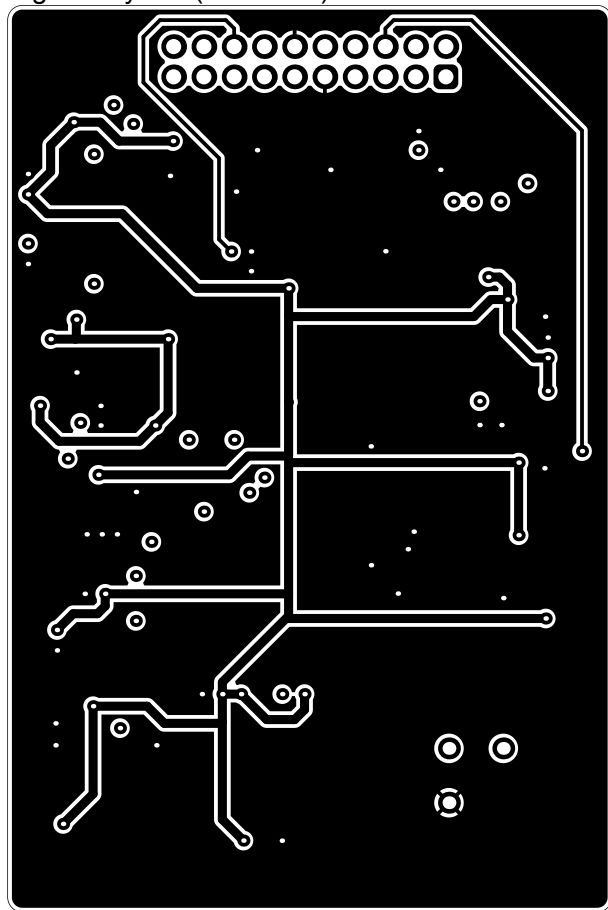
Top Layer (Scale 2:1)



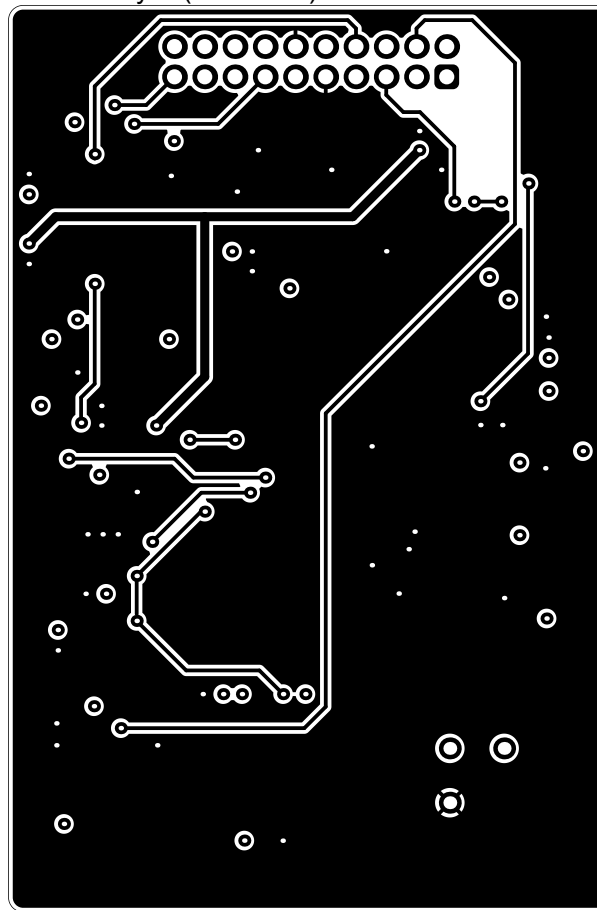
Signal Layer 1 (Scale 2:1)



Signal Layer 2 (Scale 2:1)



Bottom Layer (Scale 2:1)



Layer Stack Legend

Material	Layer	Thickness	Dielectric Material	Type	Gerber
	Top Paste			Paste Mask	GTP
	Top Overlay			Legend	GTO
Surface Material	Top Solder	0.015mm	Solder Resist	Solder Mask	GTS
Copper	Top Layer	0.018mm		Signal	GTL
Prepreg		0.387mm	FR-4	Dielectric	
Copper	Signal Layer 1	0.035mm		Signal	G1
Core		0.713mm		Dielectric	
Copper	Signal Layer 2	0.035mm		Signal	G2
Prepreg		0.387mm		Dielectric	
Copper	Bottom Layer	0.018mm		Signal	GBL
Surface Material	Bottom Solder	0.015mm	Solder Resist	Solder Mask	GBS
	Bottom Overlay			Legend	GBO
	Bottom Paste			Paste Mask	GBP

Total thickness: 1.623mm

Drill Table

Symbol	Count	Hole Size	Plated	Hole Tolerance
◇	86	0.30mm	Plated	None
○	3	0.85mm	Plated	None
□	20	1.00mm	Plated	None
	109 Total			

