

Note: 1 – OFF and 0 – ON (Outputs are active low)

Hexadecimal Digit	Inputs				Outputs ^{segments}						
	D ₃	D ₂	D ₁	D ₀	S _g	S _f	S _e	S _d	S _c	S _b	S _a
0	0	0	0	0	1	0	0	0	0	0	0
1	0	0	0	1	1	1	1	1	0	0	1
2	0	0	1	0	0	1	0	0	1	0	0
3	0	0	1	1	0	1	1	0	0	0	0
4	0	1	0	0	0	0	1	1	0	0	1
5	0	1	0	1	0	0	1	0	0	1	0
6	0	1	1	0	0	0	0	0	0	1	0
7	0	1	1	1	1	1	1	1	0	0	0
8	1	0	0	0	0	0	0	0	0	0	0
9	1	0	0	1	0	0	1	1	0	0	0
A	1	0	1	0	X	X	X	X	X	X	X
B	1	0	1	1	X	X	X	X	X	X	X
C	1	1	0	0	X	X	X	X	X	X	X
D	1	1	0	1	X	X	X	X	X	X	X
E	1	1	1	0	X	X	X	X	X	X	X
F	1	1	1	1	X	X	X	X	X	X	X

Table 1. Truth table of the seven-segment display decoder

$$\text{segment } a = \overline{D_3} \overline{D_2} \overline{D_1} D_0 + D_2 \overline{D_1} \overline{D_0}$$

$$\text{segment } b = D_2 \overline{D_1} D_0 + D_2 D_1 \overline{D_0}$$

$$\text{segment } c = \overline{D_2} D_1 \overline{D_0}$$

$$\text{segment } d = D_2 \overline{D_1} \overline{D_0} + \overline{D_2} \overline{D_1} D_0 + D_2 D_1 D_0$$

$$\text{segment } e = D_2 \overline{D_1} + D_0$$

$$\text{segment } f = D_1 D_0 + \overline{D_3} \overline{D_2} D_0 + \overline{D_3} \overline{D_2} D_1$$

$$\text{segment } g = \overline{D_3} \overline{D_2} \overline{D_1} + D_2 D_1 D_0$$