

Note: 1 – OFF and 0 – ON (Outputs are active low)

Hexadecimal Digit	Inputs				Outputs							Segments	
	D <sub>3</sub>	D <sub>2</sub>	D <sub>1</sub>	D <sub>0</sub>	S <sub>g</sub>	S <sub>f</sub>	S <sub>e</sub>	S <sub>d</sub>	S <sub>c</sub>	S <sub>b</sub>	S <sub>a</sub>	S <sub>j</sub>	S <sub>i</sub>
0	0	0	0	0	1	0	0	0	0	0	0	0	0
1	0	0	0	1	1	1	1	1	0	0	0	1	0
2	0	0	1	0	0	1	0	0	1	0	0	0	0
3	0	0	1	1	0	1	1	0	0	0	0	0	0
4	0	1	0	0	0	0	1	1	0	0	0	1	0
5	0	1	0	1	0	0	1	0	0	1	0	1	0
6	0	1	1	0	0	0	0	0	0	0	1	0	0
7	0	1	1	1	1	1	1	1	0	0	0	0	0
8	1	0	0	0	0	0	0	0	0	0	0	0	0
9	1	0	0	1	0	0	1	1	0	0	0	0	0
A	1	0	1	0	X	X	X	X	X	X	X	X	X
B	1	0	1	1	X	X	X	X	X	X	X	X	X
C	1	1	0	0	X	X	X	X	X	X	X	X	X
D	1	1	0	1	X	X	X	X	X	X	X	X	X
E	1	1	1	0	X	X	X	X	X	X	X	X	X
F	1	1	1	1	X	X	X	X	X	X	X	X	X

Table 1. Truth table of the seven-segment display decoder

$$\text{segment\_a} = \overline{D_3} \overline{D_2} \overline{D_1} D_0 + D_2 \overline{D_1} \overline{D_0}$$

$$\text{segment\_b} = D_2 \overline{D}_1 D_0 + D_2 D_1 \overline{D}_0$$

$$\text{segment\_c} = \overline{D}_2 D_1 \overline{D}_0$$

$$\text{segment\_d} = D_2 \overline{D}_1 \overline{D}_0 + \overline{D}_2 \overline{D}_1 D_0 + D_2 D_1 D_0$$

$$\text{segment\_e} = D_2 \overline{D}_1 + D_0$$

$$\text{segment\_f} = D_1 D_0 + \overline{D}_3 \overline{D}_2 D_0 + \overline{D}_3 \overline{D}_2 D_1$$

$$\text{segment\_g} = \overline{D}_3 \overline{D}_2 \overline{D}_1 + D_2 D_1 D_0$$