#### **DISCRETE SEMICONDUCTORS**

## DATA SHEET

# 2N3553 Silicon planar epitaxial overlay transistor

Product specification Supersedes data of October 1981 File under Discrete Semiconductors, SC08a 1995 Oct 27





### Silicon planar epitaxial overlay transistor

2N3553

#### **APPLICATIONS**

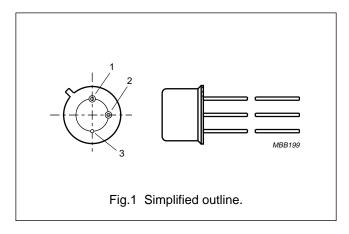
• The 2N3553 is intended for use in VHF and UHF transmitting applications.

#### PINNING - TO-39/3

PIN	DESCRIPTION	
1	emitter	
2	base	
3	collector	

#### **DESCRIPTION**

The device is a silicon NPN overlay transistor in a TO-39 metal package with the collector connected to the case.



#### QUICK REFERENCE DATA

SYMBOL	PARAMETER	CONDITIONS	MAX.	UNIT
V <sub>CEX</sub>	collector-emitter voltage	$I_C \le 200 \text{ mA}; V_{BE} = -1.5 \text{ V}$	65	V
V <sub>CEO</sub>	collector-emitter voltage	open base; I <sub>C</sub> ≤ 200 mA	40	V
I <sub>CM</sub>	peak collector current		1.0	Α
P <sub>tot</sub>	total power dissipation	up to T <sub>mb</sub> = 25 °C	7.0	W
Tj	junction temperature		200	°C
f <sub>T</sub>	transition frequency	I <sub>C</sub> = 125 mA; V <sub>CE</sub> = 28 V	500	_

#### RF performance

f	V <sub>CE</sub>	P <sub>o</sub>	G <sub>p</sub>	η
(MHz)	(V)	(W)	(dB)	<b>(%)</b>
175	28	2.5	>10	>50

#### **WARNING**

#### Product and environmental safety - toxic materials

This product contains beryllium oxide. The product is entirely safe provided that the BeO disc is not damaged. All persons who handle, use or dispose of this product should be aware of its nature and of the necessary safety precautions. After use, dispose of as chemical or special waste according to the regulations applying at the location of the user. It must never be thrown out with the general or domestic waste.

### Silicon planar epitaxial overlay transistor

2N3553

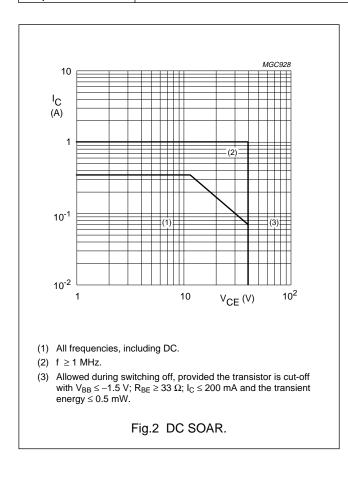
#### **LIMITING VALUES**

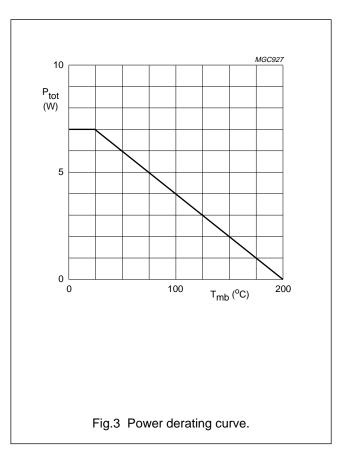
In accordance with the Absolute Maximum Rating System (IEC 134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V <sub>CBO</sub>	collector-base voltage	open emitter	_	65	V
V <sub>CEX</sub>	collector-emitter voltage	$I_C \le 200 \text{ mA}; V_{BE} = -1.5 \text{ V}$	_	65	V
V <sub>CEO</sub>	collector-emitter voltage	open base; I <sub>C</sub> ≤ 200 mA	_	40	V
V <sub>EBO</sub>	emitter-base voltage	open collector	_	4	V
I <sub>C</sub>	collector current (DC)		_	0.35	Α
I <sub>CM</sub>	peak collector current		_	1	А
P <sub>tot</sub>	total power dissipation	up to T <sub>mb</sub> = 25 °C	_	7	W
T <sub>stg</sub>	storage temperature		-65	+200	°C
Tj	junction temperature		_	200	°C

#### THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	VALUE	UNIT
R <sub>th j-mb</sub>	thermal resistance from junction to mounting base	25	K/W





### Silicon planar epitaxial overlay transistor

2N3553

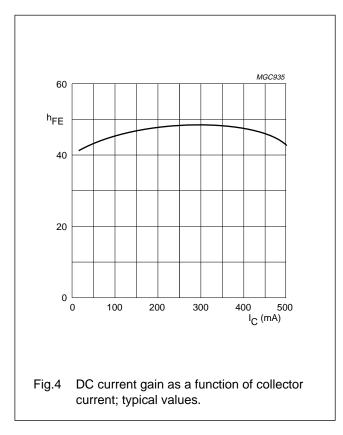
#### **CHARACTERISTICS**

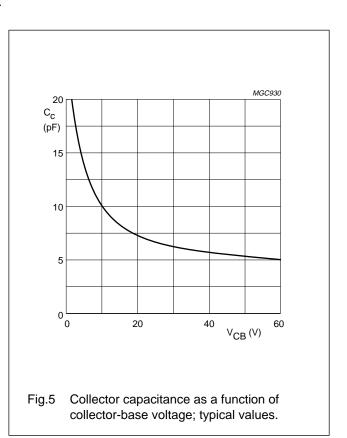
 $T_j = 25$  °C unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
V <sub>(BR)CBO</sub>	collector-base breakdown voltage	open emitter; I <sub>C</sub> = 0.25 mA	65	_	_	V
V <sub>(BR)CEO</sub>	collector-emitter breakdown voltage	open base; I <sub>C</sub> up to 200 mA; note 1	40	_	_	V
V <sub>(BR)CEX</sub>	collector-emitter breakdown voltage	$I_C$ up to 200 mA; $V_{BE}$ = -1.5 V; $R_B$ = 33 $\Omega$ ; note 1	65	_	_	V
V <sub>(BR)EBO</sub>	emitter-base breakdown voltage	open collector; I <sub>E</sub> = 0.25 mA	4	_	_	V
V <sub>BE</sub>	base-emitter voltage	I <sub>C</sub> = 250 mA; V <sub>CE</sub> = 5 V	_	_	1.5	V
V <sub>CEsat</sub>	collector-emitter saturation voltage	I <sub>C</sub> = 250 mA; I <sub>B</sub> = 50 mA	_	_	1.0	V
I <sub>CEO</sub>	collector leakage current	open base; V <sub>CE</sub> = 30 V	_	_	0.1	mA
h <sub>FE</sub>	DC current gain	$V_{CE} = 5 \text{ V}; I_{C} = 125 \text{ mA}$	15	_	200	
		$V_{CE} = 5 \text{ V}; I_{C} = 250 \text{ mA}$	10	_	100	
f <sub>T</sub>	transition frequency	I <sub>C</sub> = 125 mA; V <sub>CE</sub> = 28 V	_	500	_	MHz
Rho <sub>ie</sub> )	real part of input impedance	I <sub>C</sub> = 125 mA; V <sub>CE</sub> = 28 V; f = 200 MHz	_	_	20	Ω
C <sub>c</sub>	collector capacitance	$V_{CB} = 28 \text{ V}; I_E = I_e = 0;$ f = 1 MHz	_	_	10	pF

#### Note

1. Pulsed through an inductor of 25 mH;  $\delta$  = 0.5; f = 50 Hz.





### Silicon planar epitaxial overlay transistor

2N3553

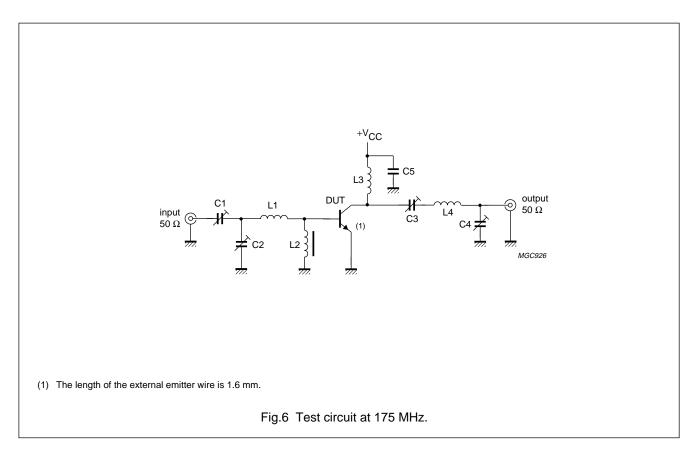
#### **APPLICATION INFORMATION**

RF performance at  $T_{mb}$  = 25 °C.

f	V <sub>CE</sub> (V)	P <sub>o</sub>	G <sub>p</sub>	η
(MHz)		(W)	(dB)	<b>(%)</b>
175	28	2.5	>10	>50

#### Ruggedness

The transistor is capable of withstanding a load mismatch corresponding to VSWR = 3 : 1 varied through all phases, under the conditions:  $V_{CE} = 28 \text{ V}$ ; f = 175 MHz;  $T_{mb} = 25 \,^{\circ}\text{C}$ ;  $P_{o} = 2.5 \text{ W}$ .



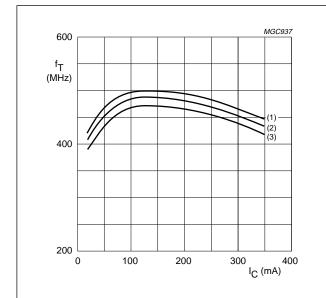
1995 Oct 27 5

### Silicon planar epitaxial overlay transistor

2N3553

#### List of components (see Fig.6)

COMPONENT	DESCRIPTION	VALUE	DIMENSIONS	CATALOGUE No.
C1, C2, C3, C4	air trimmer capacitor	4 to 29 pF		
C5	polyester capacitor	10 nF		
L1	1 turn 1.0 mm copper wire		int. diameter 10 mm; leads 2 × 10 mm	
L2	Ferroxcube choke coil	$Z = 550 \Omega \pm 20\%;$ f = 175 MHz		4312 020 36640
L3	15 turns enamelled 0.7 mm copper wire		int. diameter 4 mm; closely wound	
L4	3 turns enamelled 1.5 mm copper wire		int. diameter 12 mm; leads 2 × 20 mm; closely wound	



T<sub>i</sub> = 25 °C.

- (1)  $V_{CE} = 28 \text{ V}.$
- (2)  $V_{CE} = 14 \text{ V}.$
- (3)  $V_{CE} = 7 V$ .

Fig.7 Transition frequency as a function of collector current; typical values.

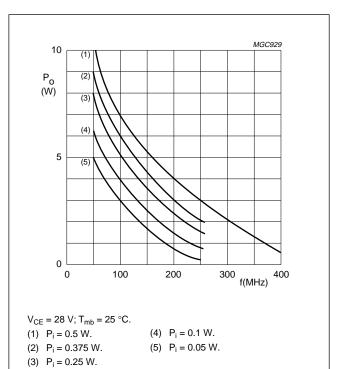
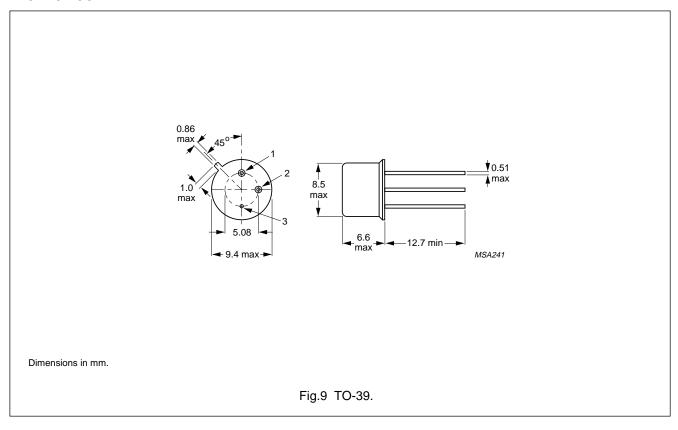


Fig.8 Output power as a function of frequency; typical values.

# Silicon planar epitaxial overlay transistor

2N3553

#### **PACKAGE OUTLINE**



### Silicon planar epitaxial overlay transistor

2N3553

#### **DEFINITIONS**

Data Sheet Status	
Objective specification	This data sheet contains target or goal specifications for product development.
Preliminary specification	This data sheet contains preliminary data; supplementary data may be published later.
Product specification	This data sheet contains final product specifications.
Limiting values	

Limiting values given are in accordance with the Absolute Maximum Rating System (IEC 134). Stress above one or more of the limiting values may cause permanent damage to the device. These are stress ratings only and operation of the device at these or at any other conditions above those given in the Characteristics sections of the specification is not implied. Exposure to limiting values for extended periods may affect device reliability.

#### **Application information**

Where application information is given, it is advisory and does not form part of the specification.

#### LIFE SUPPORT APPLICATIONS

These products are not designed for use in life support appliances, devices, or systems where malfunction of these products can reasonably be expected to result in personal injury. Philips customers using or selling these products for use in such applications do so at their own risk and agree to fully indemnify Philips for any damages resulting from such improper use or sale.

1995 Oct 27