

Lab Name: ECE 3004 Microprocessor and Microcontroller

Expt No: 1(a)

Date: 17-11-2023

ACM: Addition of two 32 bit Numbers.

Sample Input: AX = 70601523H, BH

Sample Output: 07200H = 147123553H.

Registration no: 21BCE10381

Full Signature: Akshay

Choudhary

Memory location	Hex codes	Mnemonics.
0000H 0000H 0000H	75 40 23	MOV 40H, #23
0003H	75 41 15	MOV 41H, #15
0006H	75 42 60	MOV 42H, #60
0009H	75 43 70	MOV 43H, #70
000CH	75 50 30	MOV 50H, #30
000FH	75 51 40	MOV 51H, #40
0012H	75 52 63	MOV 52H, #63
0015H	75 53 77	MOV 53H, #77
0018H	F5 40	MOV R0, #40H
001AH	F5 60	MOV R1, #60H
001CH	F5 04	MOV R2, #04H
001EH	D2 98	SETB R50
0020H	F5 50	MOV R1, #50H.
0022H	C2 98	CLR R50
0024H	C2 97	CLR C

0026H	E540	MOVA, @R0
0028H	D298	SETB R50
002AH	3550	INC ADDCA, @R1
002CH	0550	INCR ₁
002EH	C298	CLRR50
0030H	F560	MOV @R ₁ , A
0032H	0540	INC R0
0034H	0560	INCR ₁
0036H	D504	DJNZ R ₂ , REPEAT
0038H	5006	INC EXIT
003AH	0560	INC @R ₁
003CH	00	EXIT: NOP
003DH	80FE	SJMP \$
003FH		END.

218CE10391

~~Report~~

AKSHAY

CHAUDHARY

Code:-

MOV AX, First-number

MOV DX, Second-number

~~Add~~

ADD AX, BX.

ADD DX, CX

Lab Name: ECE 3004 Microprocessor and Microcontroller.
 Expt No: 1 (6)
 Date: 17-12-2023.
 AIM: Multiply (-09H) with (+02H)
 Sample Input: A = -09H, B = +02H.
 Sample Output: 07200H = -18

21BCE

Registration No-21BCE1281
 Full Signature: Akshay
 Chaudhary.

Memory location	Hex code	Mnemonics.
0000H	75 F0 40	MOV R0, #40H
0003H	E5 00	MOV A, @R0
0005H	08	INC R0
0006H	E5 00	MOV B, @R0
0008H	A4	MUL AB
0009H	08	INC R0
000AH	F5 00	MOV @R0, A
000CH	08	INC R0
000DH	F5 00	MOV @R0, B
000FH		END.

Code:
 MOV A, -09H
 MOV B, 02H
 IMUL B

Lab Name: ECE 3004 Microprocessor and
 Microcontroller

Expt No 1 (d)

Date 17-12-2023

Aim Divide 0034H with 07H

Sample Input A=0034H, B₇=07H

Sample Output a=07, b=03

Registration No: 21BCE10381

Full Signature - Akshay
 Chaudhary

Memory Location	Hexcode	Mnemonics
0000H	7434	MOV A, #24H
0002H	75F007	MOV B, #07H
0005H	84	DIV AB
0006H		END

Code

MOV AX, 0034H

MOV BH, 07H

IDIV BH

Lab Name: ECE3004 Microprocessor and Microcontroller
 Expt No 2
 Date 17-12-2023
 Aim Array Sorting: arrange 5 data bytes in ascending order.
 Sample Input: 010, 004, 002, 001, 006.
 Sample Output: 001, 002, 004, 006, 010

Registration No: 21BCE10381
 Full Signature: AKSHAY
 CHAUDHARY.

Memory Location	Hexcode.	Mnemonics.
0000H.	75 F4 04	MOV R4, #04H
0003H	75 F3 04	MOV R3, #04H
0006H	75 F0 30	MOV R0, #30H
0009H	E5 00	MOV A, @R0
000BH	F5 F0	MOV A, B.
000DH	08	INC R0
000EH	E5 00	MOV A, @R0
0010H	BF 00.	CINE A, B
0012H		JNC SKIP.
0012H	F5 00	MOV @R0, B.
0014H	18	INC R0
0015H	F5 00	DJNZ R4, AGAIN.
0017H	08	STOP: SJMP STOP
0018H	05 F4	END. INC R0.
001AH	80 FE	JNC SKIP
001CH		END

Lab Name: ECE 3004 Microprocessor and microcontroller.

Expt No: 4

Date: 17-12-23

Aim: Block data transfer [a block of 12 data types].

Sample Input: 0A, 0C, 12, 2D, 41, 40, 59, 2D, 41, 28, 16, 29

Sample Output: 0A, 0C, 12, 2D, 41, 40, 59, 4D, 41, 28, 16, 29

Registration No: 21BC E10381

Full Signature: AKSHAY

CHAUDHARY.

Memory location	Hexcode	Mnemonics
0000H	75 F0 30	MOV R ₀ , #30H
0003H	75 F1 40	MOV R ₁ , #40H
0006H	75 F2 10	MOV R ₂ , #10H
0009H	E5 00	MOV A, @R ₀
000BH	F5 01	MOV @R ₁ , A
000DH	08	INC R ₀
000EH	09	INC R ₁
000FH	DE F2	DJNZ R ₂ , REPEAT.

Code:

MAINPROC.

MOV AX, @DATA

MOV DS, AX

LEA SI, Source-block.

LEA DI, dest-block.

MOV CX, 12

CLD

REP MOVSB.

MOV AH, 4CH

INT 21H

Lab Name: ECE 3004 Microprocessor and
Microcontroller

Expt. No.: 5

Date: 17-12-23

Aim: Delay calculation and generation of square
wave at Port P1.0 with 1KHz, f.

Reg. No: 21BCF10381
Full Signature

Code:-

Start: Set P1.0

~~LCALL~~ LCALL DELAY

CLR P1.0

LCALL DELAY

S JMP START.

DELAY: MOV R3, #F0H

L1 DJNZ R3, L1

RET

END :

Q6 Lab Name: ECE 3004 MPMCL

Expt No: 06

DATE: 17-12-2023

Aim: Assume two LED connected to P1.0 P1.1.
 Write a program to blink two LED with a delay
 0.5 ms

Reg. No: 21BCE10381

Full Signature: AKSHAY CHOUDHARY

Code: ~~0500~~

org 0x00.

MAIN: MOV P1, #0x01

CALL DELAY

MOV P1, #0x00

CALL DELAY

MOV P1, #0x02

CALL DELAY

MOV P1, #0x00

CALL DELAY

SJMP MAIN

DELAY:

MOV R2, #0x0A

Outerloop:

MOV R1, #0xFF

INNERloop:

NOF

DJNT R1, INNER_LOOP

DJNT R2, OUTER_LOOP

RET

END

LAB NAME : ECE 3004 MPMC

Expt No : 7

Date 17-12-2023

Aim Delay calculation and generation for square wave at Port 1.0 with K_H using timer.

Reg No : 21BCE10381

Enl Signature : Akshay Choudhary

Code:-

```
org: 0x00  
MOV TMO1 #01h  
MOV TNO #6X3C  
MOV TLO #0XB0  
SET TRO
```

MAIN:

```
JNB TFO, MAIN  
CLP P10  
CLR TFO  
SJMP MAIN
```

END:

Q8) LABNAME: ECE3004 MPML

Lab No: 08

Date: 17-12-2023

Aim: Interfacing LCD Display.

Reg No: 21BCE10381

Full Signature: ARSHAY CHOUDHARY

Theory:

8051 typically involves sending commands and data through the MC I/O ports to control the display.

Assembly Code:

ORG: 0x00

MOV DPTR, #LCD.int

CALL LCD-command

MOV DPTR, 1

CALL LCD-Message

HLT; Halt the processor.

LCD-init: ; LCD initialization routine.

MOV A, #0x38;

CALL LCD-command-Delay.

MOV A, #0x0E;

CALL LCD-command-Delay.

MOV A, #0x06;

CALL LCD-command-Delay.

MOV A, #0x01;

CALL LCD-command-Delay.

RET

Reg No : 21BCE10381

Full Signature : AKSHAY

CHOUDHARY

LCD-Command-Display;

MOV R2, #50

Delay - C1:

DJNZ R2, Delay - C1

RET.

LCD-Message;

MOV R0, #0;

Message-Loop:

MOVA, Message[R0];

INC R0;

JZ Message-End;

CALL LCD-Delay;

CALL LCD-Data;

JMP Message-Loop;

Message-End:

RET.

LCD-Command:

MOV P1, A;

CLR RS; RS = 0 for command

DB

CLR RW;

CLR RW;

SET EN;

CLR EN;

RET.

OUTPUT :-> DB "Hello, LCD!", 0; Null terminated string

Q9 LabNAME : ECE 3004 MPMC
 Lab No: 09
 Date : 17-12-2023
 Aim: Interfacing with Keypad

Reg No: 21BCE10381
 Full Signature: ANSHAY CHOUDHARY

Theory: In 8051 we read the key presses from the keypad and perform action based on pressed keys.

Code:

```
ORG 0x00
MOV DPTR, # Keypad-Init
CALL Keypad-Command.
```

```
*MOV DPTR
MAIN-LOOP:
    CALL Read-Keypad
    JZ MAIN-LOOP;
```

```
MOV A, P3;
CALL Display-Key.
```

```
JMP MAIN-Loop;
HLT;
```

```
Keypad-Init:;
MOV P2, # 0x F0;
RET
```

Read-Keypad;

MOV A, #0xFF;

MOV P₂, #0x0F;

NO P;

•

MOV A, P₂;

CPL A;

MOV R₁, A;

MOV P₂, #0xF0;

NO P;

• MOV A, R₁;

ORL A, R₁;

RET

Display-Key;

MOV P₁, A;

CALL Delay

CLR P₁;

RET

Delay::

MOV R₂, #50

Delay-Loop:

DJNZ R₂, Delay-Loop

RET.

ORG 0x30

Main Loop:

JMP MainLoop

Q10 LabNAME : ECE3004 WPMU
 Lab No: 10
 Date: 17-12-2023
 Aim: Programming ADC.

Reg No: 21BCE10321
 Full signature: AKSHAY CHOUDHARY

Theory:- It involves conversion of analog signals using the ADC and generating analog signals using the DAC.

Code:

```
ORG 0x00
MOV DPTR, #ADC-Init
CALL ADC-command.
```

MAIN-LOOP:

```
CALL ADC-Read;
CALL DAC-Read;
JMP MAIN-LOOP;
HLT;
```

```
ADC-Init;
MOV P1, #0xFF
RET.
```

```
ADC-Read;
MOV P1, #0xFF;
RET
MOV A, P1;
```



```
MOV P1, A;  
RET
```

```
DAC - write::  
MOV P2, A;  
CALL Delay;  
RET.
```

```
Delay: ;  
MOV R2, #50
```

```
Delay-Loop:  
DJNZ R2, Delay-Loop  
RET
```

```
ORG 0x30.
```

Q11

LAB NAME : 2PECE2004MPMC

Lab No : 11

Date : 17-12-2003

Aim : Interfering with stepper motor.

Reg No: 21BCE10381

Full Signature: AKSHAY CHOUDHARY

Code:

ORH 0x00

MOVDPR, # stepper-Init

CALL Stepper-Command

MAIN_LOOP:

MOV A, # 0x01;

CALL Stepper-MOVE

CALL Delay.

MOV A, # 0x02;

CALL Stepper-MOVE

CALL Delay.

MOV A, # 0x03;

CALL Stepper-MOVE

CALL Delay.

MOV A, # 0x04;

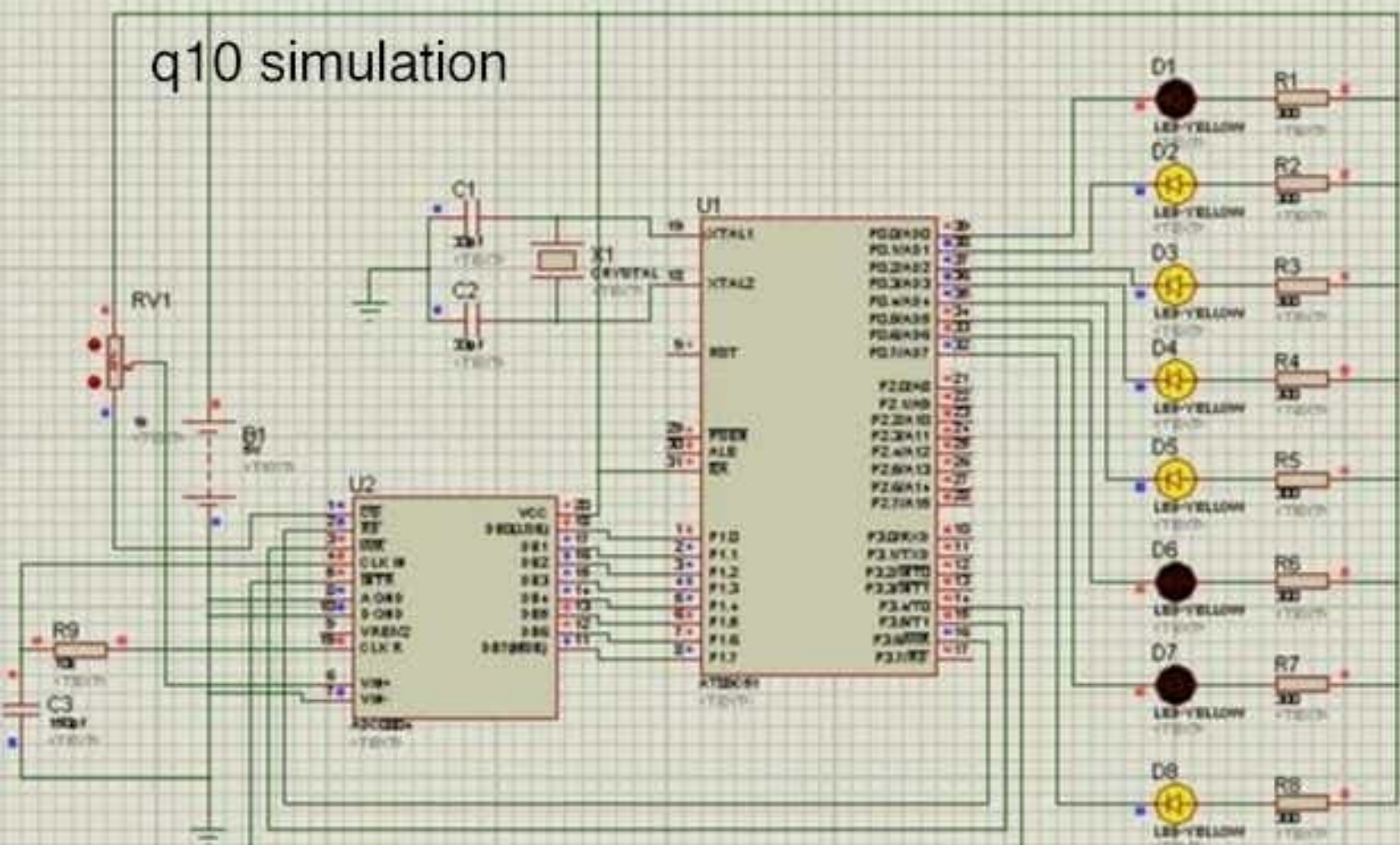
CALL Stepper-MOVE

CALL Delay

JMP MAIN_LOOP;

HLT;

q10 simulation




```

Stepper - Init: ;
    MOV P1, #0x00;
    MOV P2, #0x00;
    
```

```

Stepper - Move:;
    MOV P2, A;
    CALL Delay;
    RET
    
```

```

Delay:;
    MOV R2, #50
    
```

```

Delay - Loop:
    DJNZ R2, Delay - Loop.
    RET.
    ORH 0x30.
    
```

