

31 0 0 0 0 0 0 0 0 32 3 3 3 3 3 3 3 3

1 1 1 1 1 1 1 1 3 3 3 3 3 3 3 3

2 2 2 2 2 2 2 2 4 4 4 4 4 4 4 4

2 2 2 2 2 2 2 2 5 5 5 5 5 5 5 5

0 0 0 0 0 0 0 0



33 5 5 5 5 5 5 5 5 34 3 3 3 3 35 0 36

5 5 5 5 5 5 5 5 3 3 3 3 1

5 5 5 5 5 5 5 5 4 4 4 4 2

7 7 7 7 7 7 7 7 5 5 5 5 2

5 5 5 5 5 5 5 5

$\text{♩} = 120$ $\text{♩} = 125$



37 $\text{♩} = 120$ 38 $\text{♩} = 175$

1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1



39 3 3 3 3 3 3 3 3 3 3 3 3 3 3 3



40 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0



41 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0



42 3 3 3 3 3 3 3 3 1 1 1 1 1 1 1 1



43 3 3 3 3 3 3 3 3 3 3 3 3 3 3 3 3

0 0 0 0 0 0 0 0 X X X X



48

[illegible][illegible]

53			54			55			56		
			3			3	3	6			
2	2	2	3			3	3	6			
2	2	2	3			4	4	7	2	2	2
0	0	0	1			5	5	8	2	2	2
						5	5	8	0	0	0
						3	3	6			

57		58		59	
2	2	3	3	3	2
2	2	3	3	3	2
0	0	1	1	1	0

Diagram illustrating a 120-bit bus structure, divided into two main sections: 60 bits and 61 bits.

The 60-bit section (left) shows a sequence of 10 zeros (0) distributed across the bus lines.

The 61-bit section (right) shows a sequence of values (7 and 5) distributed across the bus lines, with some values grouped by brackets below them.

62 63

Measure 62: 3, 3, 1

Measure 63: 5 3 3, 3 3 3, 3 3 3, 3 3 3, 3 3 3, 3 3 3

64

Measure 64: 7 5, 5 5, 5 5, 7 5, 5 5, 5 5, 5 5

65

Measure 65: 7 7 5, 5 5, 5 5, 5 5, 7 7 5, 5 5, 5 5, 5 5

66

Measure 66: 1 1 1 1, 1 1 1 1, 1 1 1 1, 1 1 1 1

67

Measure 67: 5 3 3, 3 3 3 3, 5 3 3, 3 3 3 3

68

Measure 68: 2 2 0, 0 0, 0 0, 0 0, 2 2 0, 0 0, 0 0, 0 0

69

Measure 69: 0 0 0 0, 0 0 0 0, 0 0 0 0, 0 0 0 0

70

Measure 70: 1 1 1 1, 1 1 1 1, 1 1 1 1, 1 1 1 1

72

73

74

75

76

77

78

80

81

82

83

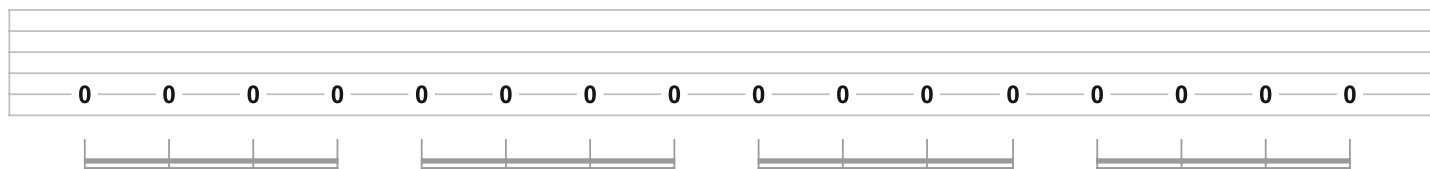
84

85

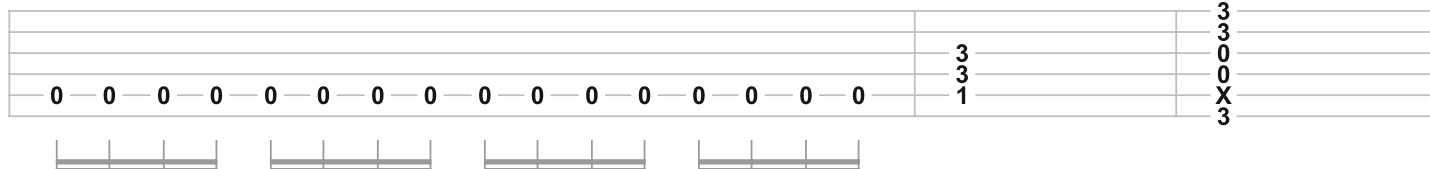
86

87

88

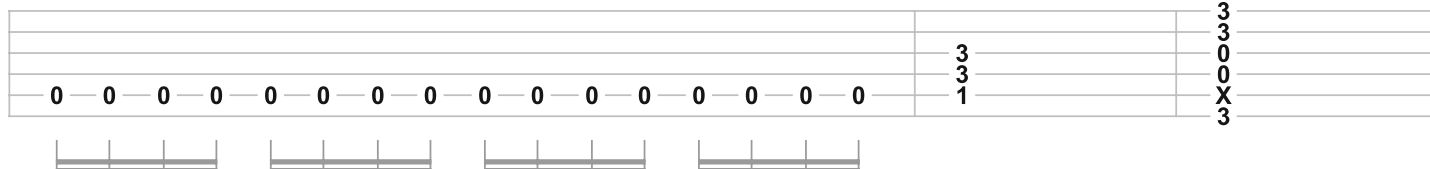


89

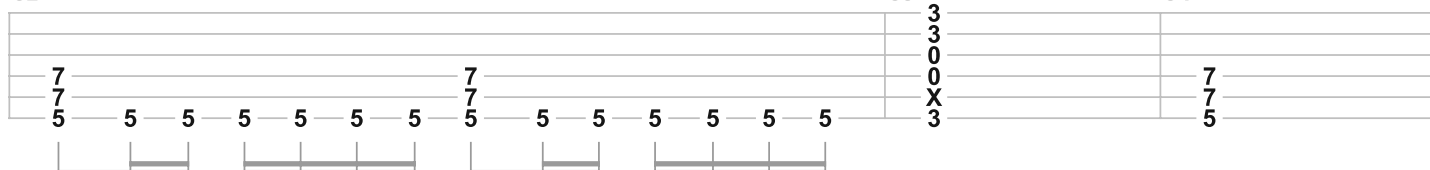


90

91

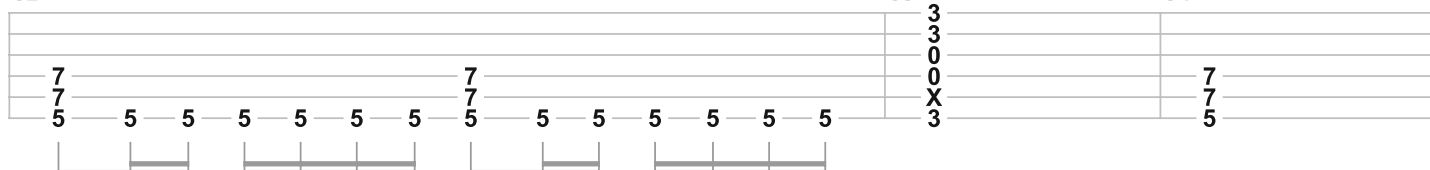


92



93

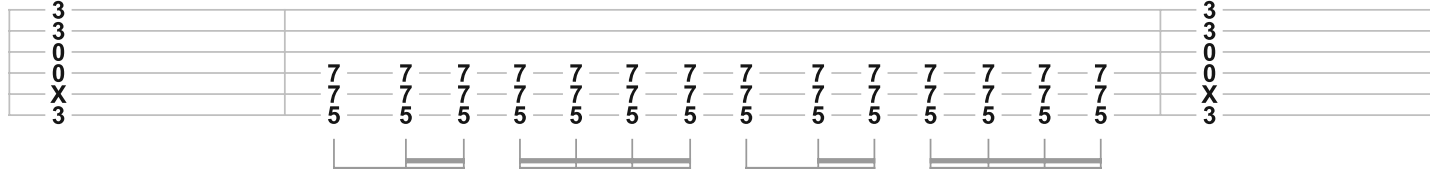
94



95

96

97



98

99

100

101



♩ = 120

♩ = 100

102

103

104

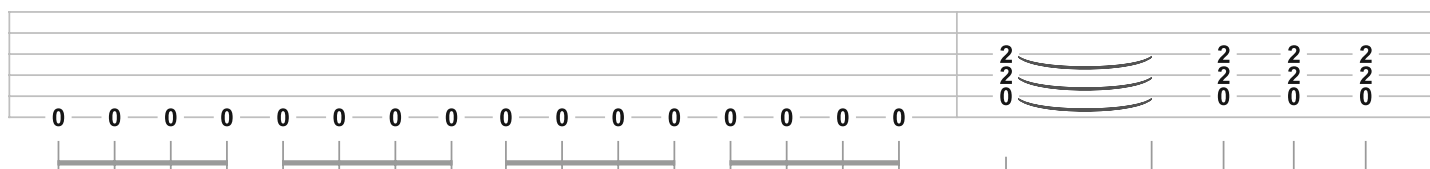
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