1 cont.

NOTE: no need to lock the task, as we do not access staff COMSW4118-16-2 that can change (ex. cmd). The tasklist lock ... wht. protects the task list, hence the parent/ 46/mg modes NOTE: fash_struct.pid is the internal liented PID, what struct proc_struct ps; Ps. Pid = partask-pid (p); Ps. Panent = pid = task-pid-ur (P > panent -> pid); you need is what the user will see attachers which translates if to the appropriate architecture type, Busically, what task-pid-nr does is translate it to PS. nice = task-me(p); the lister names pace (Which probably is the same). read_unlock (tasklist_lock); if (copy-to-user (proc, sps, sited(ps))) } return - EFAULT; letum 0; struct procestruct ps; • To call the system call: 8yscall (_NRpmfo, 1,2ps); • Timer interrupt handler (Q2 ou sample midtern): a) COUNT IS being incremented by the hardware You can set it atomically to so and read it atomically, But not both! If you update it, you will likely ship microsecond wants.
The solution in a) won't work. 6) 64 lests will be overflown on 564 years or so I five just heep adding, so just moreasing count by I every microsecond is ok! Note that COUNT -= COMPARE is not a tomic and won't work. By 1, of course). While (comparer <= COUNT) { NOTE: It will work some it is sufficient COMPARE += 1000; for the COMPARE to Ge < COUNT for Some Tlautt; point in time. This will guarantee that the HW will generate an interrupt when • TRUE/ FALSE guestions (Q1 on sample) COUNT becomes == COMPARE and the lightles a) TRUE FALSE!

6) "movitor mode" "kernel mode" "supervisor mode" will be eventually called again, even though a high priority interrupt keeps it from completion right after exiting the volume (I) change to monitor made is NOT projected! C) FALSE (I/O is generally privileged) 1 sop (the mterrupt flag is set & handler d) TRUE e) TRUE f) FALSE g) TRUE will be called ASAP agourn).