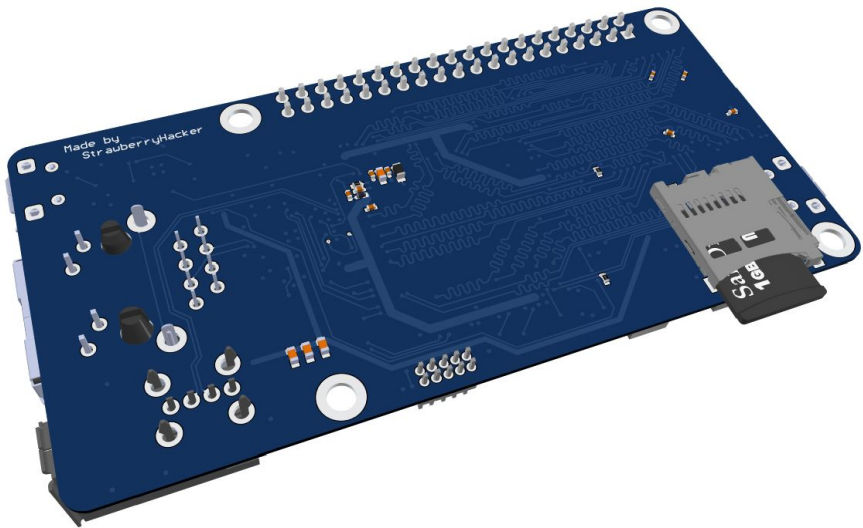
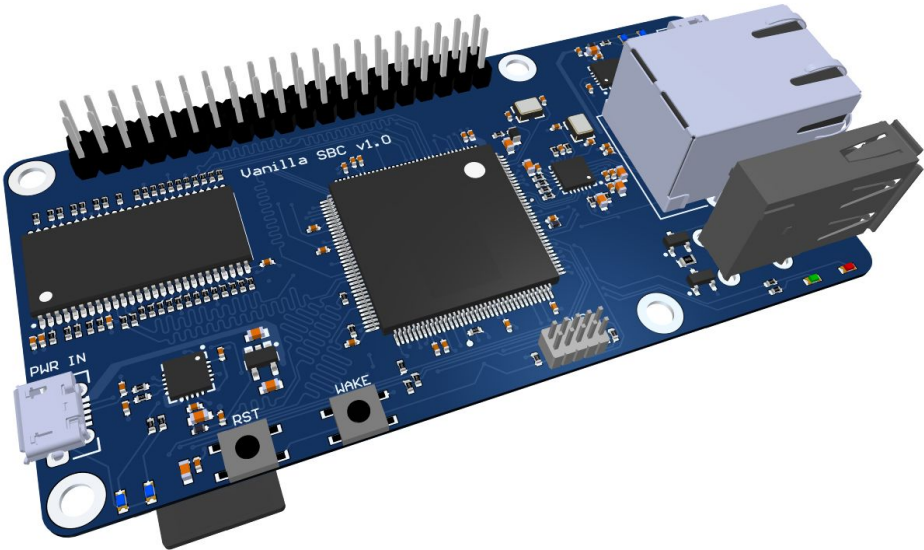


Vanilla SCB

Page	Index
1	COVER PAGE
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3	CPU POWER
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5	CPU IO
6	DRAM
7	ETHERNET
8	USB
9	SD CARD
10	LEDS, BUTTONS
11	POWER
12	CONNECTORS



Design considerations

DESIGN NOTE

Example text for informal design notes

DESIGN NOTE

Example text for critical design notes

DEBUG NOTE

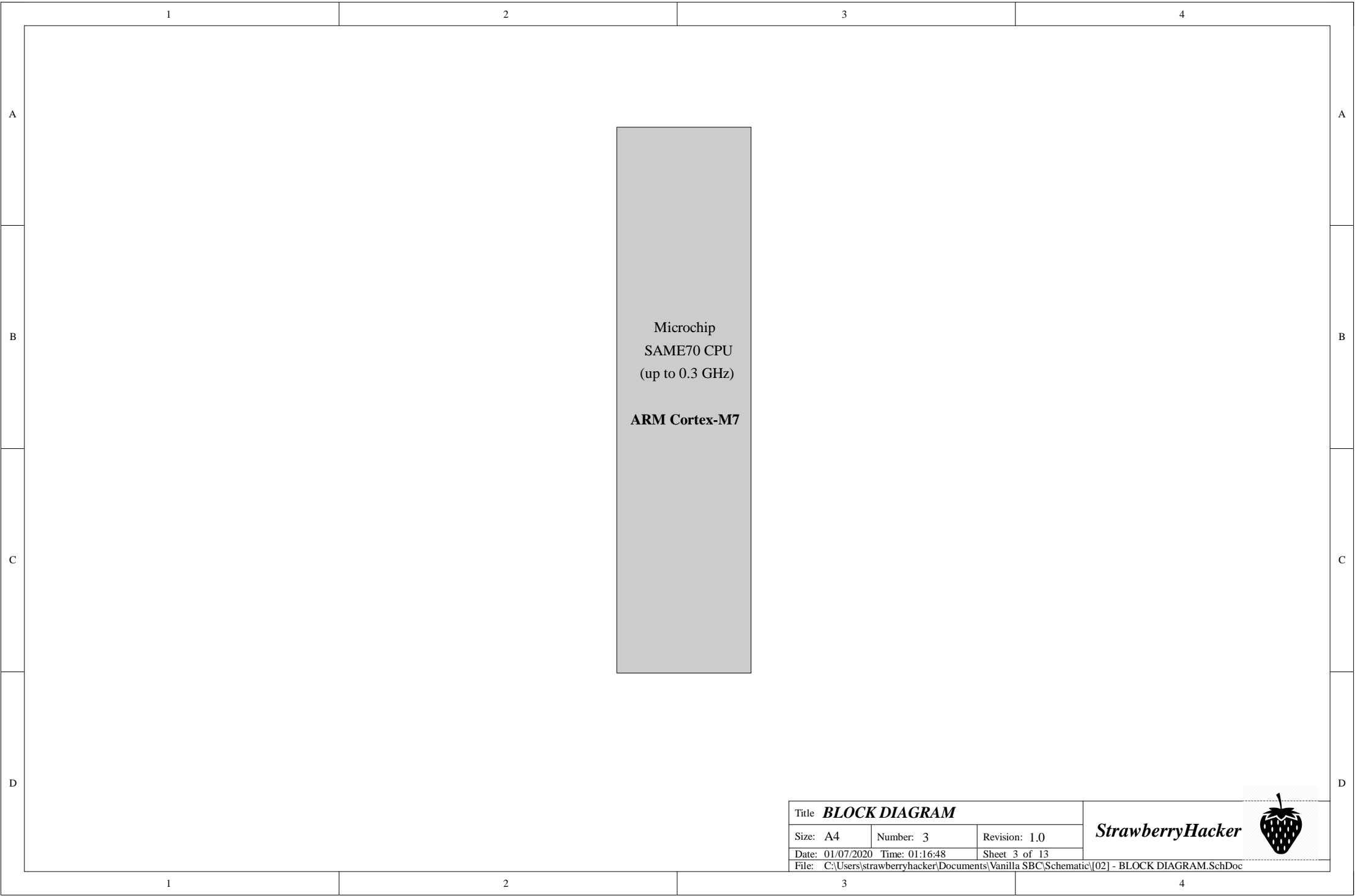
Example text for debug notes

LAYOUT NOTE

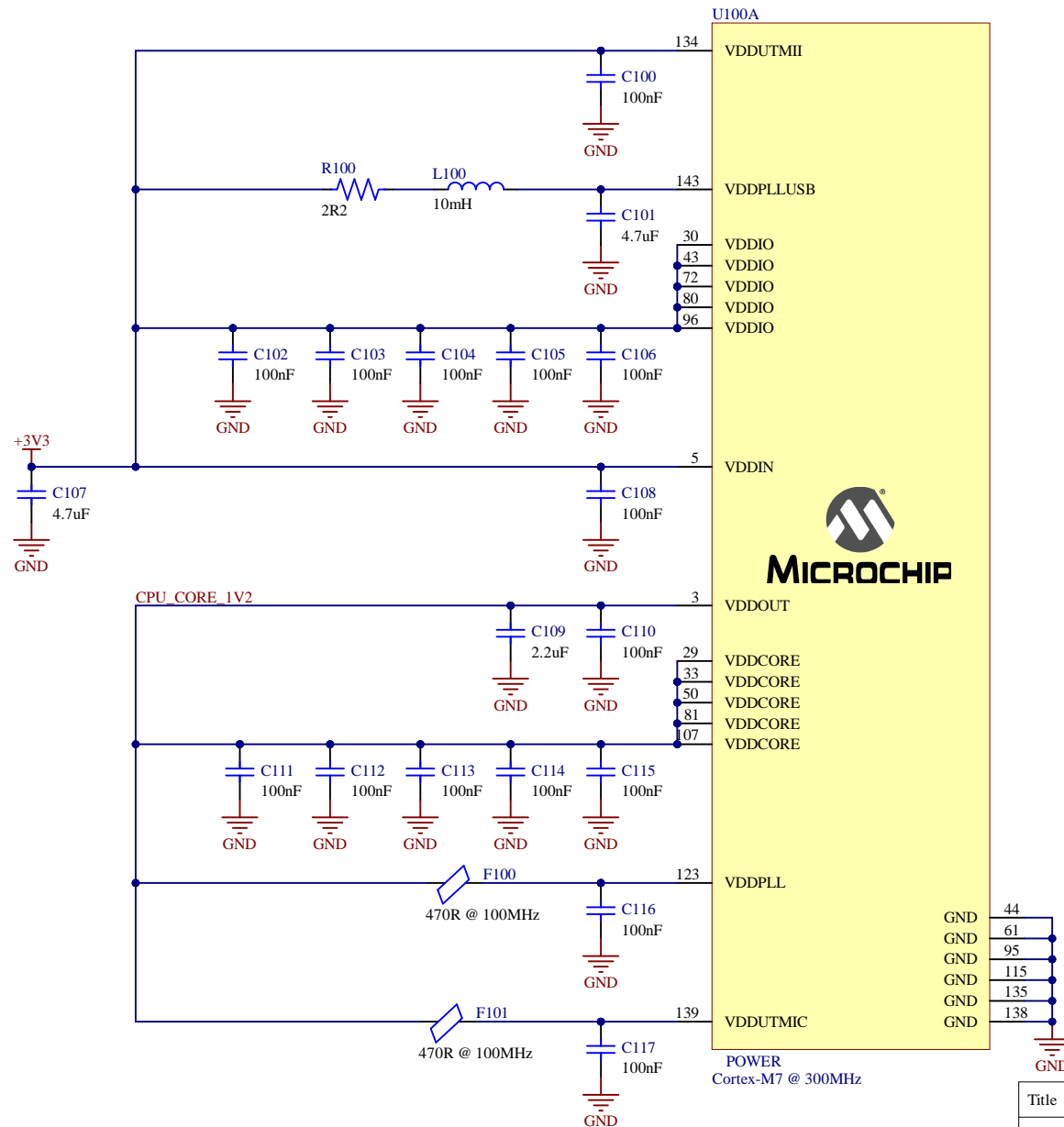
Example text for layout notes

DESIGN NOTE

Example text for cautionary design notes



CPU POWER



Title **CPU POWER**

Size: A4

Number: 4

Revision: 1.0

Date: 01/07/2020 Time: 01:16:49

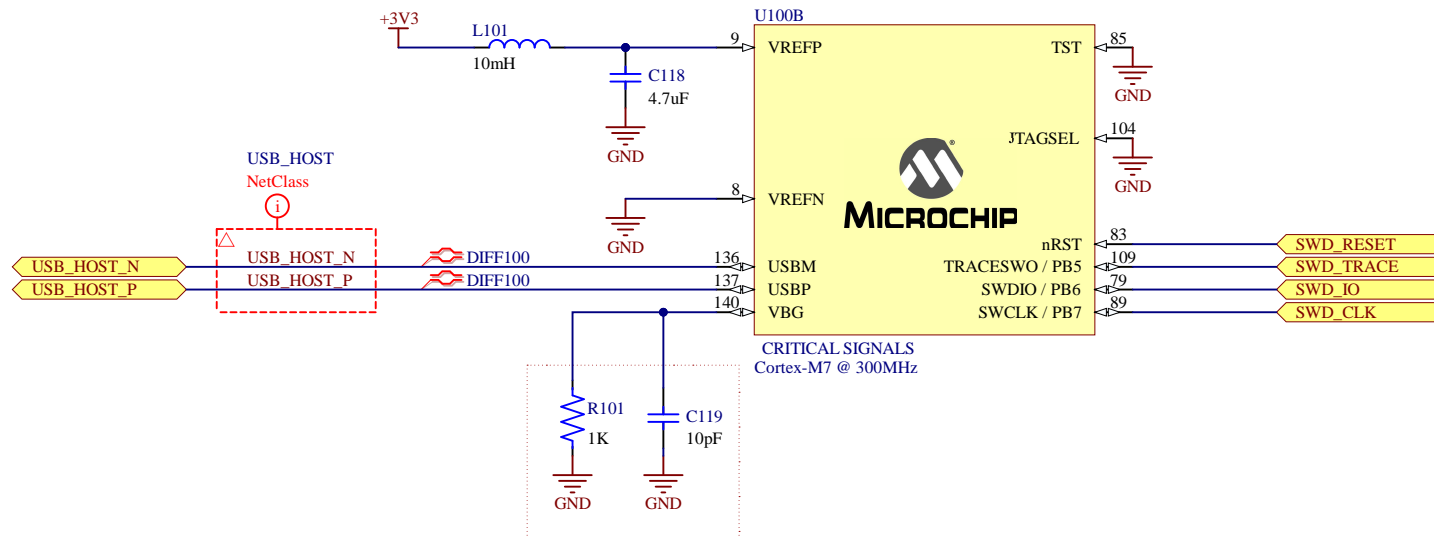
Sheet 4 of 13

File: C:\Users\strawberryhacker\Documents\Vanilla SBC\Schematic\03] - CPU POWER.SchDoc


StrawberryHacker



CPU SIGNALS

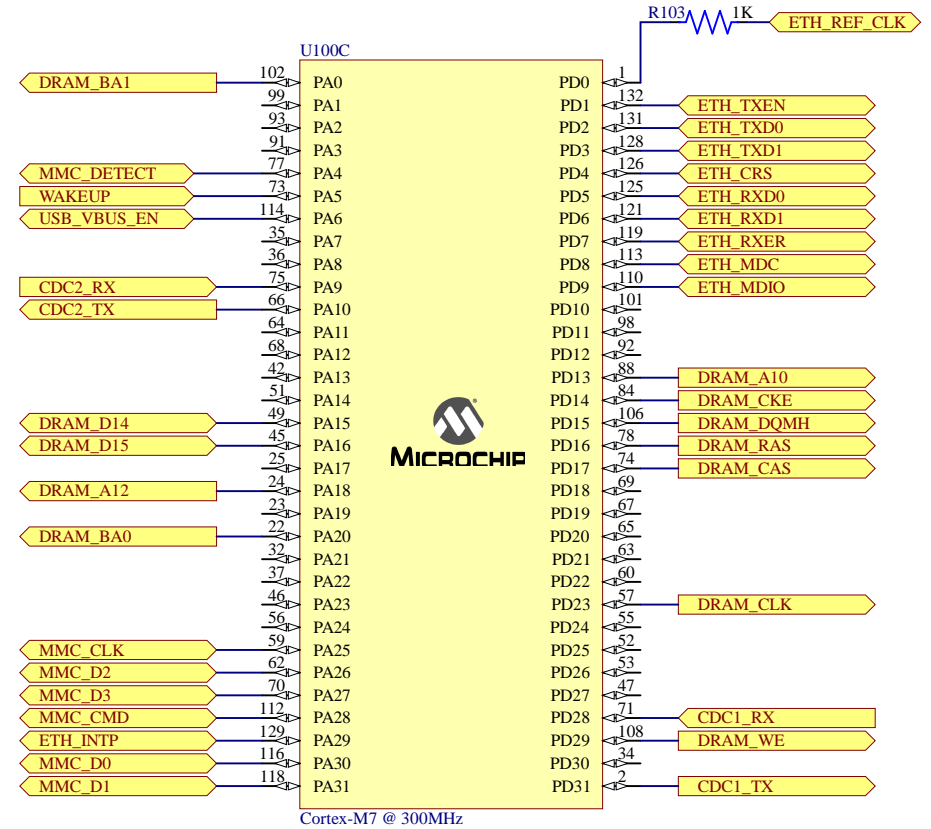
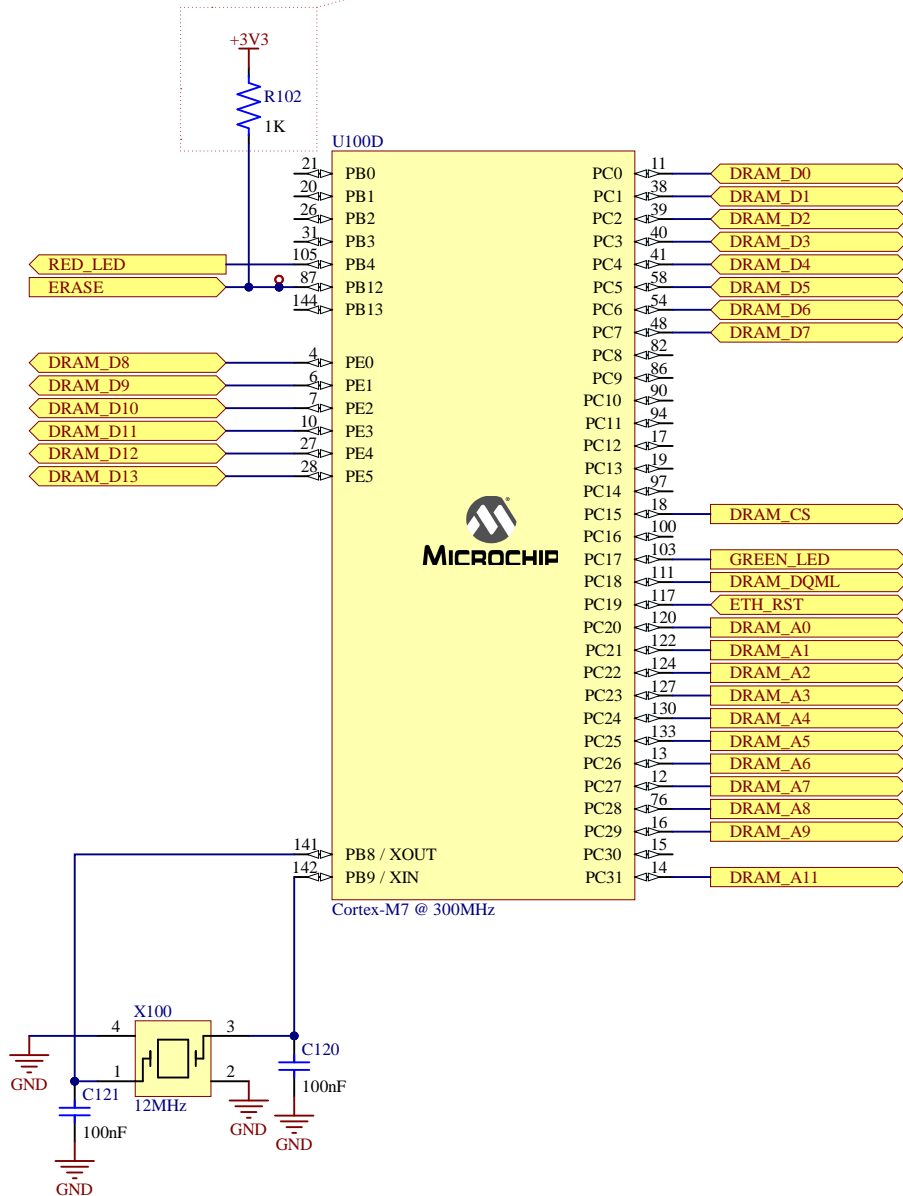


DESIGN NOTE
Bias voltage reference for the USB host

Title <i>CPU SIGNALS</i>			<i>StrawberryHacker</i> 
Size: A4	Number: 5	Revision: 1.0	
Date: 01/07/2020	Time: 01:16:50	Sheet 5 of 13	
File: C:\Users\strawberryhacker\Documents\Vanilla SBC\Schematic\04 - CPU SIGNALS.SchDoc			

CPU IO

DESIGN NOTE
Low level at startup must be preserved



Title **CPU IO**

Size: A4

Number: 6

Revision: 1.0

Date: 01/07/2020 Time: 01:16:51

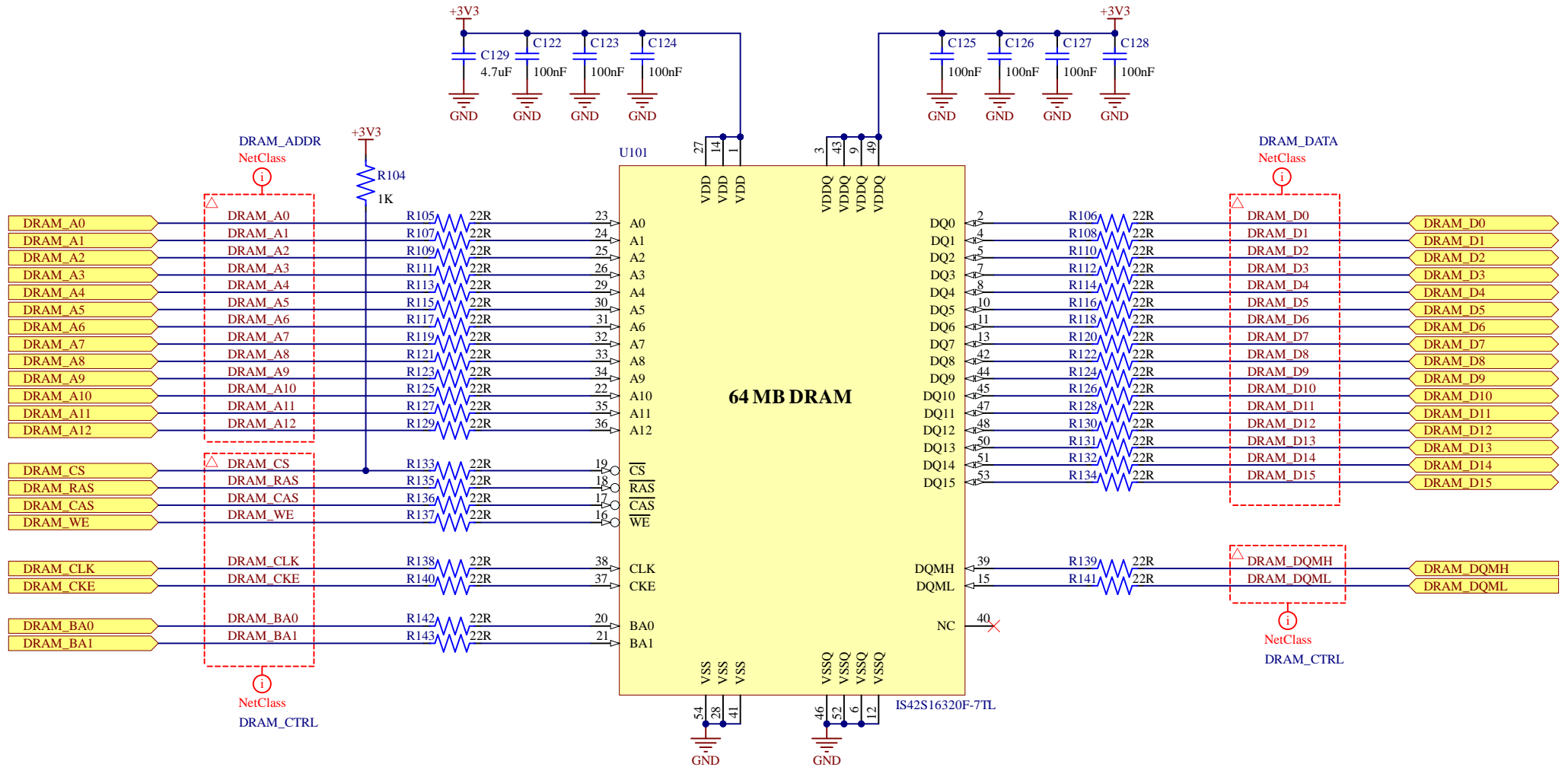
Sheet 6 of 13

File: C:\Users\strawberryhacker\Documents\Vanilla SBC\Schematic\05] - CPU IO.SchDoc

StrawberryHacker



DRAM



Title **DRAM**

Size: A4

Number: 7

Revision: 1.0

Date: 01/07/2020

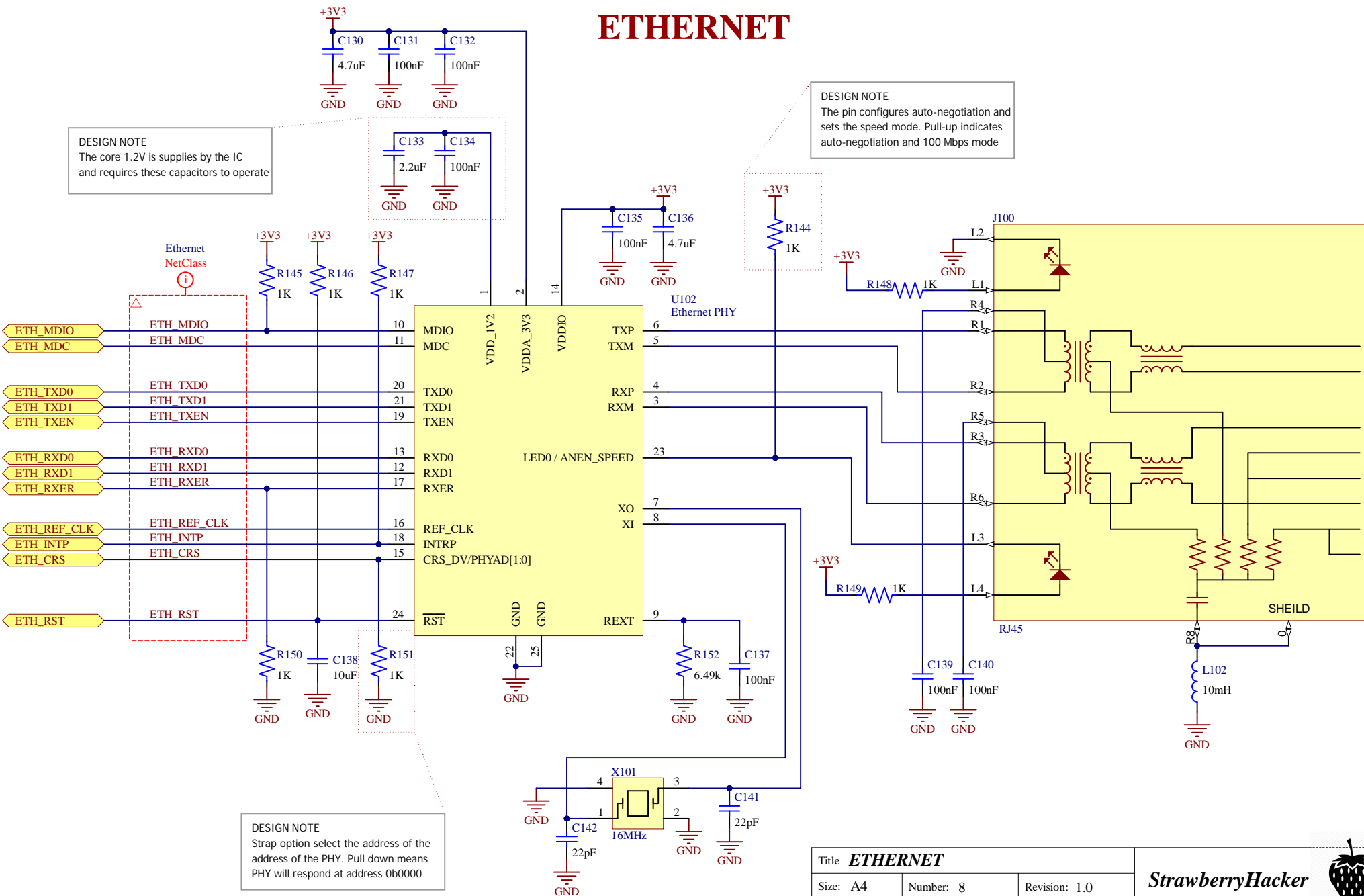
Time: 01:16:52


Sheet 7 of 13

File: C:\Users\strawberryhacker\Documents\Vanilla SBC\Schematic\06] - DRAM.SchDoc

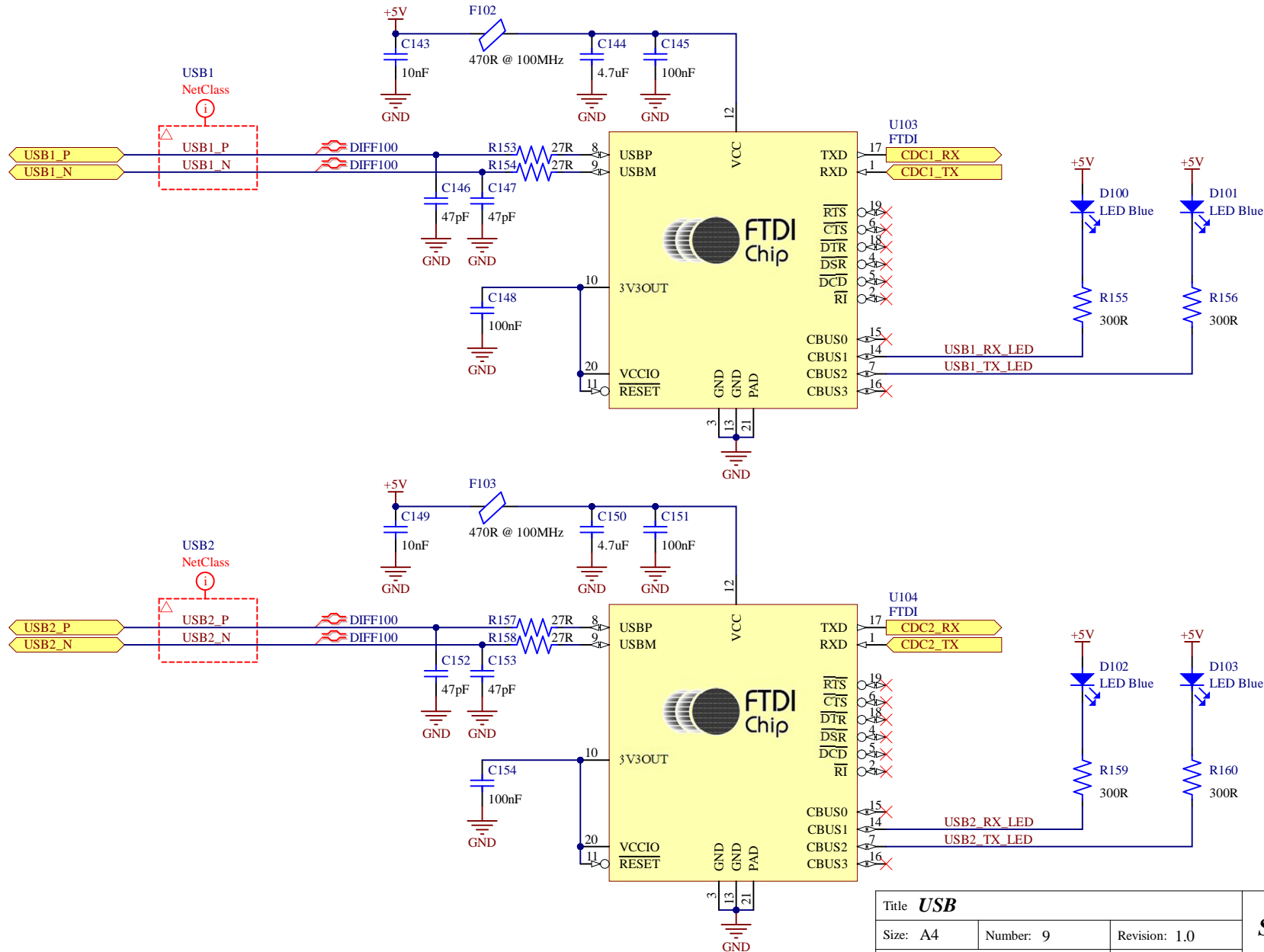
StrawberryHacker





Title <i>ETHERNET</i>			<i>StrawberryHacker</i> 
Size: A4	Number: 8	Revision: 1.0	
Date: 01/07/2020	Time: 01:16:54	Sheet 8 of 13	
File: C:\Users\strawberryhacker\Documents\Vanilla SBC\Schematic\07 - ETHERNET.SchDoc			

USB



Title **USB**

Size: A4

Number: 9

Revision: 1.0

Date: 01/07/2020 Time: 01:16:55

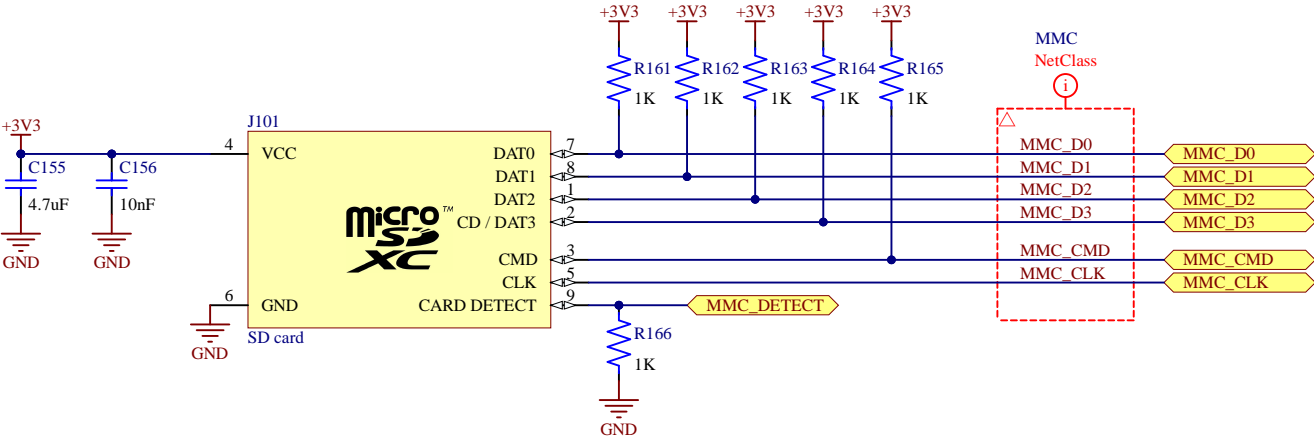
Sheet 9 of 13

File: C:\Users\strawberryhacker\Documents\Vanilla SBC\Schematic\08] - USB.SchDoc

StrawberryHacker



SD CARD



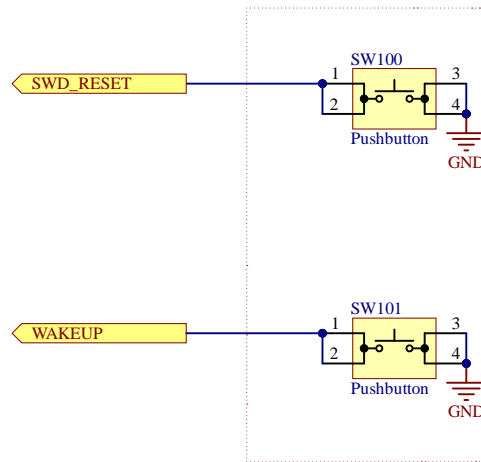
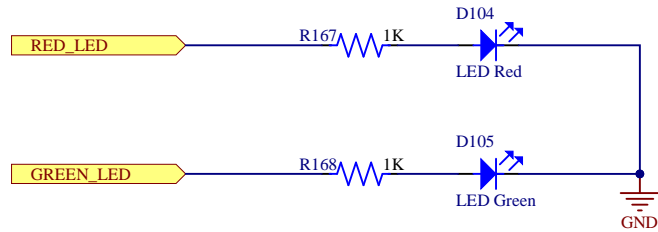
LEDS, BUTTONS

DESIGN NOTE
The red LED will indicate power to the and should allways be turned on by the software

DESIGN NOTE
The green LED will indical CPU load

DESIGN NOTE
Resets the chip

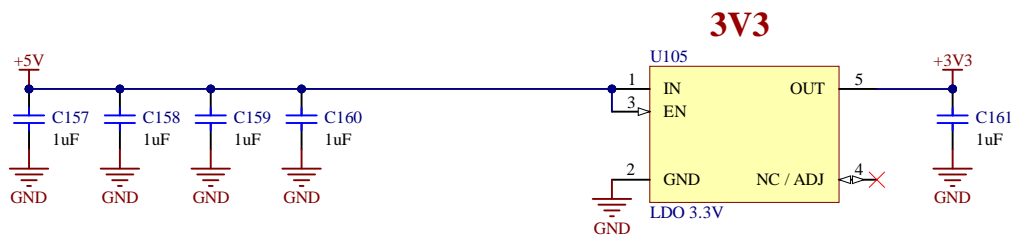
DESIGN NOTE
Wakes the chip up from powerdown



DEBUG NOTE
No filter is needed. If the button bounces, the gpio can be configured with internal filter

Title LEDS, BUTTONS			StrawberryHacker
Size: A4	Number: 11	Revision: 1.0	
Date: 01/07/2020	Time: 01:16:57	Sheet 11 of 13	
File: C:\Users\strawberryhacker\Documents\Vanilla SBC\Schematic\10] - LEDS, BUTTONS.SchDoc			

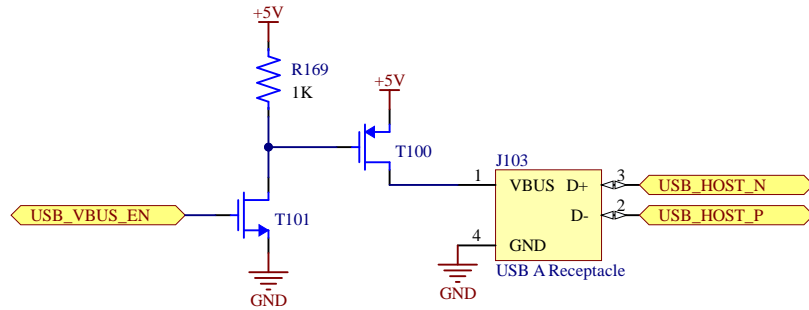
POWER



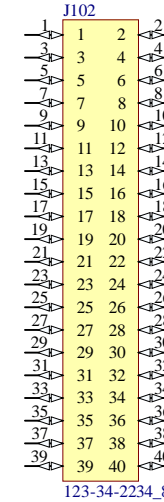
DESIGN NOTE
3V3 LDO regulator able to supply up to 450mA

CONNECTORS

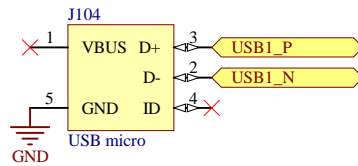
USB HOST



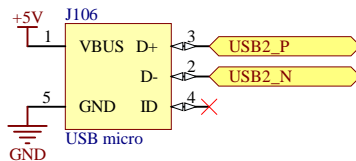
GPIO



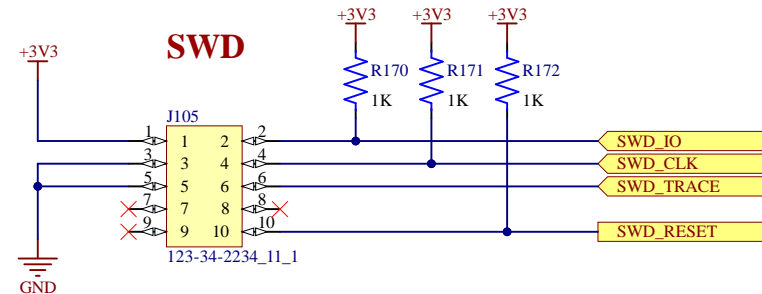
USB CDC 1



USB CDC 2 / POWER



SWD



Title **CONNECTORS**

Size: A4

Number: 13

Revision: 1.0

Date: 01/07/2020 Time: 01:16:59

Sheet 13 of 13

File: C:\Users\strawberryhacker\Documents\Vanilla SBC\Schematic\12] - CONNECTORS.SchDoc

StrawberryHacker

