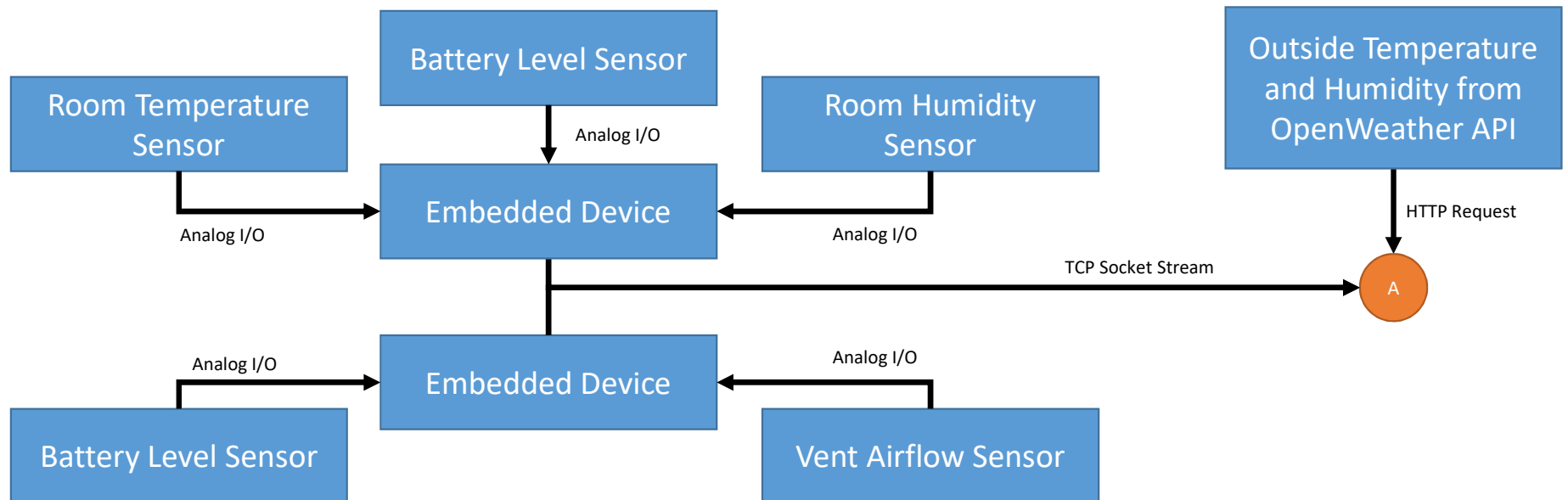


Design Level 2 Diagram

Input



Design Level 2 Diagram (continued)

Processing

Output

