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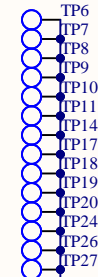
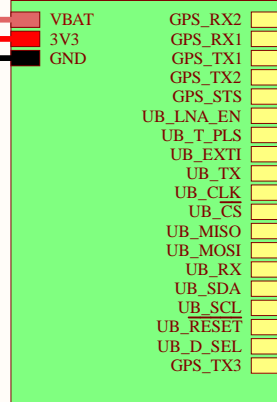
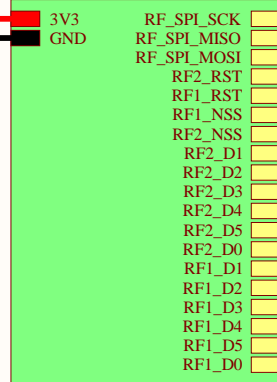
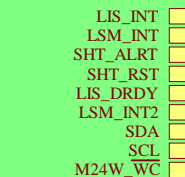
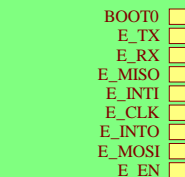
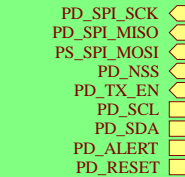
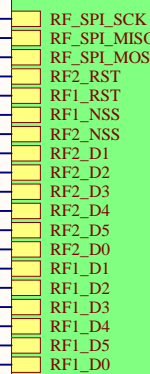
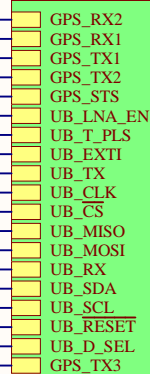
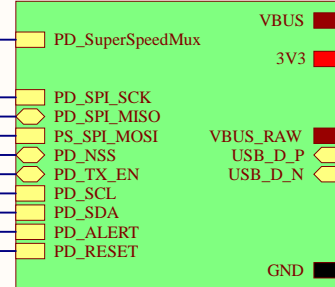
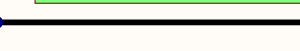
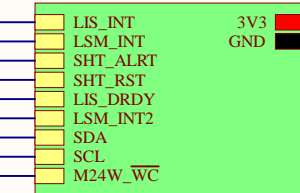
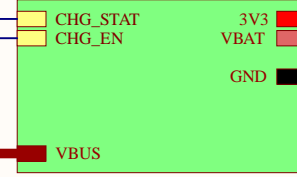
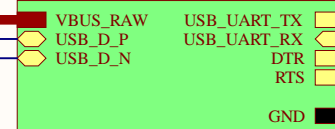
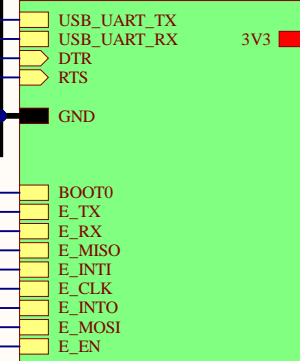
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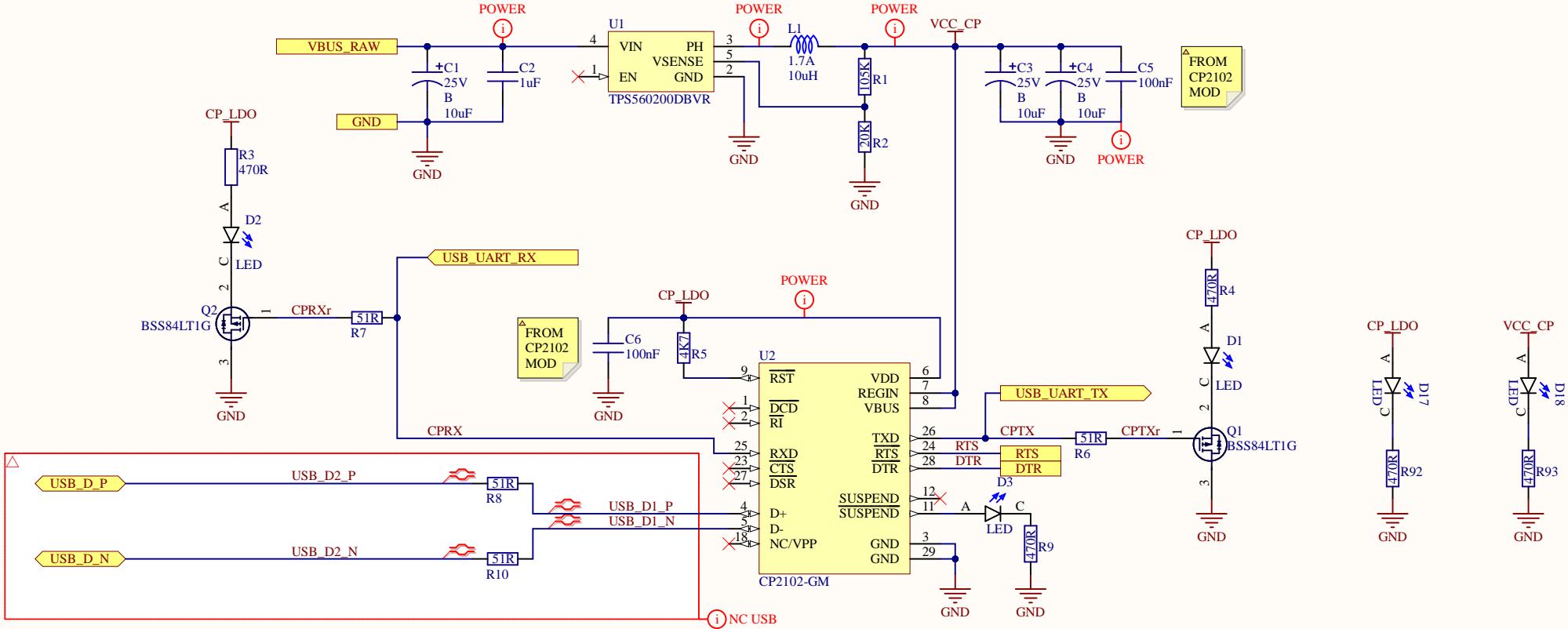
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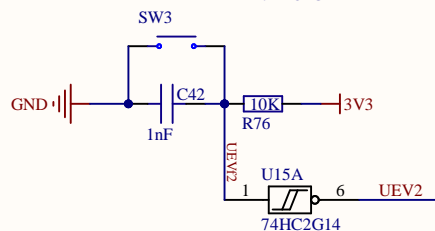
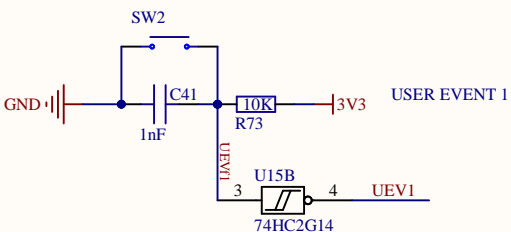
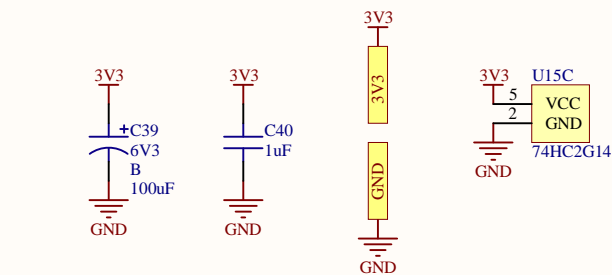
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06-LORA.SchDocU_05-STM32
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UART

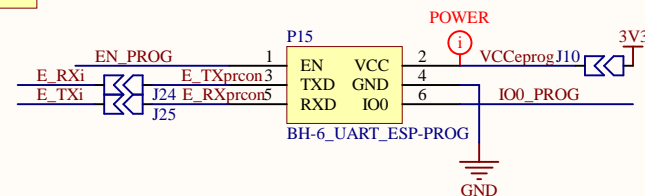
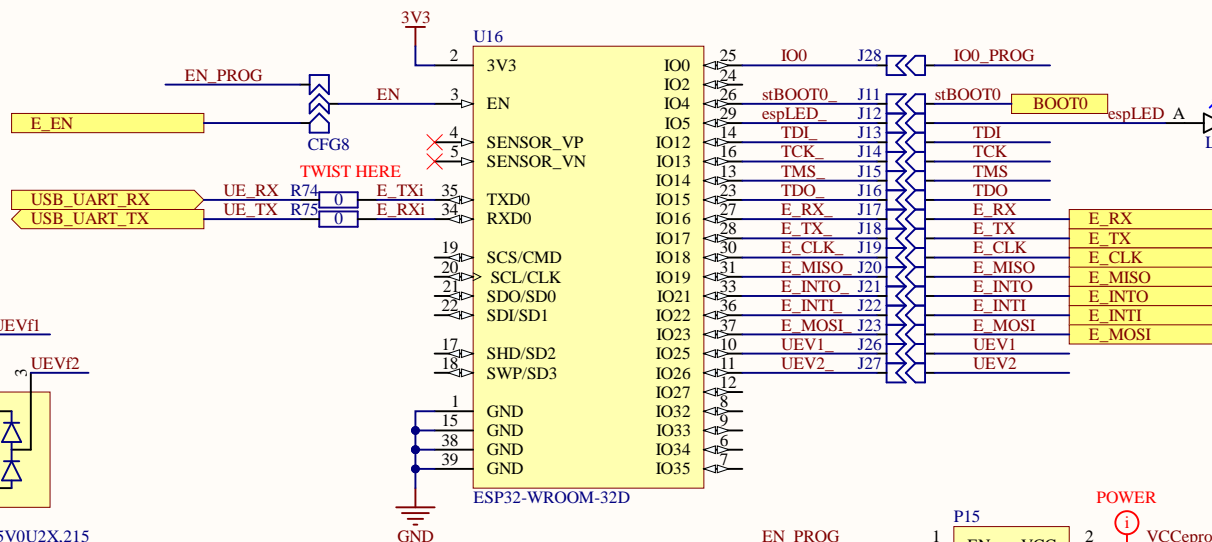
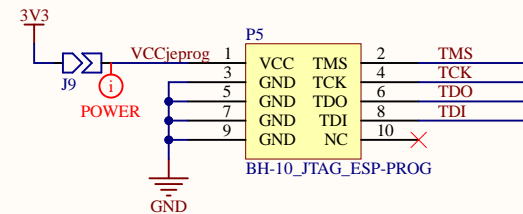
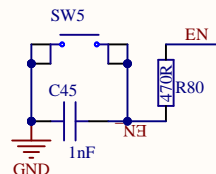
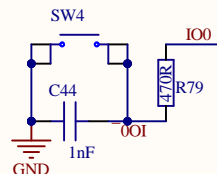
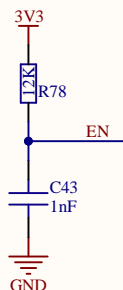
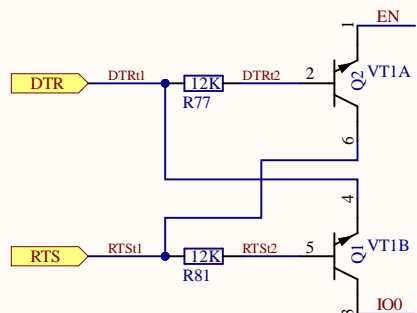


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ESP32

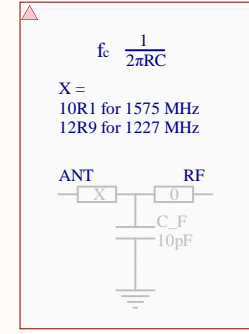
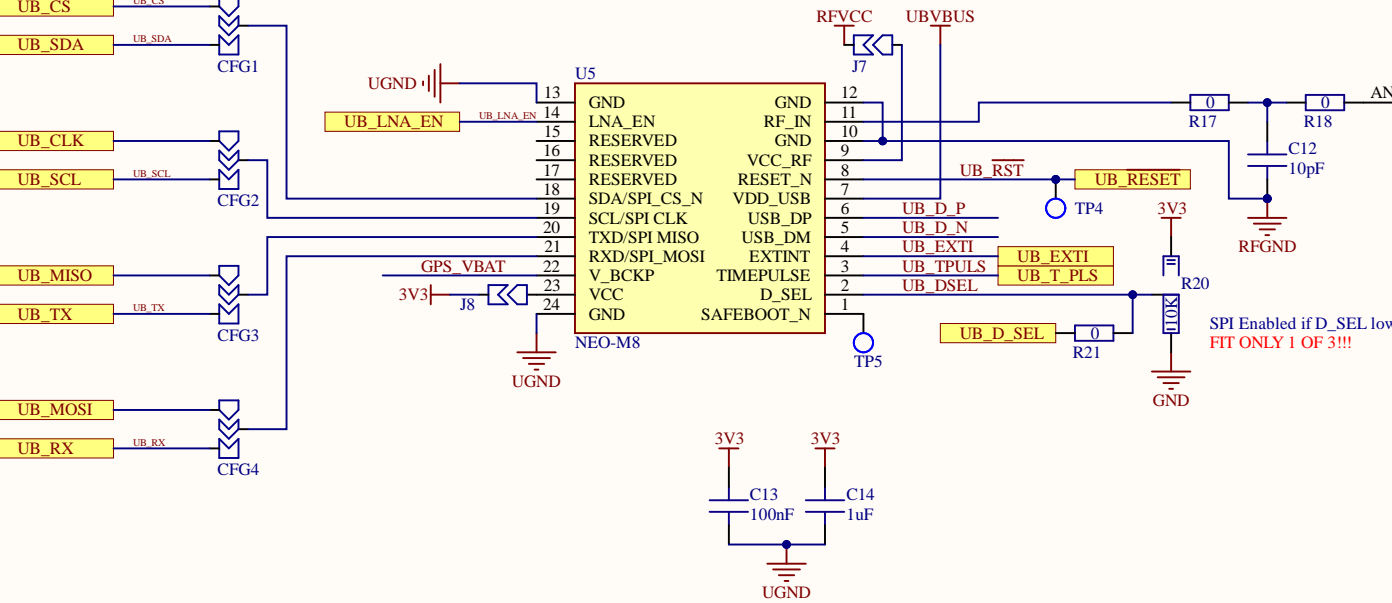
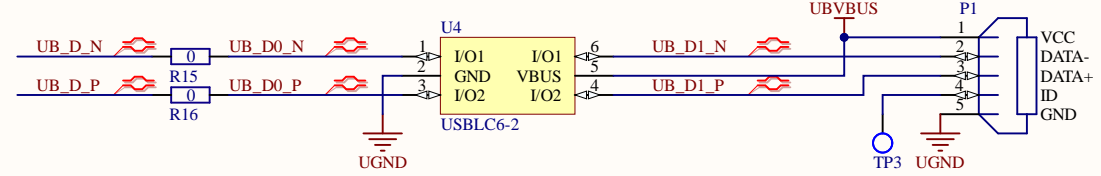
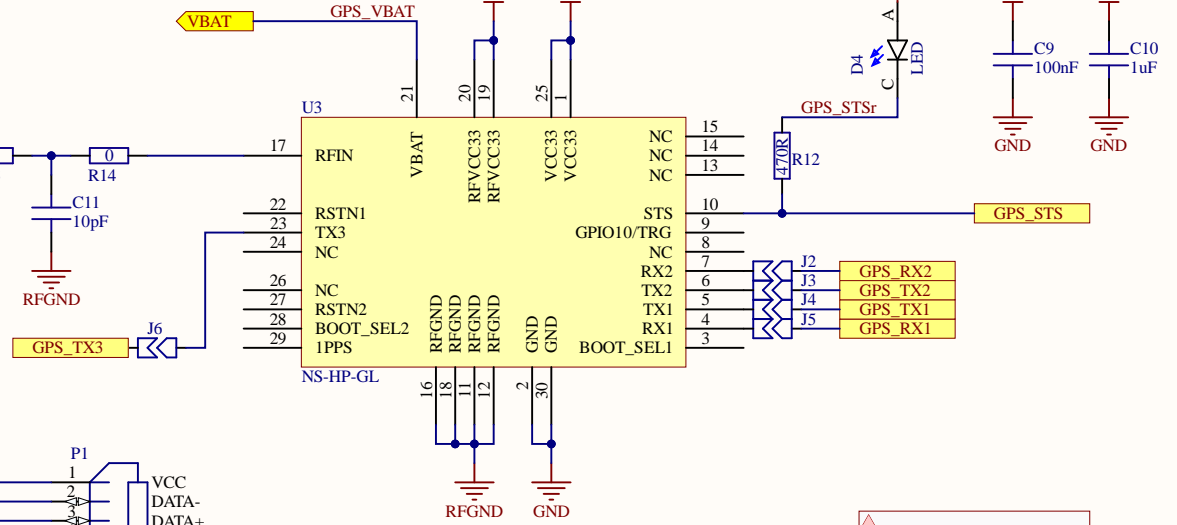
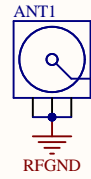
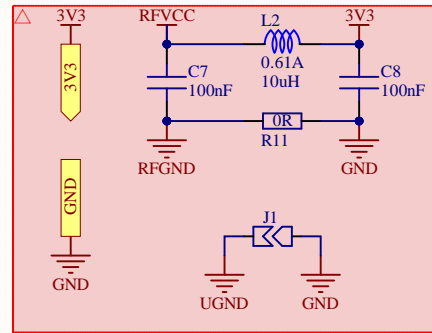


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DTR RTS -> EN IO0			
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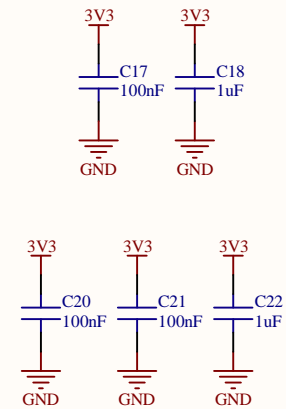
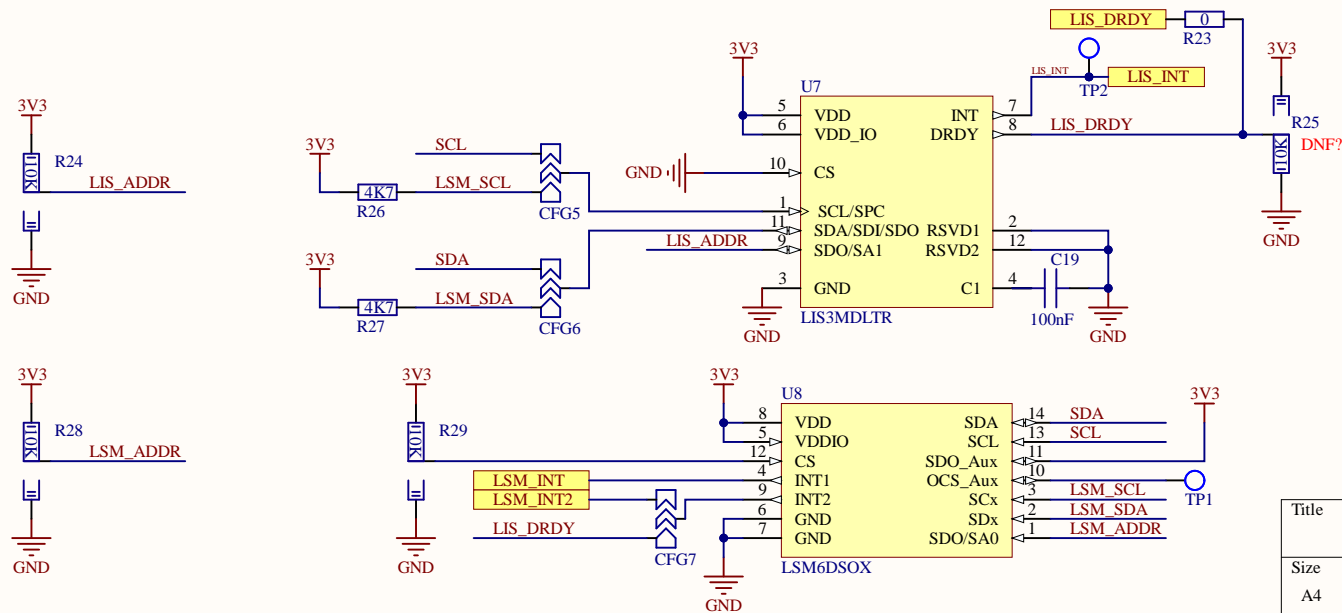
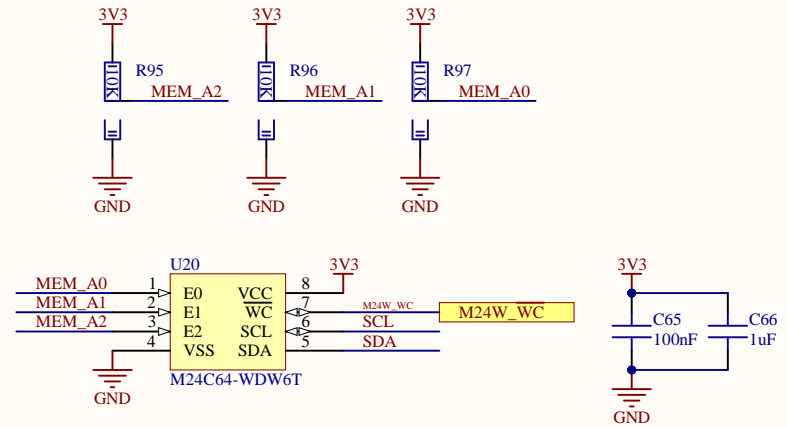
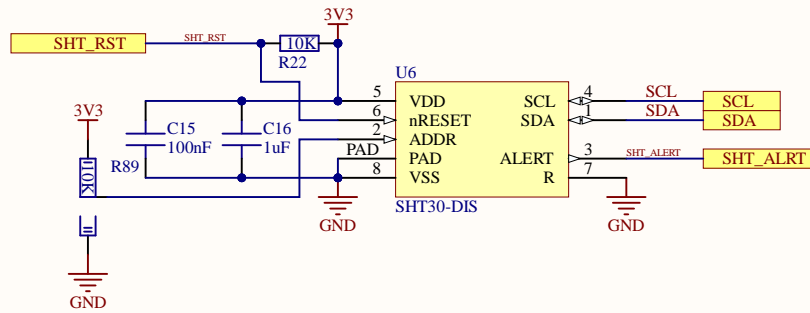
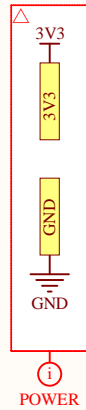
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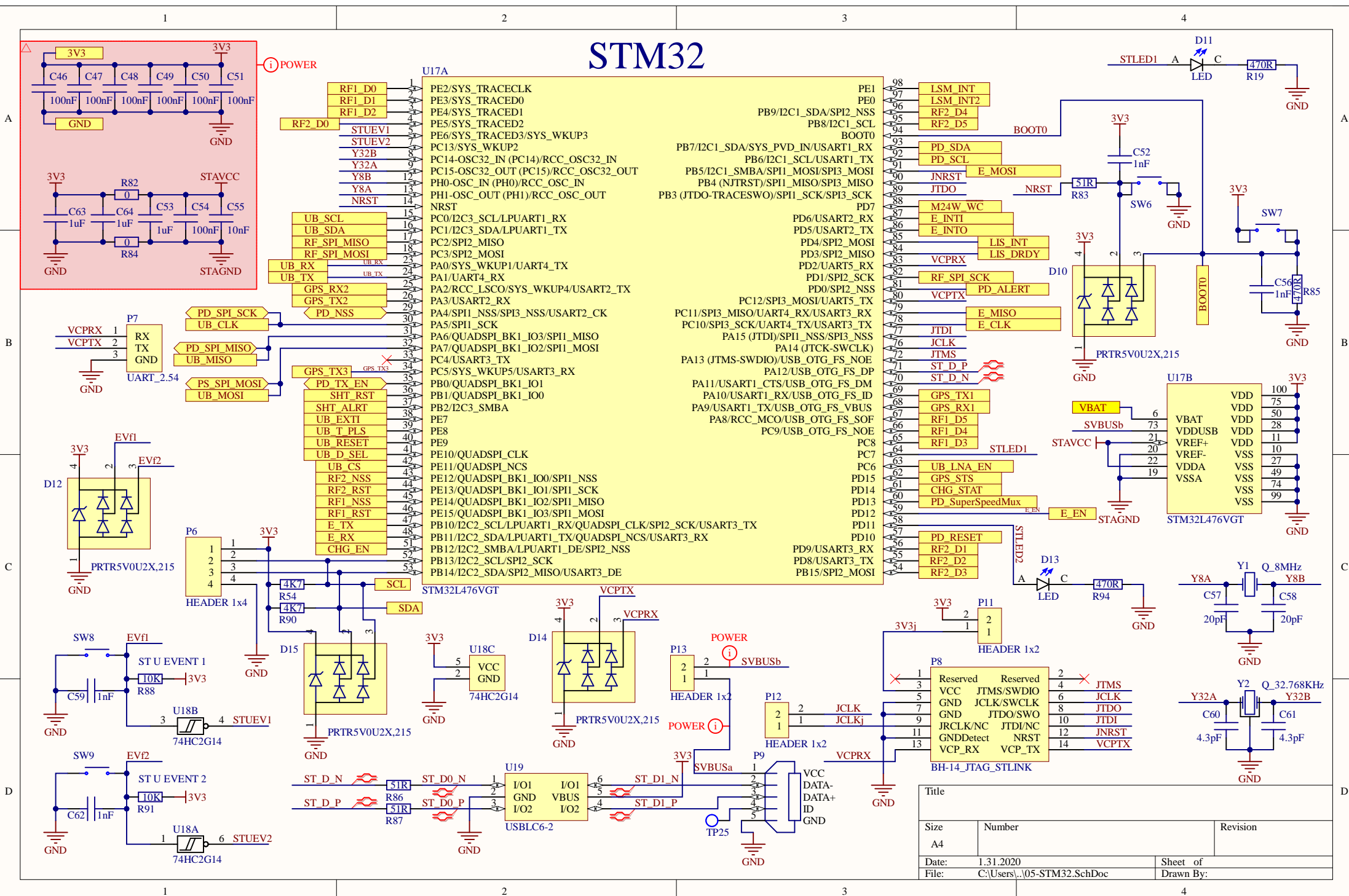


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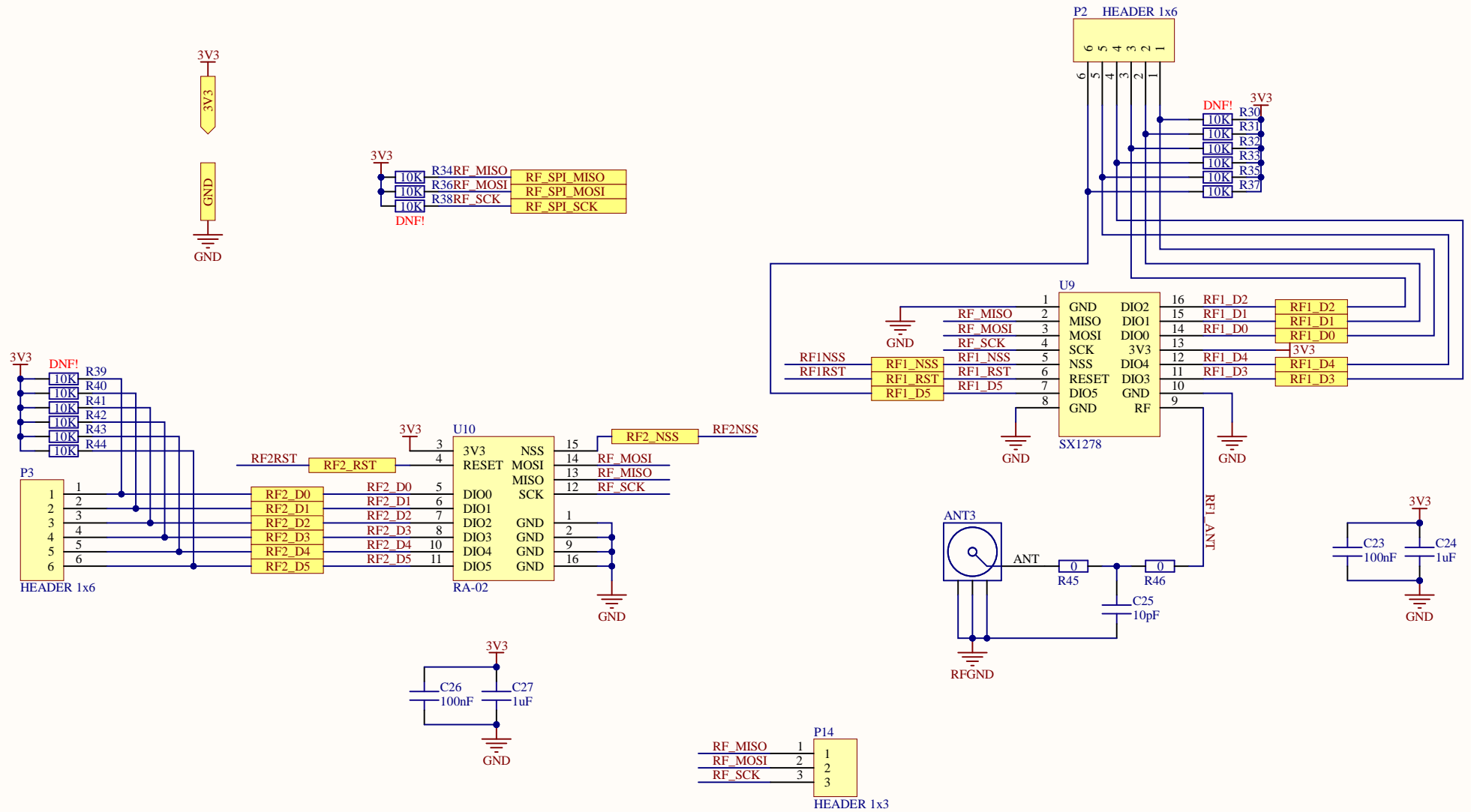
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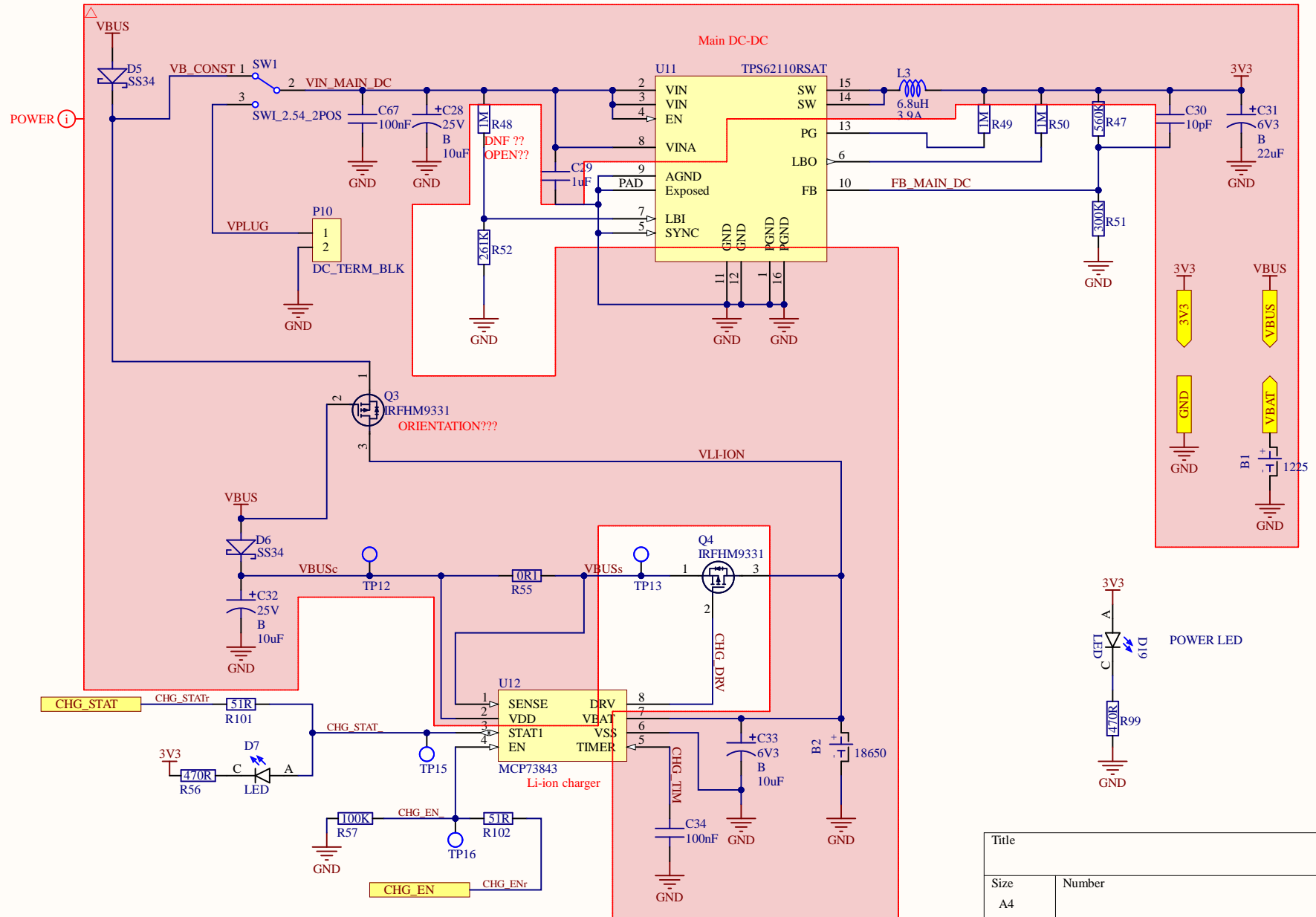
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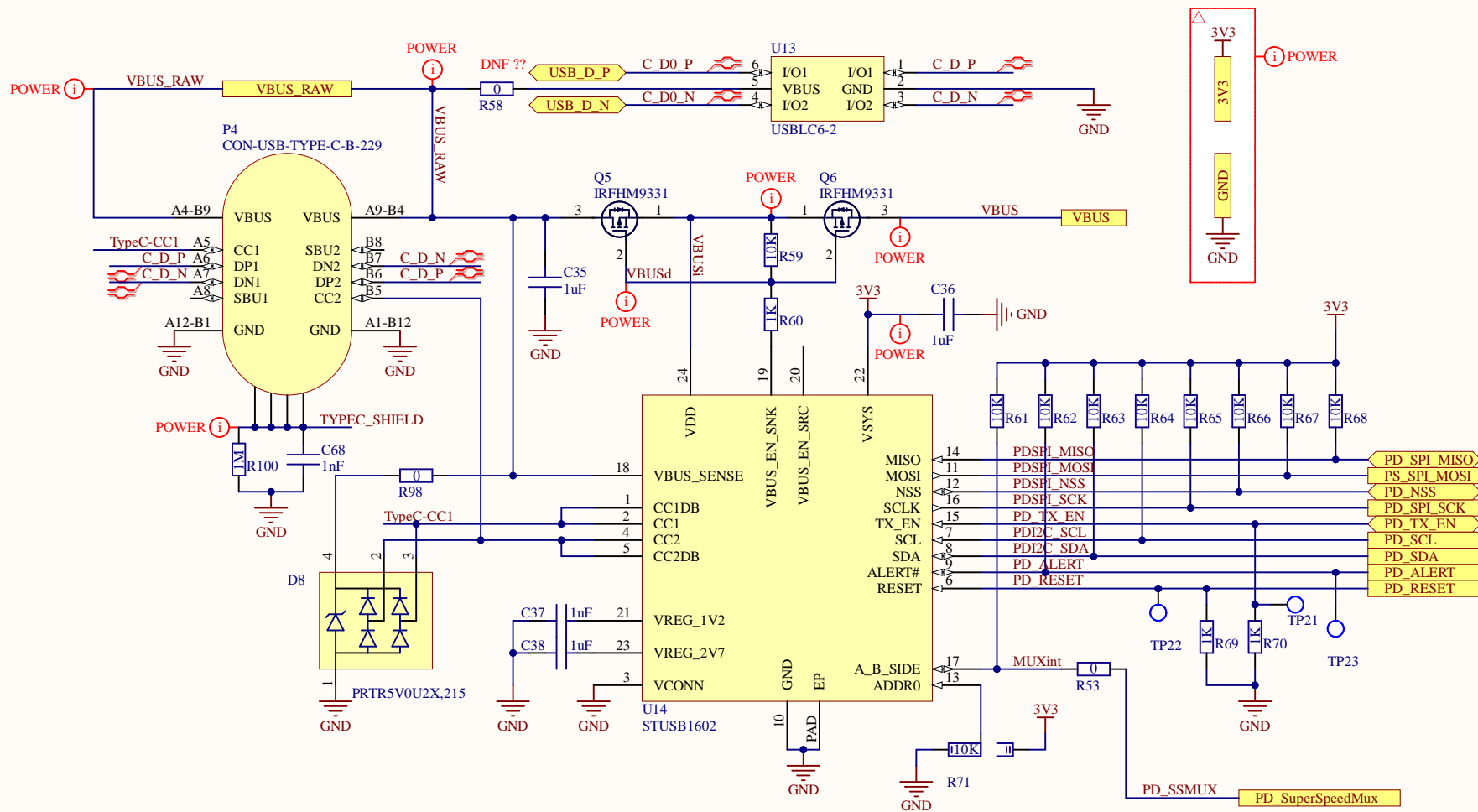


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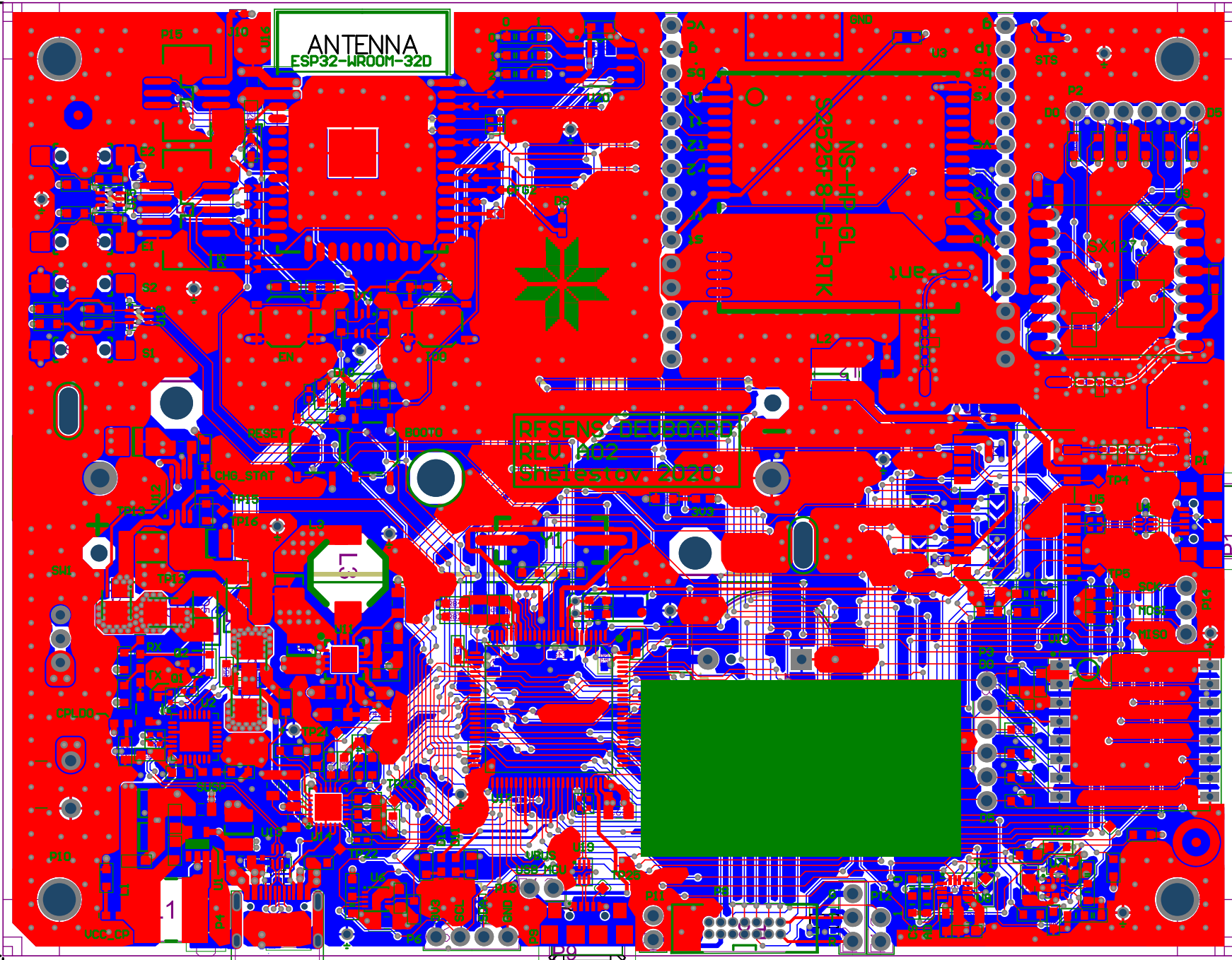
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TYPE C PORT



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101,60



Board Stack Report