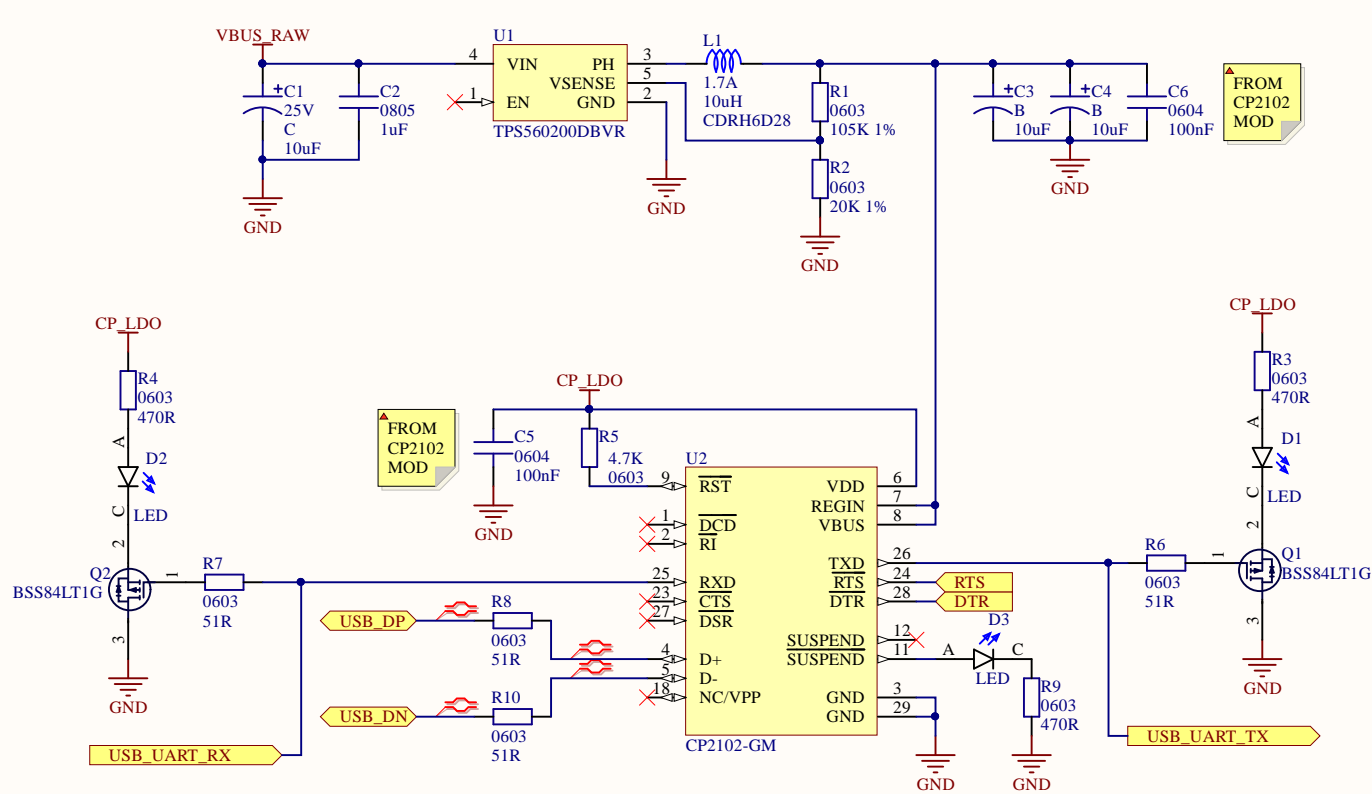


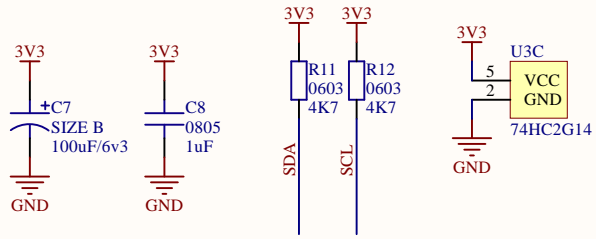
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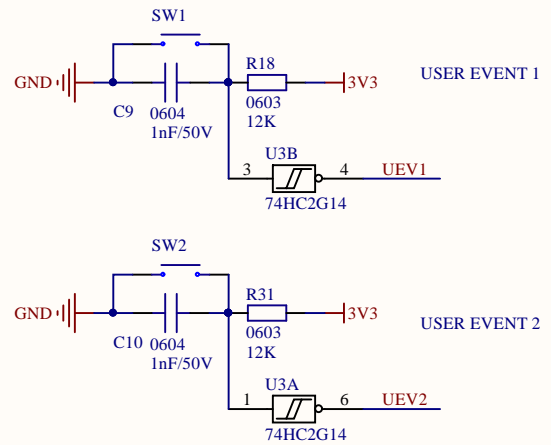
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MCU

A



B

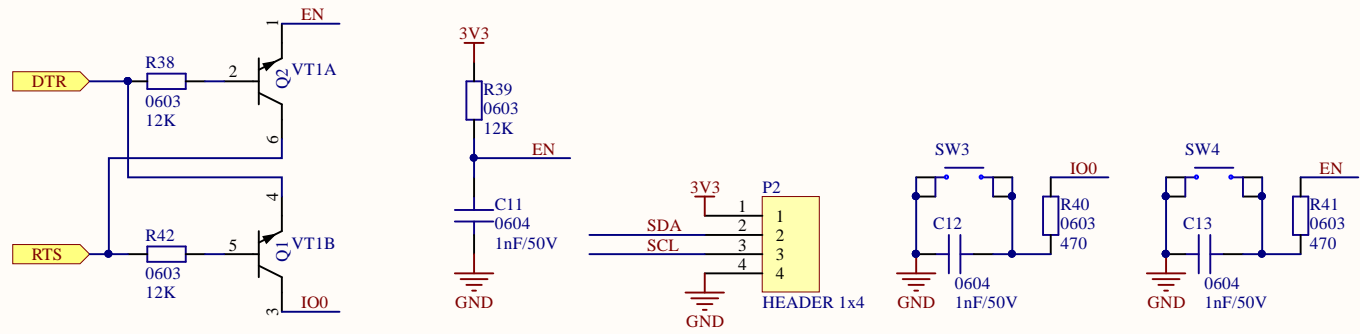


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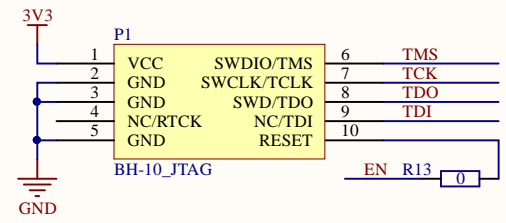
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D



3



A

B

C

D

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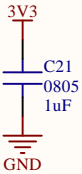
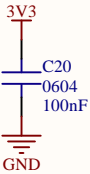
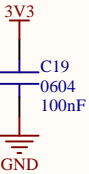
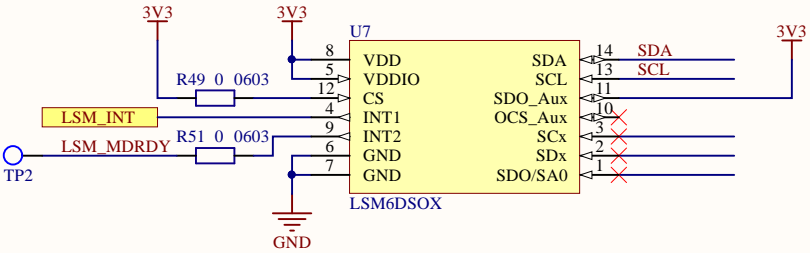
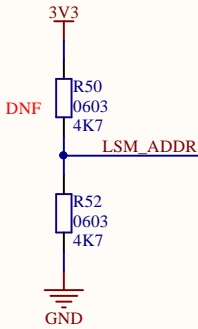
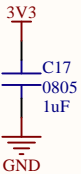
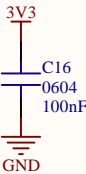
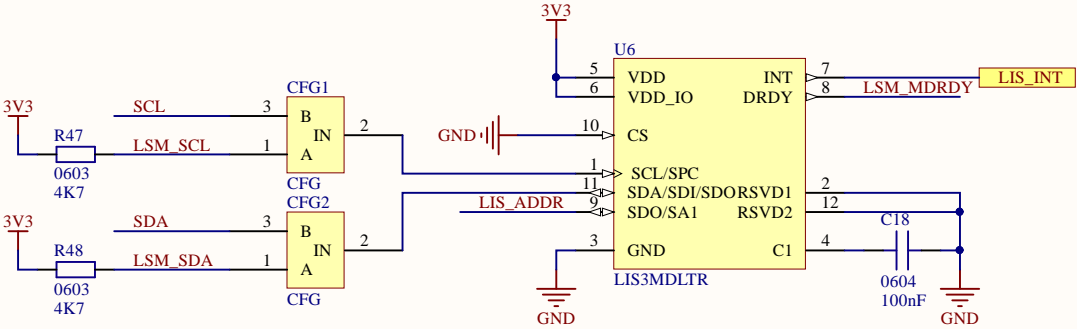
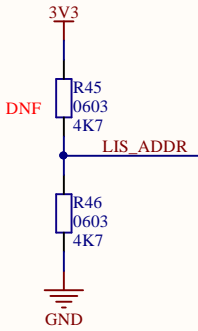
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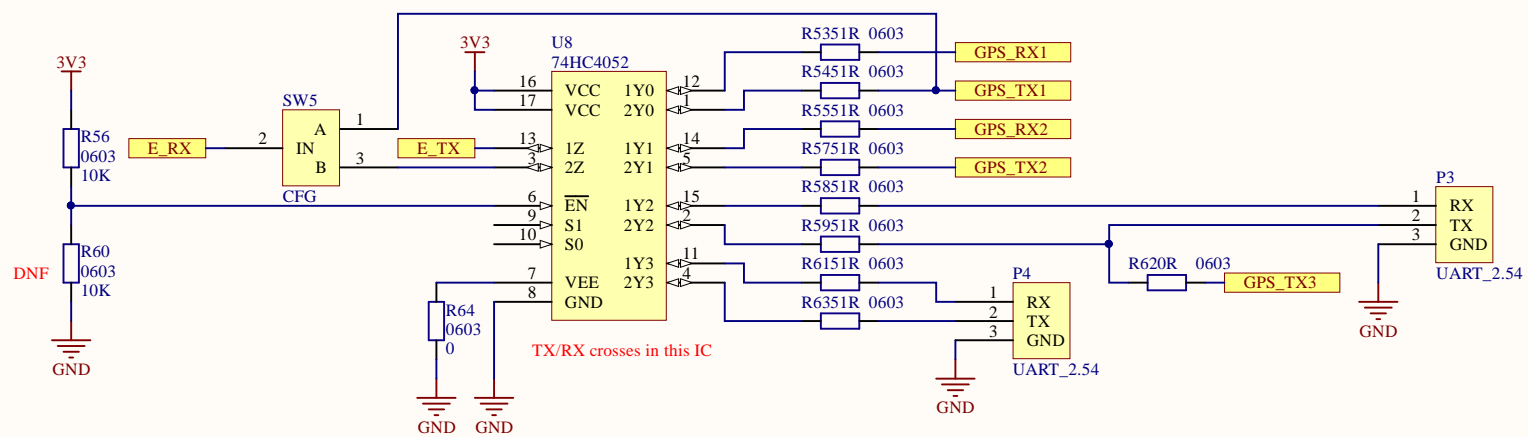
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SENSORS



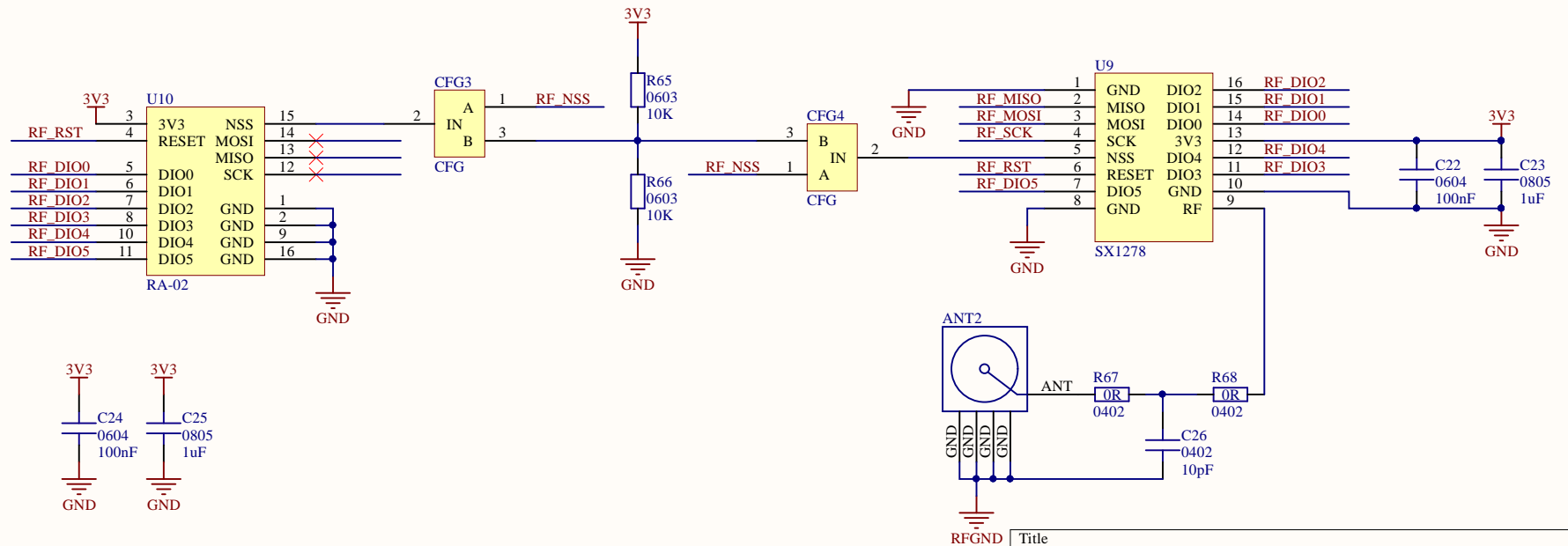
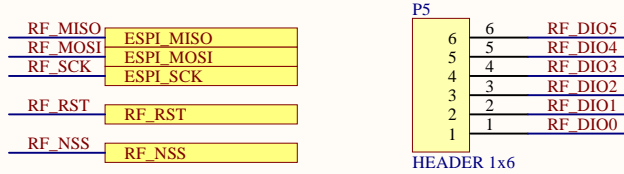
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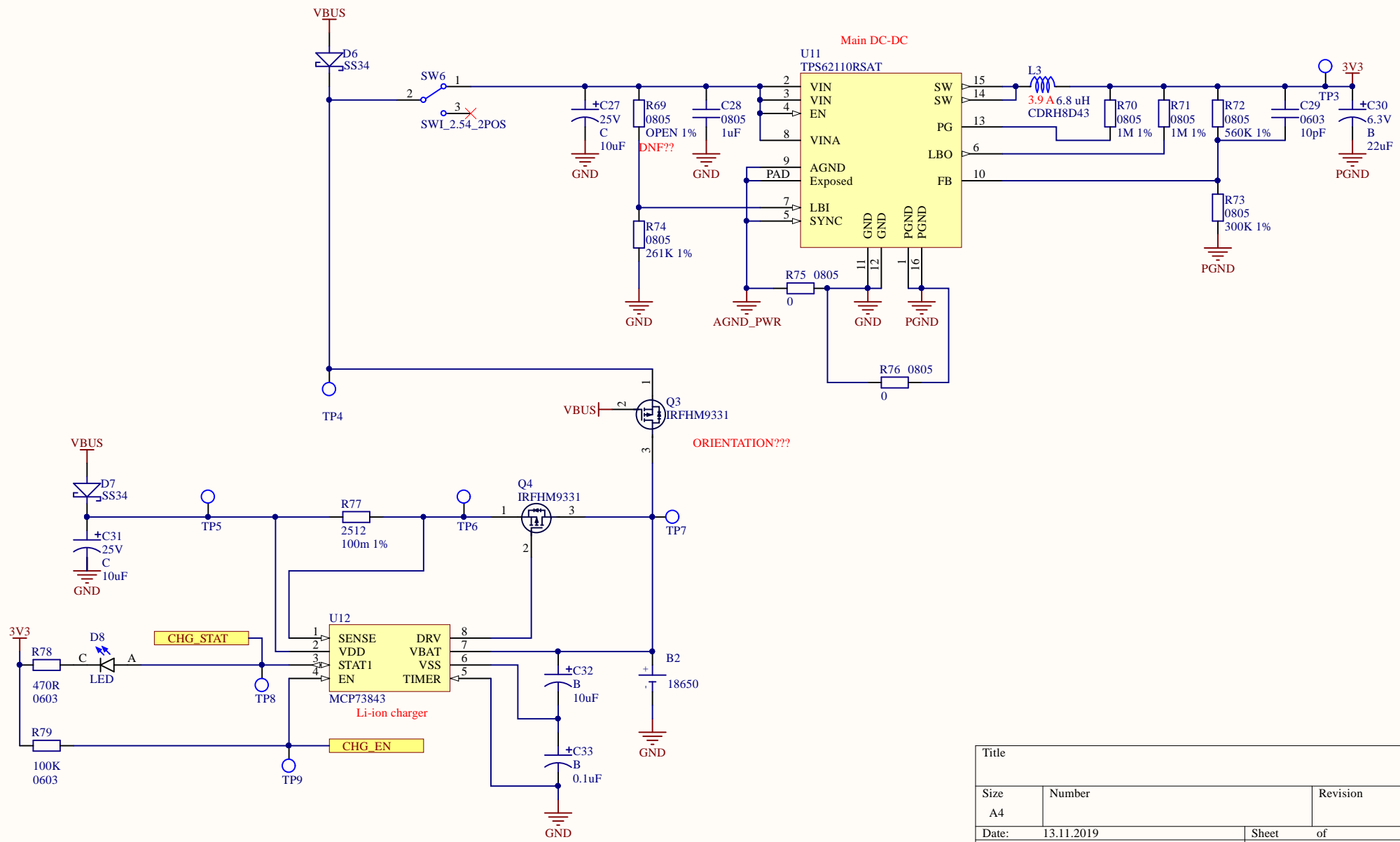
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# LORA



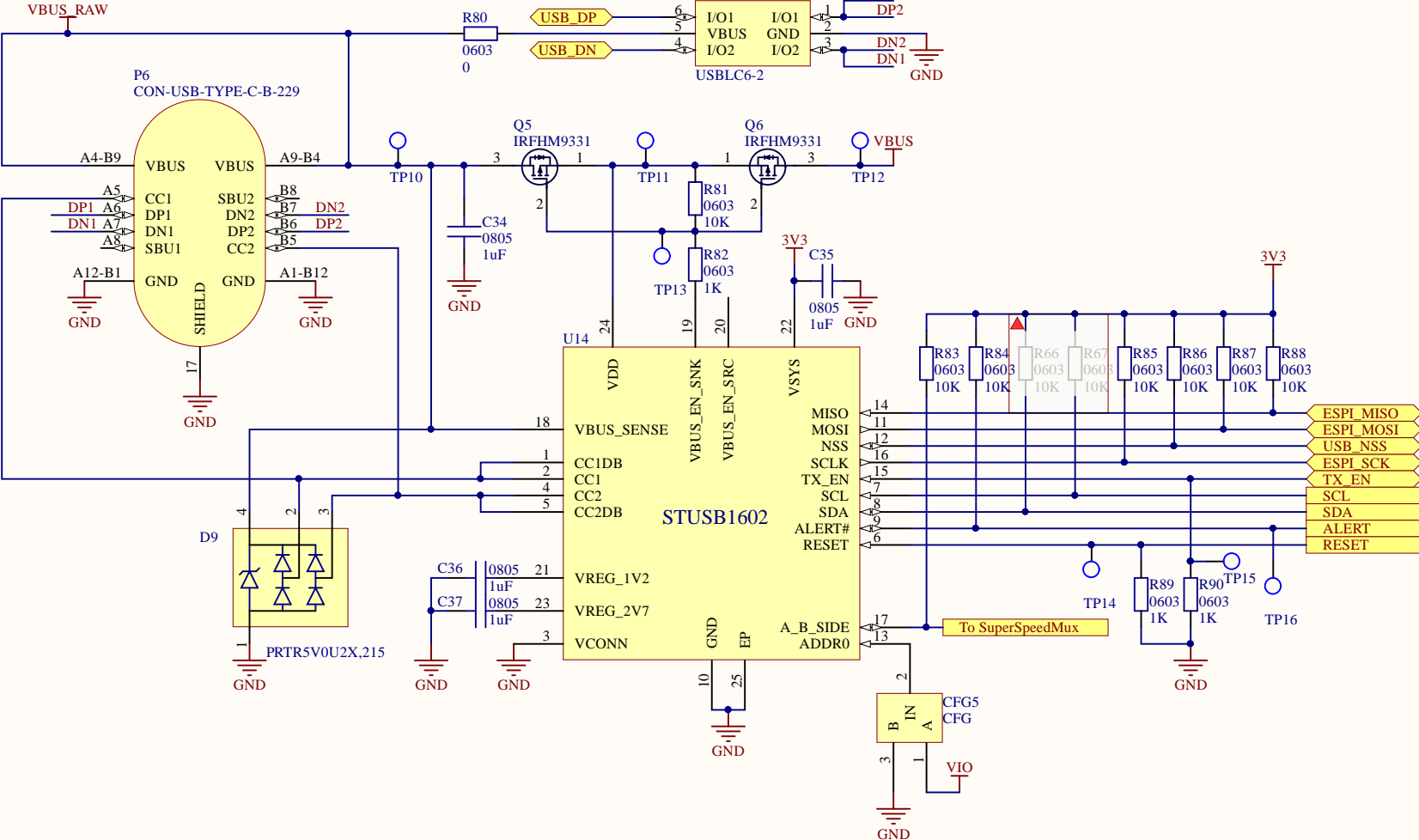
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POWER

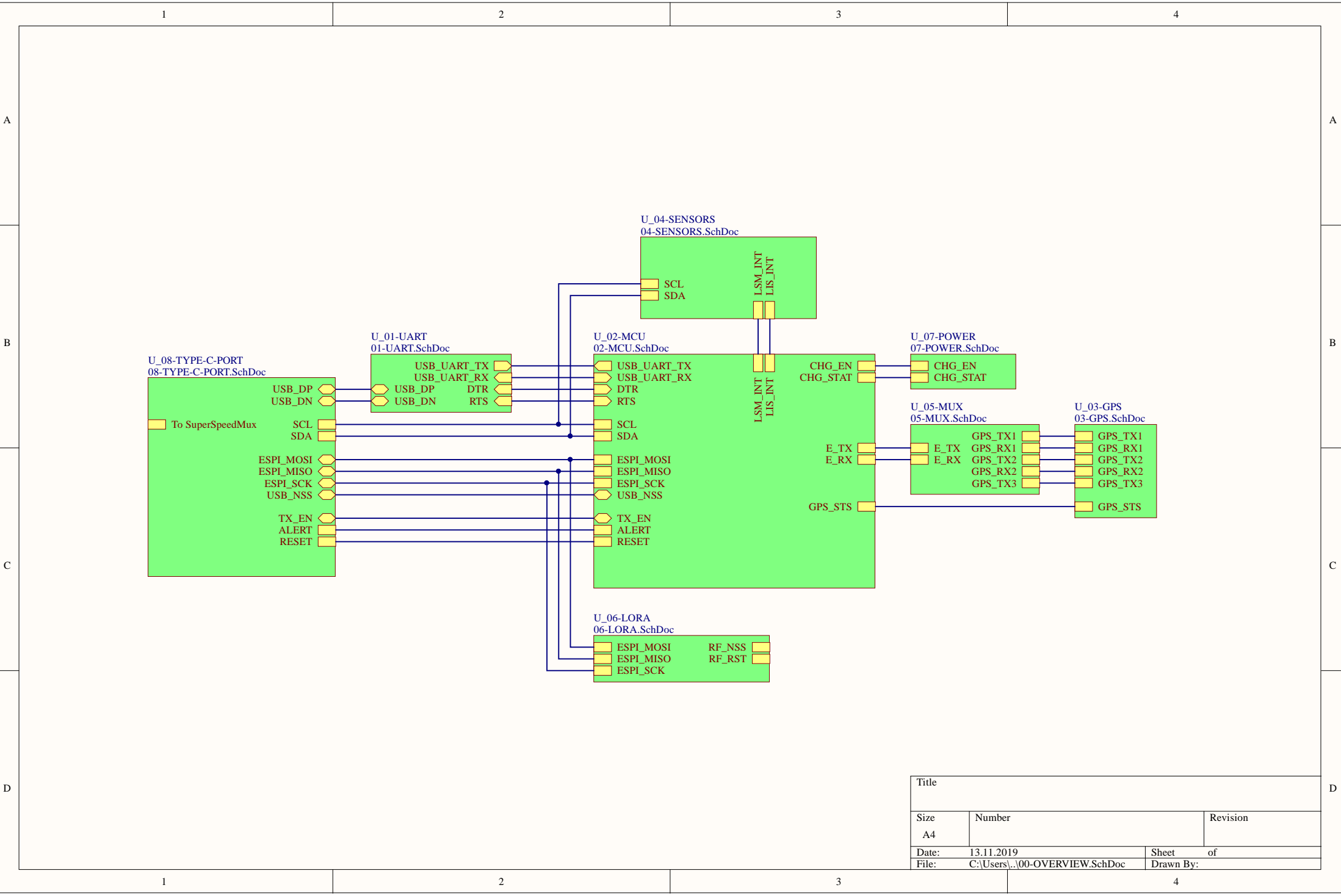


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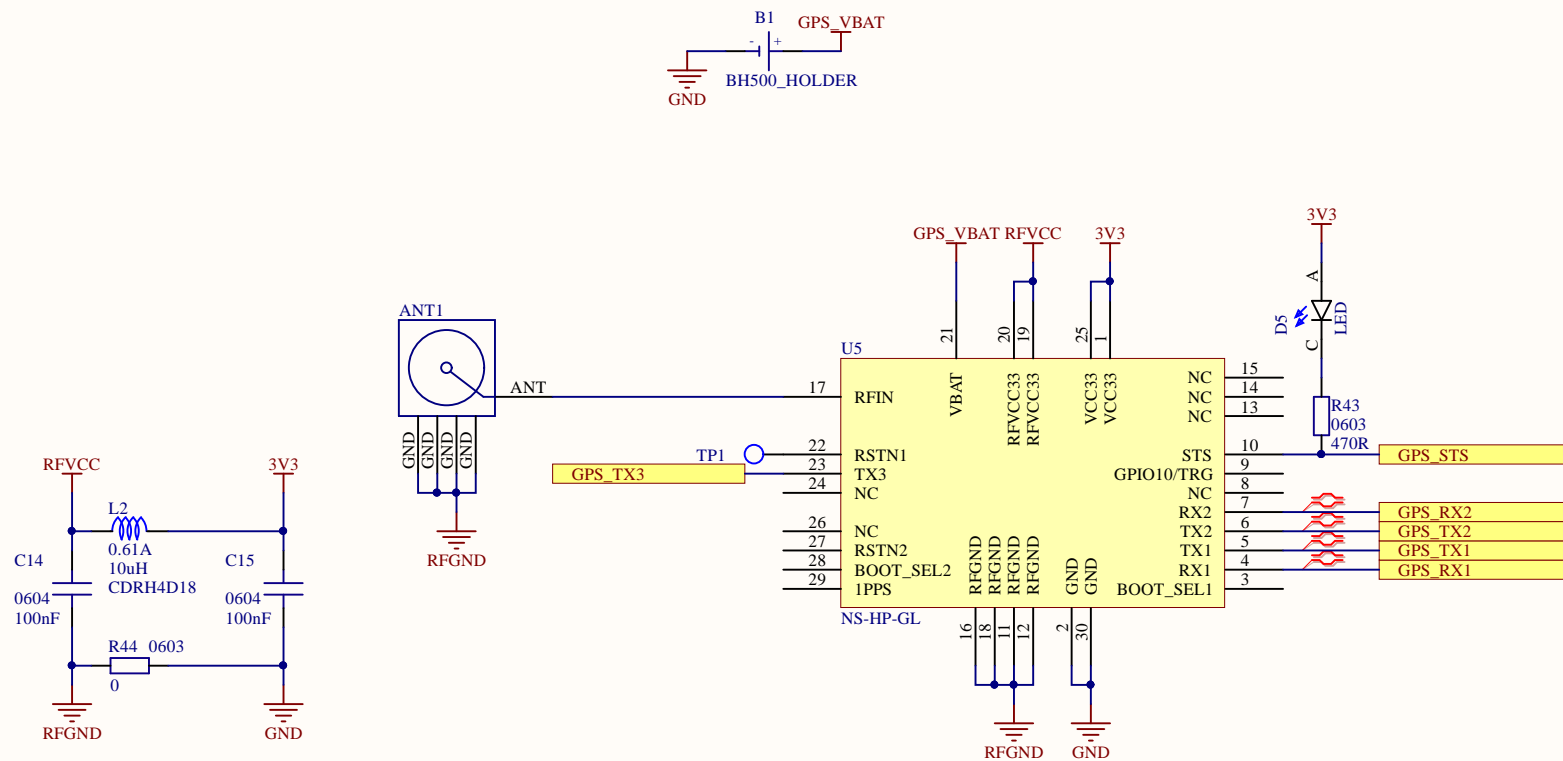
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Title		
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# BOARD SLACK REPORT