

Arm® Cortex®-M55
32-bit Microcontroller

NuMicro® Family
M55M1 Series BSP
Revision History

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Revision 3.01.002 (Released 2025-8-8)

- Startup
 - SysTick vector on flash used Default_Handler.
- Peripheral header
 - canfd_reg.h
 - CANFD_RXF0A_F0AI_Msk: Replaced CANFD_RXF0A_F0A_Msk.
 - CANFD_RXF0A_F0AI_Pos: Replaced CANFD_RXF0A_F0A_Pos.
 - hsusbd_reg.h: Fixed definitions of HSUSBD_EPBUFSTART_SADDR_Msk and HSUSBD_EPBUFSTART_SADDR_Pos.
- Standard driver
 - CANFD: Removed unsupported UTSU function.
 - HSUSBD: HSUSBD_DISABLE_HS_HANDSHAKE and HSUSBD_ENABLE_HS_HANDSHAKE added.
 - SPI: SPI_DISABLE_3WIRE_MODE and SPI_ENABLE_3WIRE_MODE added.
 - SPIM
 - Raised voltage to 1.2V for HyperBus device and octal SPI flash.
 - Supported Infineon octal SPI flash.
 - SYS
 - SYS_GPC_MFP0_PC2MFP_UTCPD0_CCDB2: Replaced SYS_GPC_MFP0_PC2MFP_UTCPD0_CCDB1.
 - SYS_GPC_MFP0_PC3MFP_UTCPD0_CCDB1: Replaced SYS_GPC_MFP0_PC3MFP_UTCPD0_CCDB2.
 - SYS_ResetModule: Added SPIM clock setting handling.
- CMSIS driver
 - CAN, ETH_MAC, Flash, GPIO, I2C, MCI, SAI, SPI, USART, USB, USBH: Added.
- Library
 - CryptoAccelerator: Improved cache coherence.
 - JpegAcceleratorLib: Improved performance.
 - UsbHostLib: Added UVC support.
- Sample code
 - AWF_GSensor_Wakeup: Used I/O buffer to replace fixed address.
 - DMIC_VAD_Wakeup: Increased VAD power threshold.
 - HSUSBD_Audio20_Codec and HSUSBD_Audio20_Headset: Fixed no sound issue.
 - HSUSBD_HID_Mouse: Improved LPM stability.
 - HSUSBD_RNDIS: Added.
 - HSUSBD_Video_CAM: Fixed UVC image noise issue and race condition of UVC functions.
 - HSUSBH_USBH_UVC, HSUSBH_USBH_VCOM_MassStorage and ImageClassification: Added.
 - I3C samples: I3C pins enable Schmitt trigger.
 - ISP_DFU_20 and ISP_HID_20: Fixed USB high-speed issue.
 - ISP_I2C and ISP_SPI: Fixed potential command failure.
 - KeywordSpotting: Added UAC support.
 - KS_AESKey: Improved cache coherence.
 - ObjectDetection_FreeRTOS, VisualWakeWords: Fixed I/O buffer alignment and race condition of UVC functions.

- RTC_TimeAndTick: Removed because almost the same as RTC_Time_Display.
- SecureApplication samples: VSCode projects require Python 3.12 at least for post-build.
- SPIM_HYPER_ExecInHRAM: Fixed I/O buffer alignment and GCC project execution failure.
- SPIM_HYPER_RW_MemMap: Fixed I/O buffer alignment.
- USB samples: Improved stability and performance.
- VSCode projects updated for Nuvoton NuMicro Cortex-M Pack.

Revision 3.01.001 (Released 2025-2-14)

- Initial release for M55M1.

Revision 3.00.001 (Released 2024-1-31)

- Initial release for M55M1ES.

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