

# Plainsong Metronome

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## NOTES

- Chassis ground connects at single point at USB connector
- Checks after Rev A fab (prototype):
  - General board bring-up
  - Check I2C address resistors for Digi-Pots

## ADDITIONAL DOCUMENTATION

PCB: 2024001 A Soutra Plainsong Metronome\_PCB.PcbDoc

BoM: 2024001 A Soutra Plainsong Metronome\_BoM.pdf

Draft: 2024001 A Soutra Plainsong Metronome\_DRAFT1.PCBDwf

2024001 A Soutra Plainsong Metronome\_DRAFT2.PCBDwf

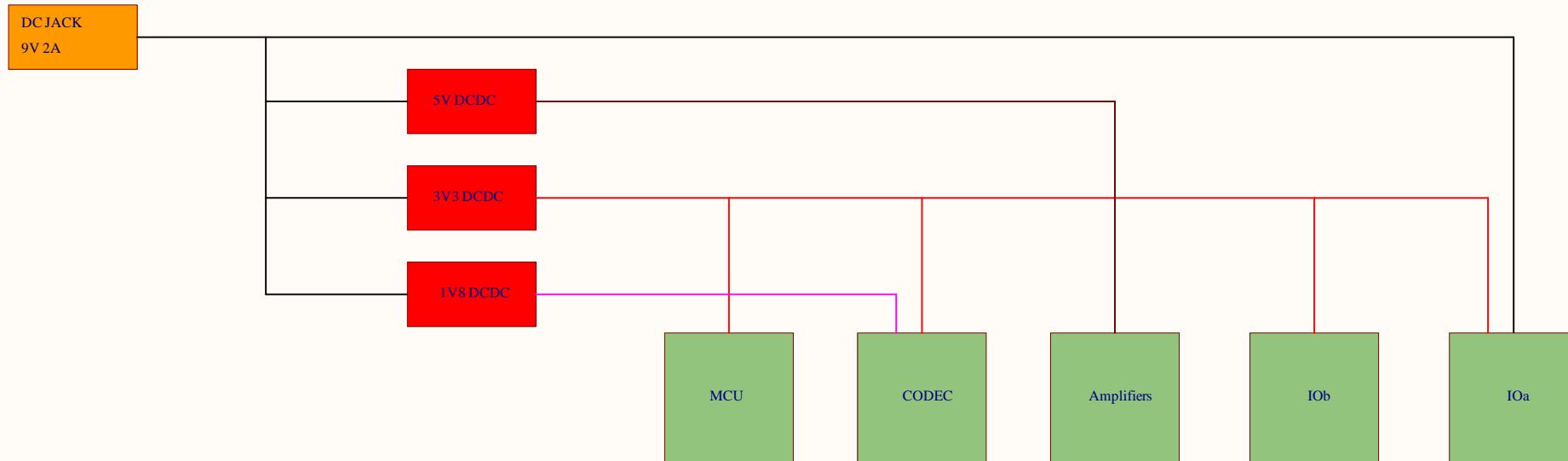
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**SOUTRA**

14 Ryehill Gardens, Edinburgh, EH6 8ER  
soutraelectronics@gmail.com +44(0)7874693650  
soutraelectronics.com

Title: Plainsong Metronome		Engineer: S.Thomas
Project:	2024001 A Soutra Plainsong Metronome	Approval: S.Thomas
Sch Rev:	A	Date: 21/06/2024
File Name:	Title-Page.SchDoc	Sheet 1 of 9
Size:	A4	Scale: 1:1
Libraries: ST_Library.LibPkg		

# Power Tree



## LEGEND

- █ - Input Connector
- █ - Power Conversion
- █ - Powered Device/Sub-Circuit
- █ - Output Connector

**SOUTRA**



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soutraelectronics@gmail.com +44(0)7874693650  
soutraelectronics.com

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Project: 2024001 A Soutra Plainsong Metronome

Approval: S.Thomas

Sch Rev: A Date: 21/06/2024

Date: 22/10/2024

### REFERENCE DOCUMENTS

BoM: 2024001 A Soutra PliRev: gMetronome\_SBMilly.LibPkg

PCB: 2024001 A Soutra PliRev: gMetronome\_PCB.PcbDoc

Draft: Draft\_Doc.draft Rev: A

### LIBRARY

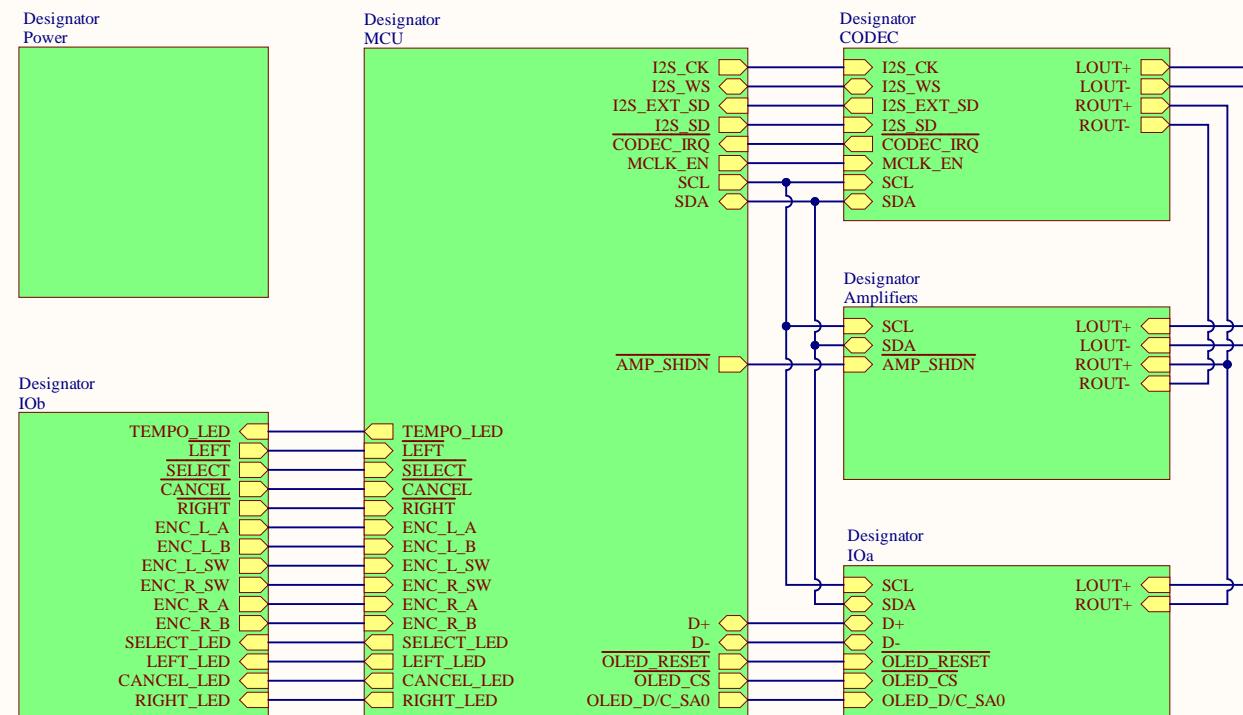
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Sheet 2 of 9

# Block Diagram



**SOUTRA**

14 Ryehill Gardens, Edinburgh, EH6 8ER  
 soutraelectronics@gmail.com +44(0)7874693650  
 soutraelectronics.com

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Engineer: S.Thomas

Approval: S.Thomas

Date: 22/10/2024

REFERENCE DOCUMENTS

BoM: 2024001 A Soutra PlRev: gMetronome\_Sheet01.LibPkg

PCB: 2024001 A Soutra PlRev: gMetronome\_PCB.PcbDoc

Draft: Draft\_Doc.draft Rev: A

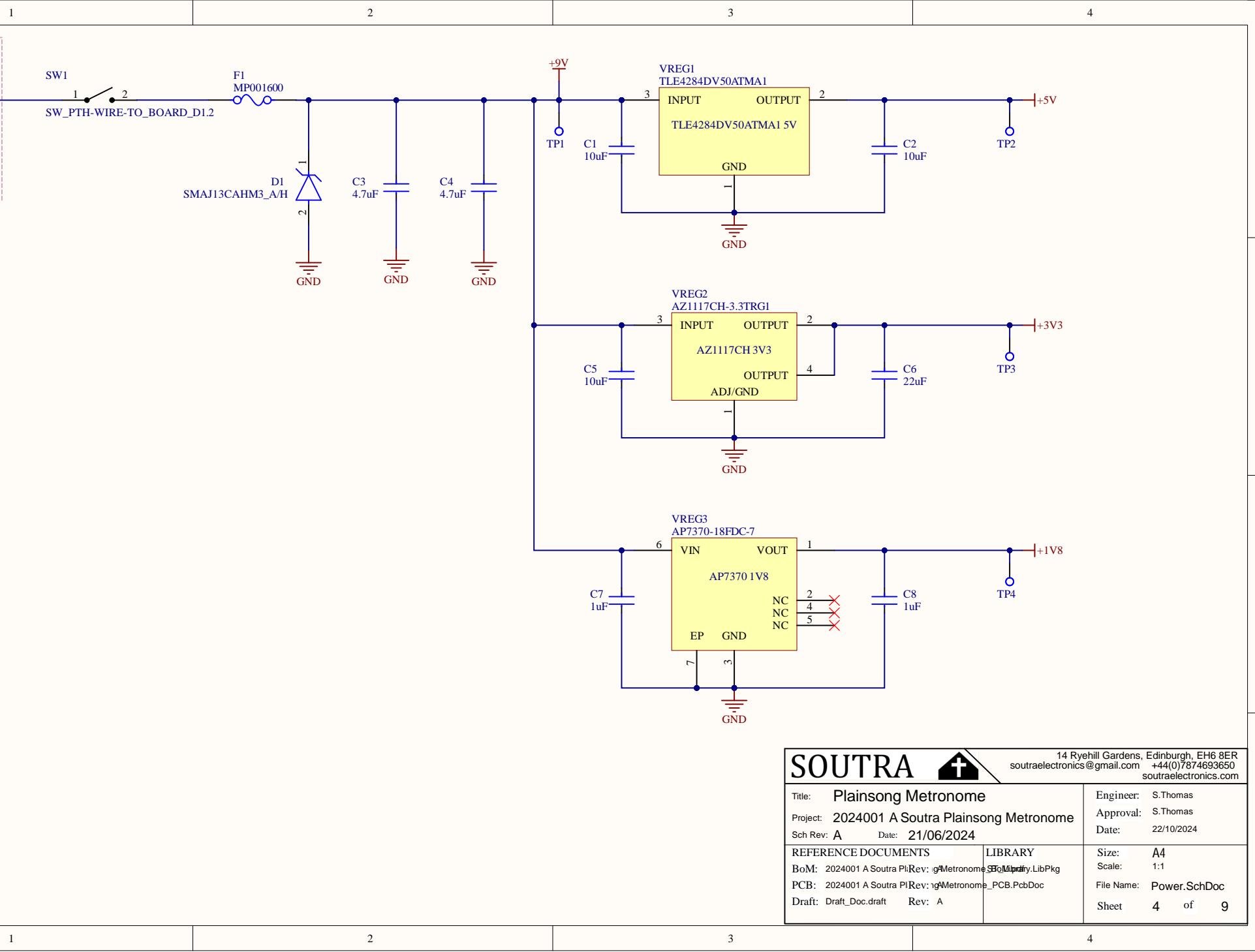
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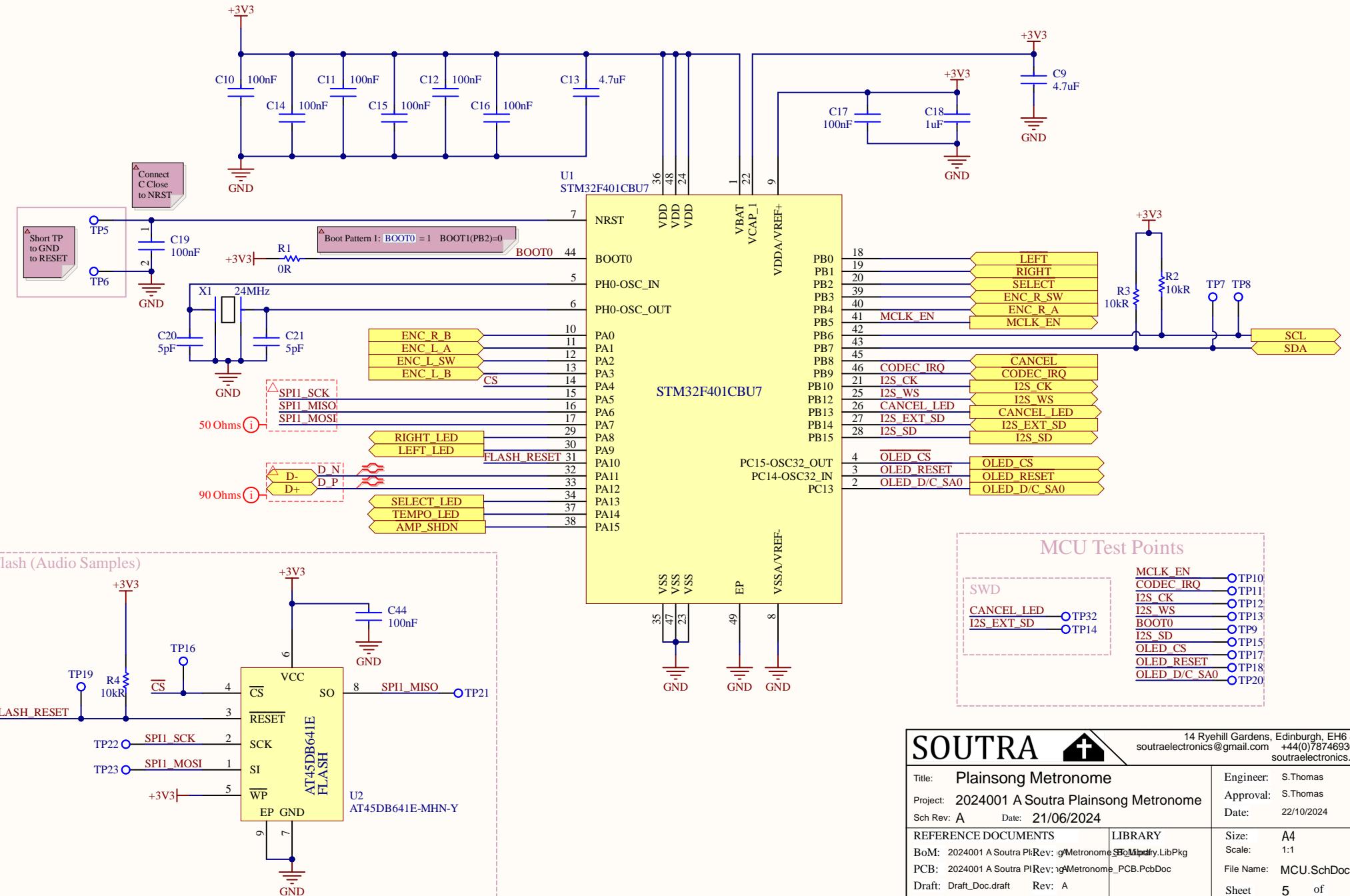
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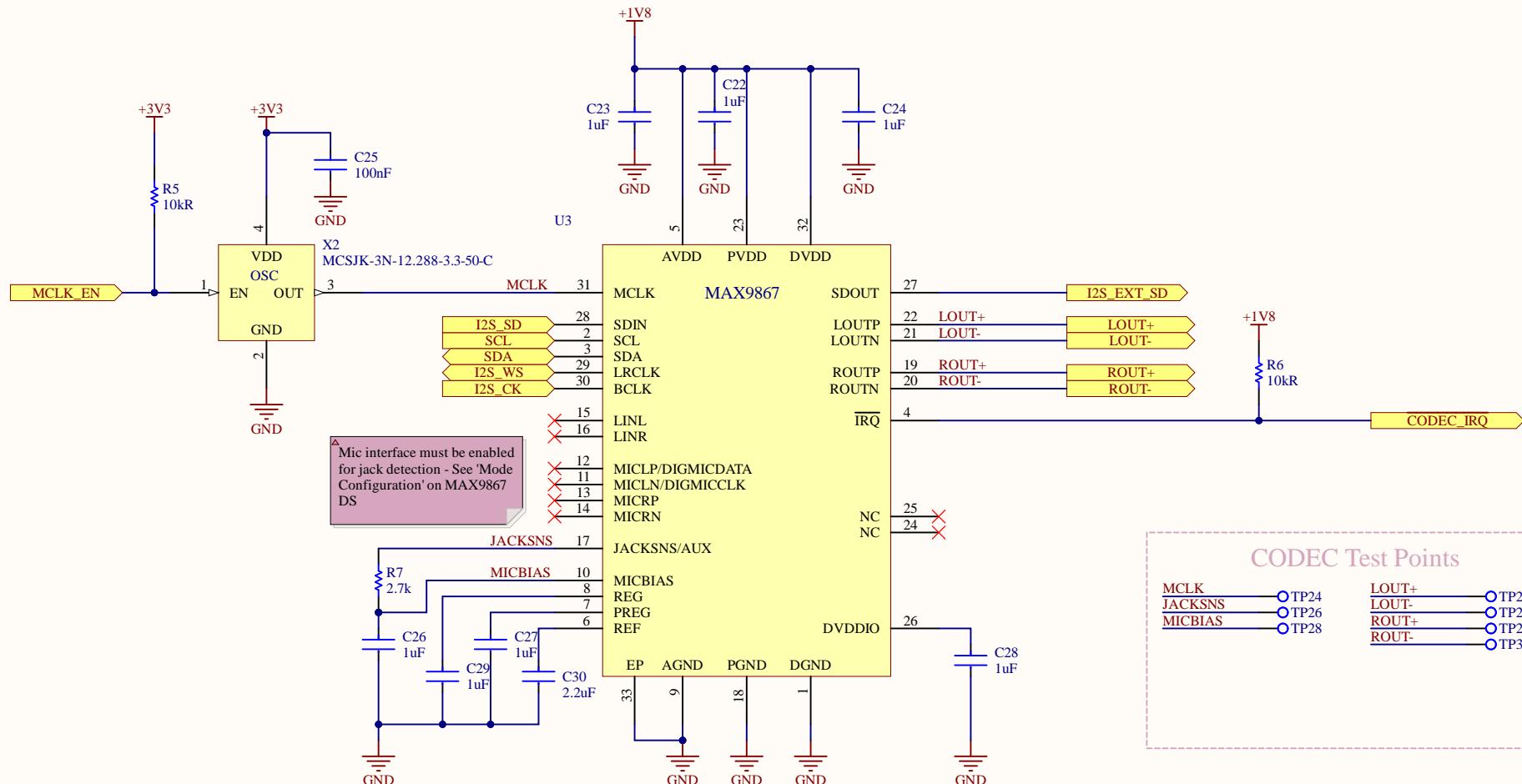
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Gardens, Edinburgh, EH6 8ER  
gmail.com +44(0)7874693650  
soutraelectronics.com

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Approval: S.Thomas  
Date: 22/10/2024

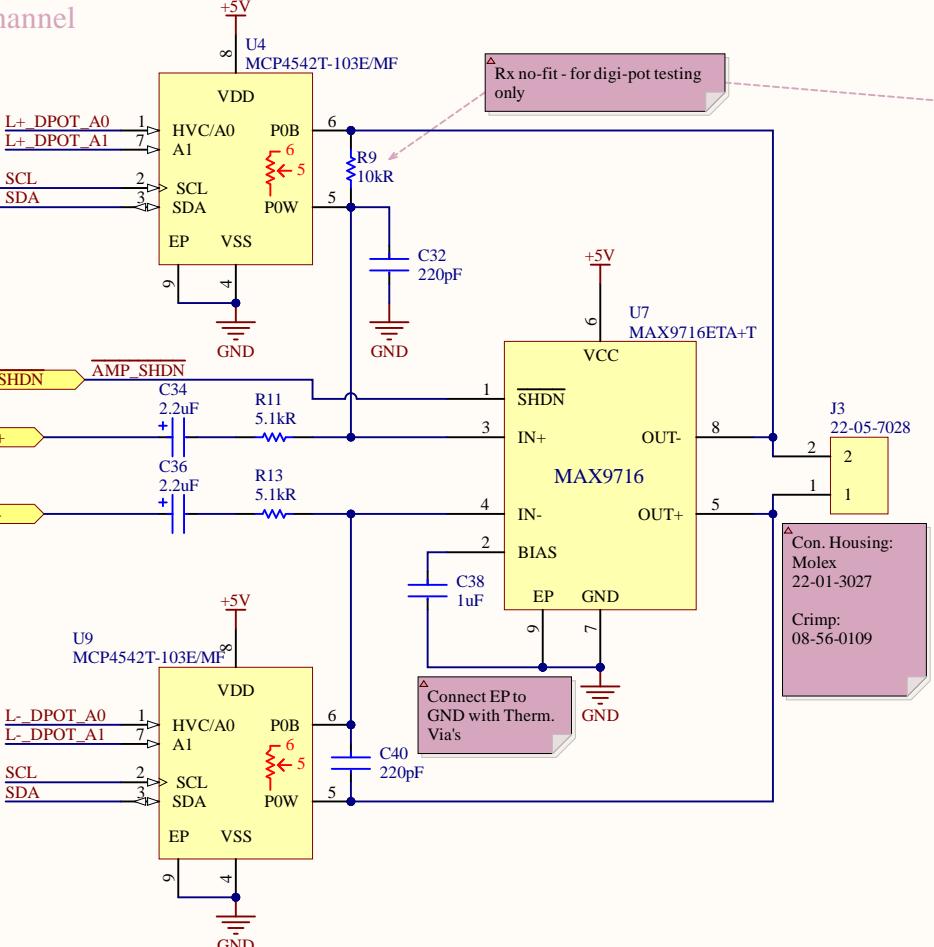
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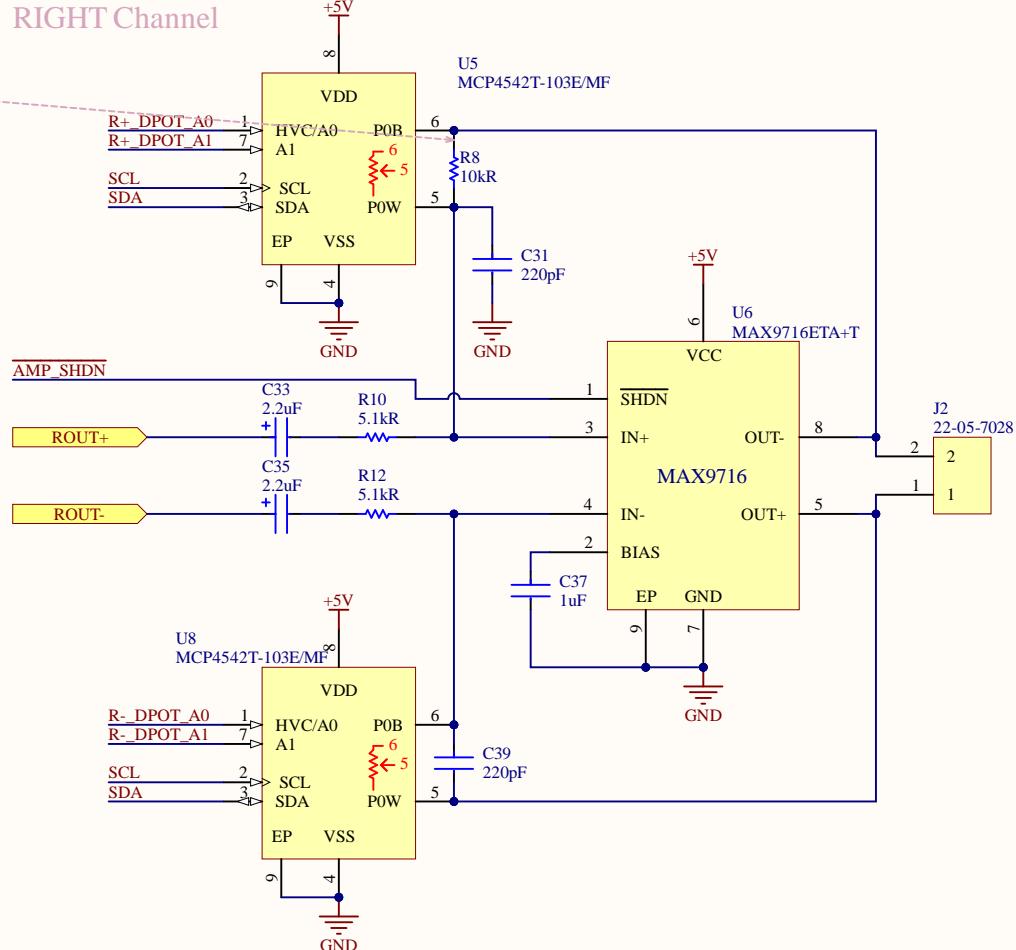
▲ Refer to datasheets for layout and groundings advice

<b>SOUTRA</b>  14 Ryehill Gardens, Edinburgh, EH6 8ER soutraelectronics@gmail.com +44(0)7874693650 soutraelectronics.com	
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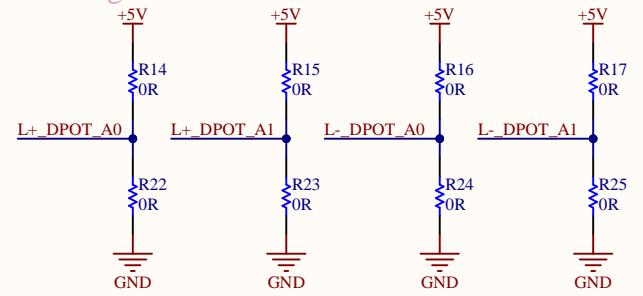
## LEFT Channel



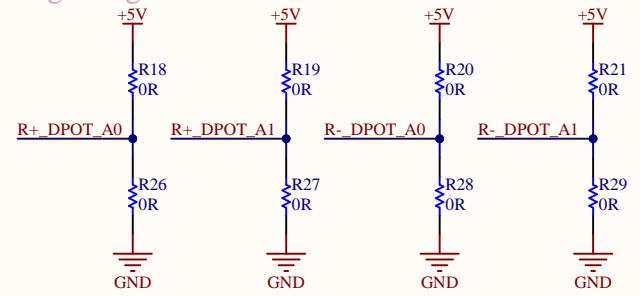
## RIGHT Channel



## Left Digi-Pot Address Select

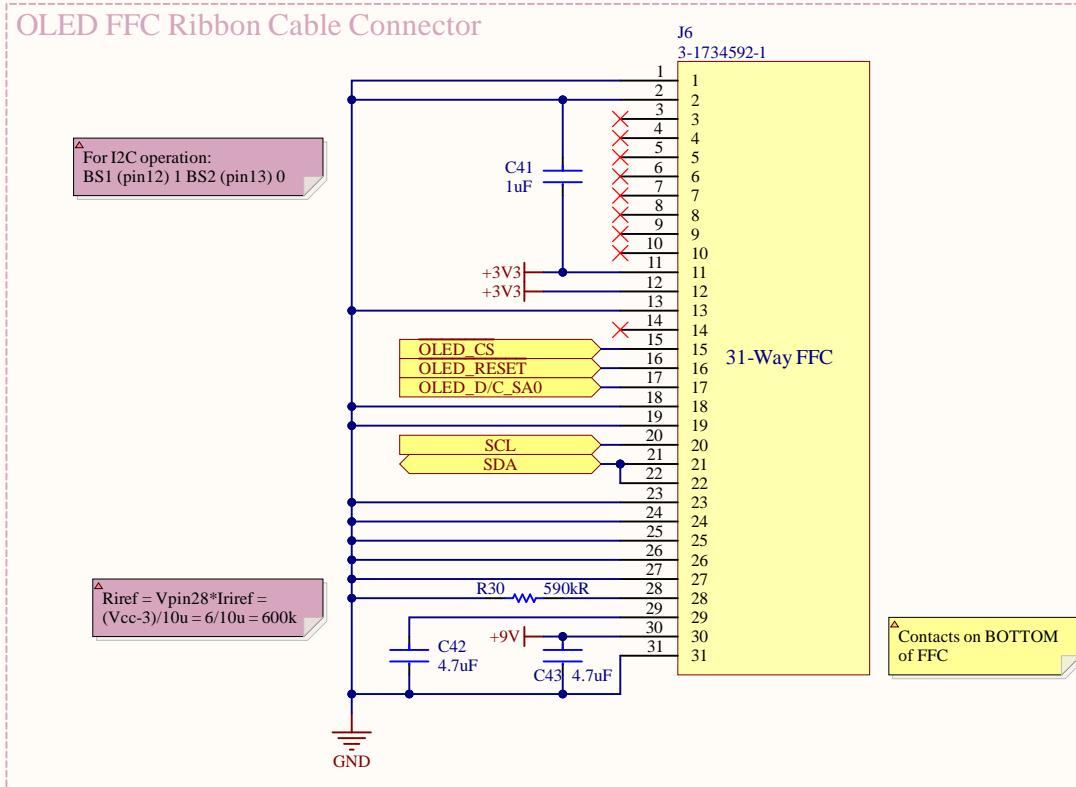
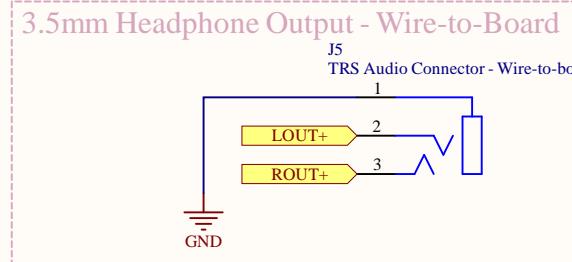
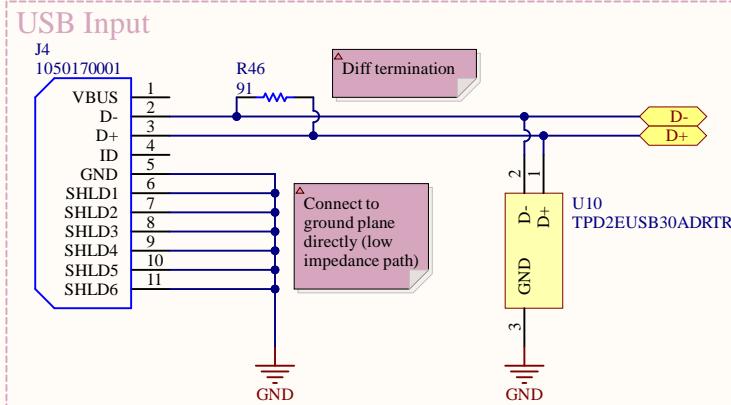


## Right Digi-Pot Address Select



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		Sheet 7 of



**SOUTRA**

14 Ryehill Gardens, Edinburgh, EH6 8ER  
soutraelectronics@gmail.com +44(0)7874693650  
soutraelectronics.com

Title: Plainsong Metronome	Engineer: S.Thomas
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Sheet 8 of 9	

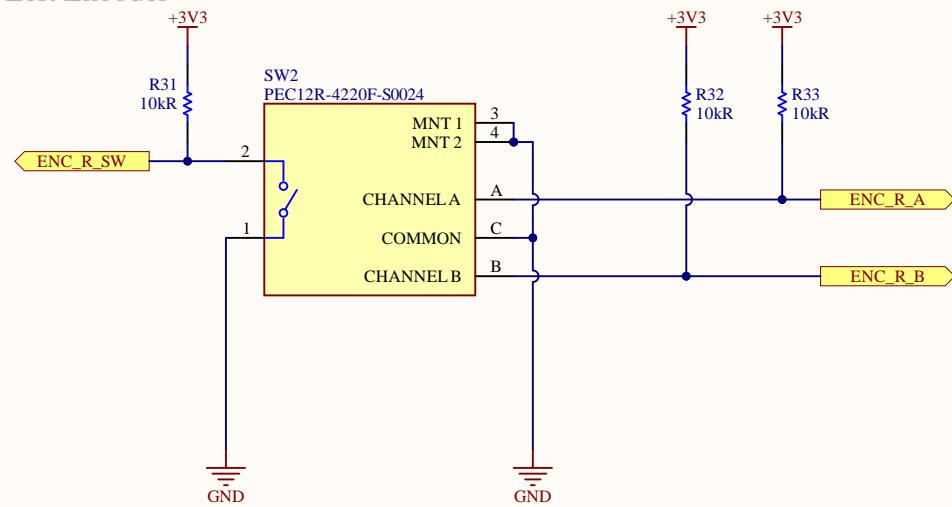
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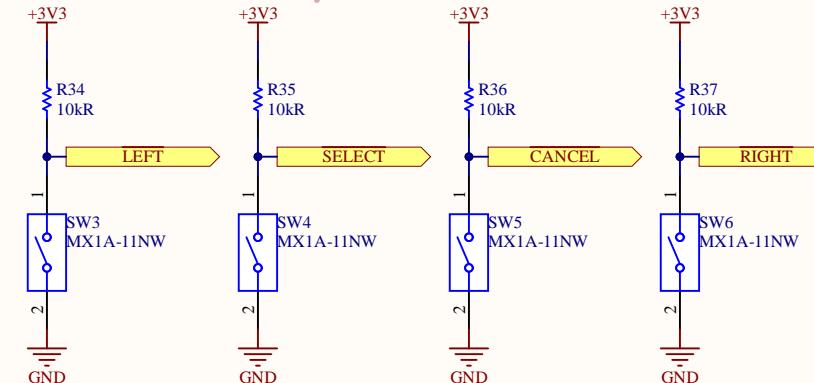
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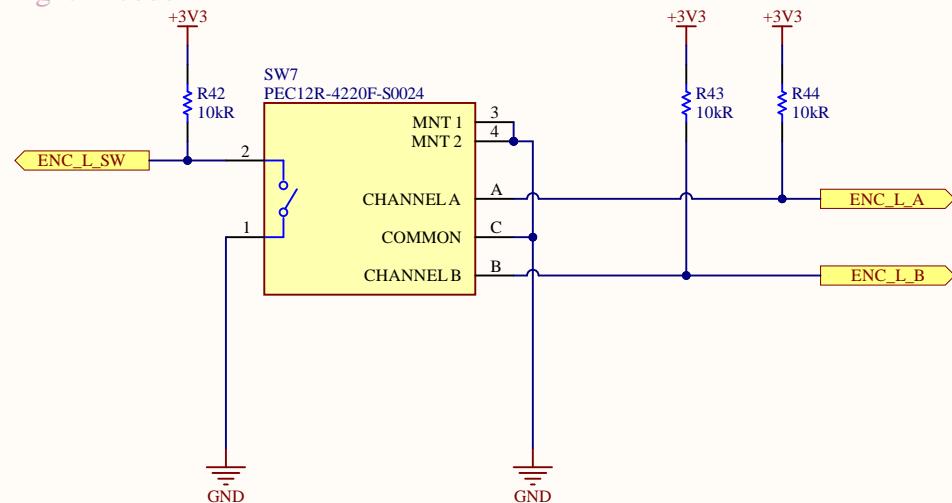
### Left Encoder



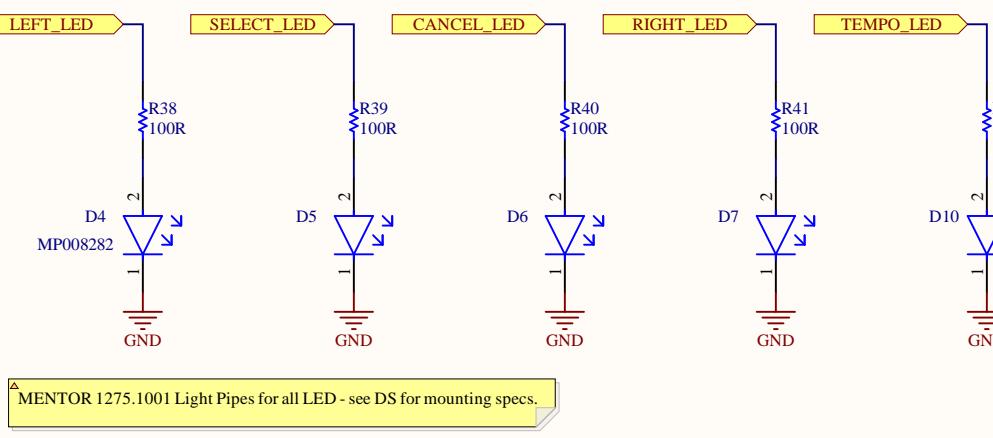
### Selection Switches - Cherry MX



### Right Encoder



### Indicator LEDs



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BoM: 2024001 A Soutra PlRev: gMetronome\_Soutra.LibPkg

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Draft: Draft\_Doc.draft Rev: A

Size: A4

Scale: 1:1

File Name: IOB.SchDoc

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1

2

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4