

Electrical Rules Check Report

Class	Document	Message
Warning	Block Diagram.SchDoc	Net MCLK_EN has no driving source (Pin R5-1, Pin U1-10, Pin X2-1)
Warning	Block Diagram.SchDoc	Nets Wire D_N has multiple names (Net Label D_N, Sheet Entry Designator-D-(I/O) (2))
Warning	Block Diagram.SchDoc	Nets Wire D_P has multiple names (Net Label D_P, Sheet Entry Designator-D+(I/O) (2))
Warning	Power.SchDoc	Off grid J1 at 912.992mil,6699.213mil
Warning	MCU.SchDoc	Off grid Net Label BOOT0 at 4512.258mil,5300mil
Warning	MCU.SchDoc	Off grid Net Label D_N at 3000.661mil,3600mil
Warning	Power.SchDoc	Off grid Pin J1-1 at 1112.992mil,6799.213mil
Warning	Power.SchDoc	Off grid Pin J1-2 at 1112.992mil,6599.213mil
Warning	Power.SchDoc	Off grid Pin J1-3 at 1112.992mil,6699.213mil
Warning	Power.SchDoc	Off grid Pin SW1-1 at 1829.135mil,6799.216mil
Warning	Power.SchDoc	Off grid Pin SW1-2 at 2429.135mil,6799.216mil
Warning	IOa.SchDoc	Off grid Port D+ at 4099.339mil,6600mil
Warning	MCU.SchDoc	Off grid Port TEMPO_LED at 3390.3mil,3300mil
Warning	Power.SchDoc	Off grid Power Object GND at 1112.992mil,6405.512mil
Warning	Power.SchDoc	Off grid Power Object GND at 4500mil,5618.113mil
Warning	Block Diagram.SchDoc	Off grid Sheet Symbol Designator at 2700mil,6055.515mil
Warning	Power.SchDoc	Off grid SW1 at 2129.135mil,6799.216mil