

**NEW**  
**2016**  
**MCA**  
**1<sup>ST</sup> Semester Examination**  
**Basic Electronics & Digital Logic Lab**  
**PAPER-107**

*Full Marks: 100*

*Time:3 Hours*

*The figures in the right-hand margin indicate full marks.*

*Candidates are required to give their answers in their*

*Owen words as far as practicable.*

*Illustrate the answers whenever questions.*