

CS5119- Advanced Computer Architecture Lab

Lab 4

Deadline: 25th September 2022 – 11:59 PM

- A. Compare the performance of AtomicSimpleCPU and TimingSimpleCPU models using default configuration script given in config->example. You can use hello world binary inside tests->test-progs->hello->bin->riscv->hello. Discuss the major differences you observe in the m5out->stats.txt for both models.
- B. Repeat task A, but this time compare riscv and x86 based on TimingSimpleCPU model. Please highlight the differences you observe.