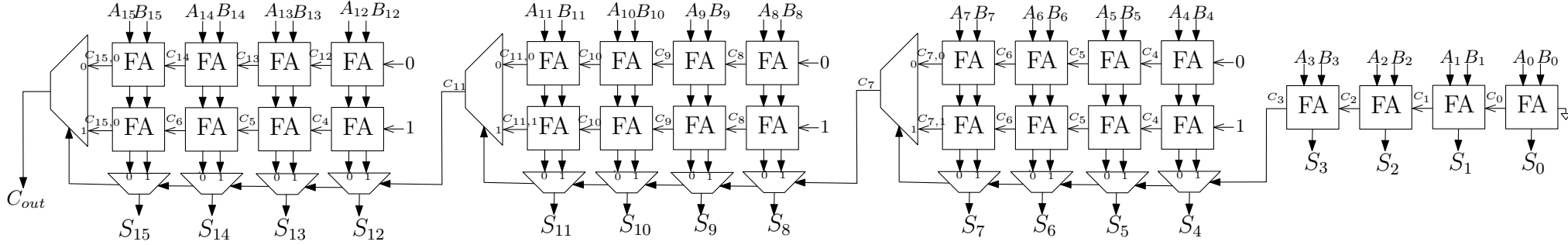


CARRY SELECT ADDER ARCHITECTURE



16-bit Carry Select Adder Delay (Block Size = 4):

$$\longrightarrow T_{SUM} = T_{4-bit-RCA-CARRY} + 3 * T_{2:1-MUX}$$

$$\longrightarrow T_{CARRY} = T_{4-bit-RCA-CARRY} + 3 * T_{2:1-MUX}$$

N-bit Carry Select Adder Delay With Block Size 'M':

$$T_{SUM} = T_{M-bit-RCA-CARRY} + \left(\frac{N}{M} - 1\right) * T_{2:1-MUX}$$

$$T_{CARRY} = T_{M-bit-RCA-CARRY} + \left(\frac{N}{M} - 1\right) * T_{2:1-MUX}$$

