

# 1. Description

## 1.1. Project

Project Name	LoRa_Transmeter_STM32G030F6P	
	6	
Board Name	custom	
Generated with:	STM32CubeMX 6.9.1	
Date	11/03/2024	

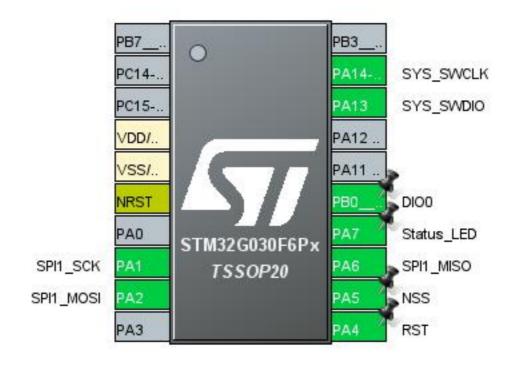
### 1.2. MCU

MCU Series	STM32G0
MCU Line	STM32G0x0 Value line
MCU name	STM32G030F6Px
MCU Package	TSSOP20
MCU Pin number	29

### 1.3. Core(s) information

Core(s)	ARM Cortex-M0+

## 2. Pinout Configuration

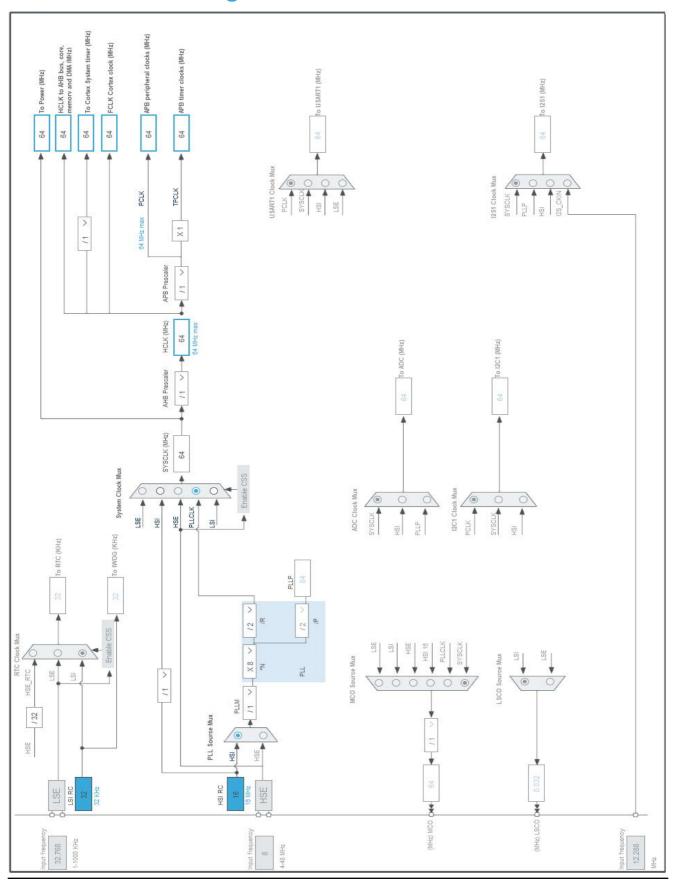


# 3. Pins Configuration

Pin Number TSSOP20	Pin Name (function after reset)	Pin Type	Alternate Function(s)	Label
4	VDD/VDDA	Power		
5	VSS/VSSA	Power		
6	NRST	Reset		
8	PA1	I/O	SPI1_SCK	
9	PA2	I/O	SPI1_MOSI	
11	PA4 *	I/O	GPIO_Output	RST
12	PA5 *	I/O	GPIO_Output	NSS
13	PA6	I/O	SPI1_MISO	
14	PA7 *	I/O	GPIO_Output	Status_LED
15	PB2	I/O	GPIO_EXTI2	DIO0
18	PA13	I/O	SYS_SWDIO	
19	PA14-BOOT0	I/O	SYS_SWCLK	

<sup>\*</sup> The pin is affected with an I/O function

## 4. Clock Tree Configuration



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## 5. Software Project

### 5.1. Project Settings

Name	Value	
Project Name	LoRa_Transmeter_STM32G030F6P6	
Project Folder	D:\STM32 CubeIDE Workspace\LoRa_Transmeter_STM32G030F6P6	
Toolchain / IDE	STM32CubeIDE	
Firmware Package Name and Version	STM32Cube FW_G0 V1.6.1	
Application Structure	Advanced	
Generate Under Root	Yes	
Do not generate the main()	No	
Minimum Heap Size	0x200	
Minimum Stack Size	0x400	

### 5.2. Code Generation Settings

Name	Value
STM32Cube MCU packages and embedded software	Copy only the necessary library files
Generate peripheral initialization as a pair of '.c/.h' files	No
Backup previously generated files when re-generating	No
Keep User Code when re-generating	Yes
Delete previously generated files when not re-generated	Yes
Set all free pins as analog (to optimize the power consumption)	No
Enable Full Assert	No

### 5.3. Advanced Settings - Generated Function Calls

Rank	Function Name	Peripheral Instance Name	
1	SystemClock_Config	RCC	
2 MX_GPIO_Init		GPIO	
3	MX_SPI1_Init	SPI1	

## 1. Power Consumption Calculator report

#### 1.1. Microcontroller Selection

Series	STM32G0
Line	STM32G0x0 Value line
мси	STM32G030F6Px
Datasheet	DS12991_Rev1

### 1.2. Parameter Selection

Temperature	25
Vdd	3.0

### 1.3. Battery Selection

Battery	Li-SOCL2(AAA700)
Capacity	700.0 mAh
Self Discharge	0.08 %/month
Nominal Voltage	3.6 V
Max Cont Current	10.0 mA
Max Pulse Current	30.0 mA
Cells in series	1
Cells in parallel	1

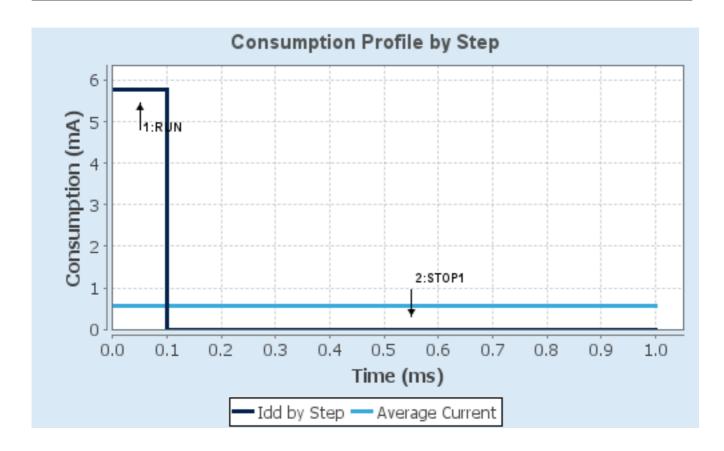
## 1.4. Sequence

	T	
Step	Step1	Step2
Mode	RUN	STOP1
Vdd	3.0	3.0
Voltage Source	Battery	Battery
Range	Range1-High	NoRange
Fetch Type	FLASH	Flash-PowerDown
CPU Frequency	64 MHz	16 MHz
Clock Configuration	HSI PLL	HSI
Clock Source Frequency	16 MHz	16 MHz
Peripherals		
Additional Cons.	0 mA	0 mA
Average Current	5.77 mA	3.7 µA
Duration	0.1 ms	0.9 ms
DMIPS	80.0	0.0
Ta Max	128.68	130
Category	In DS Table	In DS Table

#### 1.5. Results

Sequence Time	1 ms	Average Current	580.33 μA
Battery Life	1 month, 19 days,	Average DMIPS	80.0 DMIPS
	18 hours		

### 1.6. Chart



## 2. Peripherals and Middlewares Configuration

#### 2.1. RCC

#### 2.1.1. Parameter Settings:

#### **System Parameters:**

VDD voltage (V) 3.3
Instruction Cache Enabled
Prefetch Buffer Enabled
Data Cache Enabled

Flash Latency(WS) 2 WS (3 CPU cycle)

**RCC Parameters:** 

HSI Calibration Value 64
HSE Startup Timout Value (ms) 100
LSE Startup Timout Value (ms) 5000

**Power Parameters:** 

Power Regulator Voltage Scale Power Regulator Voltage Scale 1

**Peripherals Clock Configuration:** 

Generate the peripherals clock configuration TRUE

#### 2.2. SPI1

#### **Mode: Full-Duplex Master**

#### 2.2.1. Parameter Settings:

#### **Basic Parameters:**

Frame Format Motorola

Data Size 8 Bits

First Bit MSB First

**Clock Parameters:** 

Prescaler (for Baud Rate)

Baud Rate 8.0 MBits/s \*

Clock Polarity (CPOL) Low
Clock Phase (CPHA) 1 Edge

**Advanced Parameters:** 

CRC Calculation Disabled

NSSP Mode Enabled

NSS Signal Type Software

2.3. SYS

mode: Debug

Timebase Source: SysTick

\* User modified value

# 3. System Configuration

## 3.1. GPIO configuration

IP	Pin	Signal	GPIO mode	GPIO pull/up pull down	Max Speed	User Label
				down	Speed	
SPI1	PA1	SPI1_SCK	Alternate Function Push Pull	No pull-up and no pull-down	Low	
	PA2	SPI1_MOSI	Alternate Function Push Pull	No pull-up and no pull-down	Low	
	PA6	SPI1_MISO	Alternate Function Push Pull	No pull-up and no pull-down	Low	
SYS	PA13	SYS_SWDIO	n/a	n/a	n/a	
	PA14- BOOT0	SYS_SWCLK	n/a	n/a	n/a	
GPIO	PA4	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	RST
	PA5	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	NSS
	PA7	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	Status_LED
	PB2	GPIO_EXTI2	External Interrupt Mode with Rising edge trigger detection	No pull-up and no pull-down	n/a	DIO0

### 3.2. DMA configuration

nothing configured in DMA service

### 3.3. NVIC configuration

## 3.3.1. NVIC

Interrupt Table	Enable	Preenmption Priority	SubPriority	
Non maskable interrupt	true	0	0	
Hard fault interrupt	true	0	0	
System service call via SWI instruction	true	0	0	
Pendable request for system service	true	0	0	
System tick timer	true	3	0	
Flash global interrupt	unused			
RCC global interrupt	unused			
EXTI line 2 and line 3 interrupts	unused			
SPI1 global interrupt	unused			

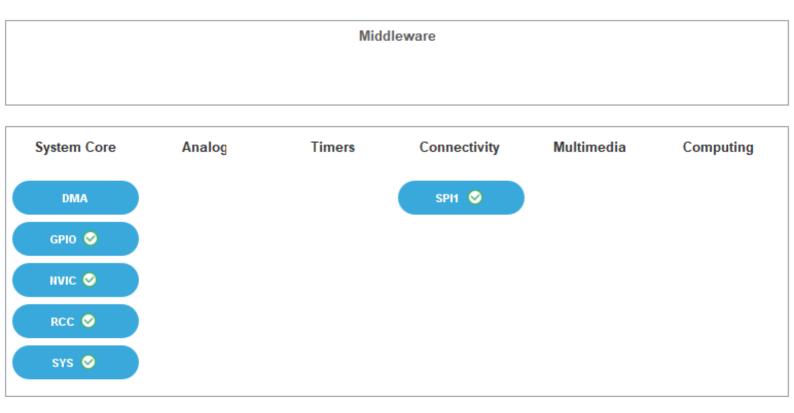
### 3.3.2. NVIC Code generation

Enabled interrupt Table	Select for init	Generate IRQ	Call HAL handler
	sequence ordering	handler	
Non maskable interrupt	false	true	false
Hard fault interrupt	false	true	false
System service call via SWI instruction	false	true	false
Pendable request for system service	false	true	false
System tick timer	false	true	true

<sup>\*</sup> User modified value

## 4. System Views

- 4.1. Category view
- 4.1.1. Current



## 5. Docs & Resources

Type Link

IBIS models https://www.st.com/resource/en/ibis\_model/stm32g0\_ibis.zip

System View https://www.st.com/resource/en/svd/stm32g0-svd.zip

Description

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