Siddaganga Institute of Technology, Tumakuru

(An Autonomous institution affiliated to Visvesvaraya Technological University, Belagavi,
Approved by AICTE, New Delhi, Accredited by NAAC and ISO 9001:2015 certified)

Employee Emotion Detection

Keeping Your Employees Happy

A project report submitted to
Visvesvaraya Technological University. Belgaum, Karnataka
in the partial fulfillment of the requirements for the award of degree of
Bachelor of Engineering
in
Computer Science and Engineering
by

Student-1	**1SI12CS001**
Student-2	**1SI12CS002**
Student-3	**1SI12CS003**
Student-4	**1SI12CS004**

under the guidance of

Prof. ABC
Assistant Professor

Department of Computer Science & Engineering
(Program Accredited by NBA)
Siddaganga Institute of Technology
B.H Road, Tumakuru-572 103, Karnataka, India.
Web : www.sit.ac.in

June, 2022

Department of Computer Science and Engineering

Siddaganga Institute of Technology, Tumakuru

(An Autonomous institution affiliated to Visvesvaraya Technological University, Belagavi,
Approved by AICTE, New Delhi, Accredited by NAAC and ISO 9001:2015 certified)

Certificate

This is to certify that the Project Report entitled **"My Wonderful Project"** is a bonafide work carried out by **Student-1(1SI16CS001)**, **Student-2(1SI16CS002)**, **Student-3(1SI16CS003)** and **Student-4(1SI16CS004)** in the partial fulfillment of the requirement for the award of the degree of Bachelor of Engineering in Computer Science and Engineering, Visvesvaraya Technological University, Belagavi during the year 2019-20. It is certified that all corrections/suggestions indicated for the internal assessment have been incorporated in the report.The project report has been approved as it satisfies the academic requirements in respect of project work prescribed for the Bachelor of Engineering Degree.

.....	.....
Guide	**Group Convener**
Prof. Prabodh C P	**Dr. Shreenath K N**
Asst. Professor	**Professor**
Dept of CSE, SIT	**Dept of CSE, SIT**
.....	.....
Dr. Poornima A S	**Dr. S V Dinesh**
Professor and Head	**Principal**
Dept of CSE, SIT	**SIT, Tumakuru**

Name of the Examiners — Signature with Date

1. Prof.
2. Prof.

Department of Computer Science and Engineering
Siddaganga Institute of Technology
Tumakuru - 572103

DECLARATION

I hereby declare that the entire work embodied in this dissertation has been carried out by me at **Siddaganga Institute of Technology** under the supervision of ——— ————-. This dissertation has not been submitted in part or full for the award of any diploma or degree of this or any other University.

Name of the student with USN
Department of Computer Science and Engineering
Siddaganga Institute of Technology
Tumakuru - 572103

Acknowledgements

Your acknowledgements go here.....

Abstract

Abstract (Min. 2 pages)

1. Paragraph on motivation to do the current project
2. The social relevance i.e., usefulness of the project to society/user/industry etc.
3. Problem Statement and Objectives (in precise)
4. Process used to solve the problem
5. Objectives achieved and Results summary

Contents

List of Figures

List of Tables

Chapter 1

Introduction

Introduction (8-10 pages)
Chapter Preamble

1.1 Background Study

Detailed discussions on motivation, relevance, social impact, industrial impact, etc.

1.2 Related Works

From research papers, white papers, product descriptions, etc.
Title of the work:
Authors:
Publication details:
Description: I am citing a past work from Mostafa[2]. I am citing a past work from Rumelhart et al[3] I am now citing [1]

1.3 Summary of Gaps identified (optional)

1.4 Project Problem Statement and Objectives (in detail)

1.5 Organization of the Report

Chapter 2

High-level Design

High-level Design (20-30 pages)
Chapter Preamble

2.1 Software development methodology

Discussion on the model used for software development like waterfall, spiral, prototype, incremental, scrum, v-model, and so on.

2.2 Architecture

Block diagram depicting control flow and data flow with description.

2.3 Modules Description (optional)

2.4 Functional Requirements

2.4.1 Name of the function (e.g., search Article, Remove Article, Add Record)

Name of the module:
Parameters:
Purpose:

2.4.2 Name of the function

Name of the module:
Parameters:
Purpose:
.....

Chapter 3

Detailed Design

Detailed Design (20-30 pages)
Chapter Preamble

3.1 Interface design

How does the software interact with people, the system's hardware, other hardware and other software?

3.2 Data Structures and Algorithms

3.2.1 Function Name1

Purpose:
Data Structures used:
Use Cases:
Algorithm:
Error handling:

3.2.2 Function Name2

Purpose:
Data Structures used:
Use Cases:
Algorithm:
Error handling:
.....

3.3 UML diagrams with discussions

3.4 Data Source/Database used and Formats

Chapter 4

Implementation

Implementation (5-10 pages)

4.1 Tools and Technologies

4.1.1 Name1

Brief and relevant description on why this tool/technology is useful in your project

4.1.2 Name2

Brief and relevant description on why this tool/technology is useful in your project

.....

4.2 Experimental Setup

This section includes hardware details, and other infrastructure details

4.3 Coding Standards followed

4.4 Code Integration details

4.5 Implementation work flow

4.6 Execution Results and Discussions

4.7 Non-functional requirements results

Chapter 5

Testing

Testing (5-10 pages)

5.1 Test workflow

5.1.1 Name of the test1

Procedure used for testing

5.1.2 Name of the test2

Procedure used for testing

.....

5.2 Test case details

5.2.1 Test case id:

Unit to test: What to be verified?
Assumptions:
Test data: Variables and their values
Steps to be executed:
Expected result:
Actual result:

Pass/Fail:
Comments:

5.2.2 Test case id:

Unit to test: What to be verified?
Assumptions:
Test data: Variables and their values
Steps to be executed:
Expected result:
Actual result:
Pass/Fail:
Comments:
.....

Chapter 6

Conclusions and Future Scope

Type in the final Conclusions and Future Scope of the research work here

Appendix A

Title of Appendix-A

Type in the details of Appendix-A.

Appendix B

Title of Appendix-B

Type in the details of Appendix-B.

Bibliography

[1] J E Moody and C Darken. Fast learning in networks of locally tuned processing units. *Neural Computation*, 1:281–294, 1989.

[2] Y S Abu Mostafa. The vapnik-chervonenkis dimension: Information verses complexity in learning. *Neural Computation*, 1:312–317, 1989.

[3] D E Rumelhart, G E Hinton, and R J Williams. Learning internal representation by error propagation. *Parallel distributed processing: Explorations in the microstructures of cognition*, 1, 1986.