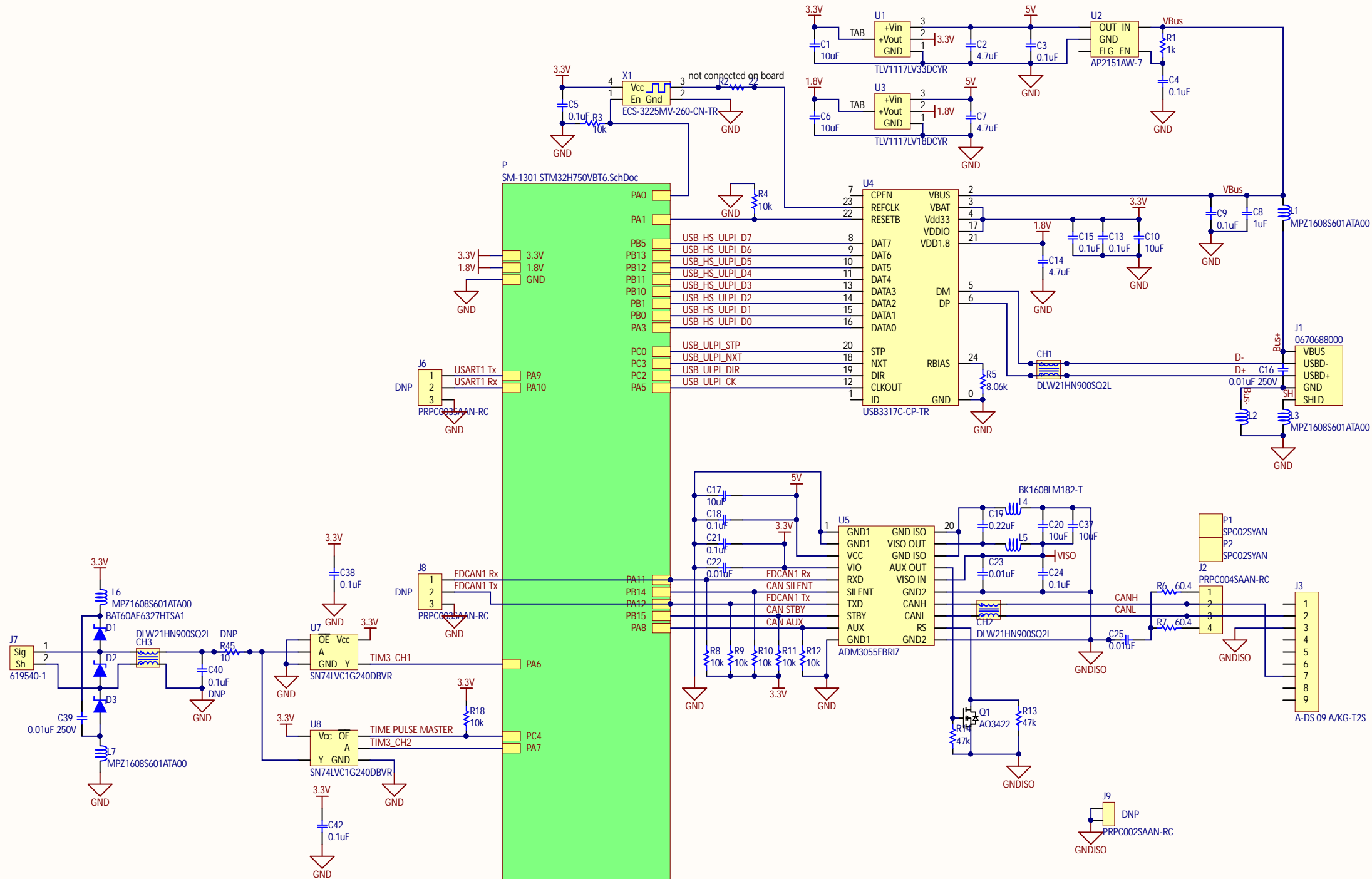
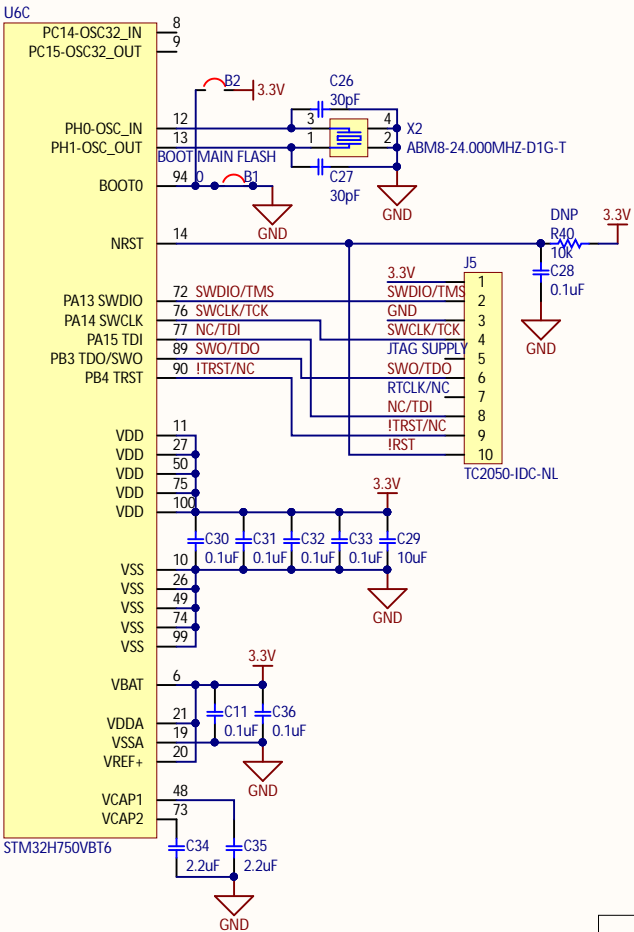
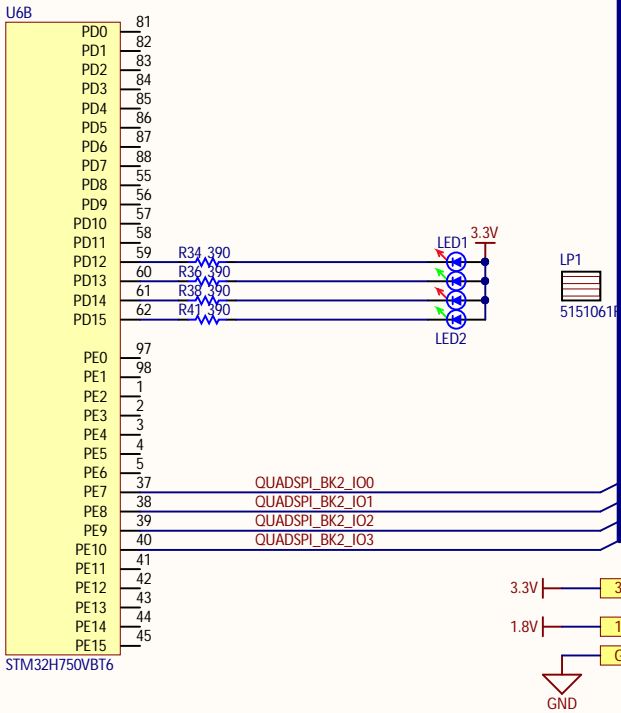
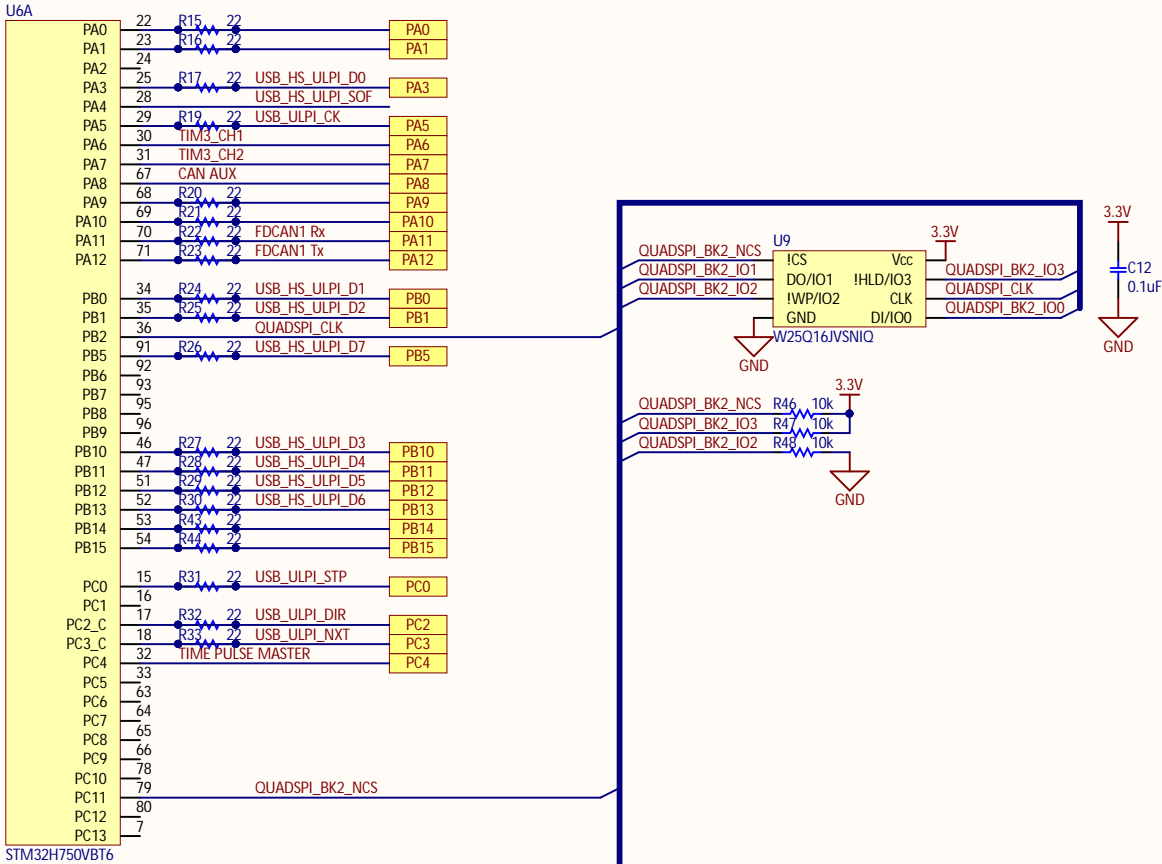


Change Log
Rev A 11/2018
Initial Release

Rev B 1/2019
Removed transistor LED driver. LED now driven directly by processor outputs.
Fixed PA1, PA2, PC2, PC3 labels.
CAN now defaults to standby and silent mode.
Added test header for CAN ttl signals
Added test header for isolated CAN ground
Doubled capacitance to ADM3055E power output
Added quad SPI flash
Added SMA connector and two buffers so multiple boards can synchronize message timestamps.
Switched to standard D9 can bus connector.
Added light pipe so the LED can be visible on the end.
Slope control now 47k or gnd, software controlled.

Rev C 3/2019
Replaced 3.3V and 1.8V regulators due to startup overshoot problem
Changed board size and enclosure fitment
Fixed CAN debug header labeling
Made STM32H7 nRST pullup (R40) Standard (No BOM).





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|--|--------------|----------|-----------|--------------------------|--|---|
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| | Drawn | NJW | 3/22/2019 | Dwg #: | | |
| | Checked | NJW | | SM-1301 | | |
| | Mfg Appr | | | Revision: C | | |
| | Eng Appr | NJW | | Project: | | |
| | Special Appr | | | Page: 2 of 2 | | |