in1intest

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DESCRIPTIONS

MODULE example_2

 $example_2$

Basic Inheritance documentation : mod_3 inherits both mod_1 and mod_2 . Inherits $v2_m1$, $v2_m2$, Overrides $v1_m1$, new locals $v2_m3$. Interface Inheritance : mod_4 inherits interface iface_1, overrides $v1_i$

Children

- 1. rec 1
- 2. rec 2
- 3. rec_3
- 4. mod 1
- 5. mod_2
- 6. mod_3
- 7. iface 1
- 8. mod_4

RECORD rec_1

 $example_2 \setminus$

 ${
m rec}_1$

RECORD rec_2

example $_2$ \

 rec_2

RECORD rec_3

example $_2$ \

 rec_3

MODULE mod_1

example_2 \setminus

 mod_1

Children

- 1. v1_m1
- 2. v2_m1

ATTRIBUTE v1_m1

example $_2 \setminus \text{mod}_1 \setminus$

real8

 $v1_m1$

ATTRIBUTE v2_m1

example $_2 \setminus \text{mod}_1 \setminus$

 $v2_m1$

MODULE mod_2

example $_2$ \

 mod_2

Children

- 1. v1_m1
- 2. v2_m2

ATTRIBUTE v1_m1

example $_2 \setminus \text{mod}_2 \setminus$

 $v1_m1$

ATTRIBUTE v2_m2

example $_2 \setminus \text{mod}_2 \setminus$

 $v2_m2$

MODULE mod_3

example $_2$ \

 mod_3

Children

- 1. v2_m1
- 2. v2_m2
- 3. v1_m1
- 4. v2_m3

ATTRIBUTE v2_m1

example $_2 \setminus \text{mod}_3 \setminus$

 $v2_m1$

INHERITED True

ATTRIBUTE v2_m2

example_2 \ mod_3 \

 $v2_m2$



ATTRIBUTE v1_m1

example $_2 \setminus \text{mod}_3 \setminus$

v1_m1

OVERRIDE True

ATTRIBUTE v2_m3

example $_2 \setminus \text{mod}_3 \setminus$

v2_m3

INTERFACE iface_1

example_2 \setminus

iface_1

${\bf Children}$

1. v1_i1

ATTRIBUTE v1_i1

example $_2 \setminus iface_1 \setminus$

real8 v1_i1

MODULE mod_4

example $_2$ \

 mod_4

Children

- 1. v1_i1
- 2. v2_m4

ATTRIBUTE v1_i1

example $_2 \setminus \text{mod}_4 \setminus$

 $v1_i1$

OVERRIDE True

ATTRIBUTE v2_m4

 $example_2 \setminus mod_4 \setminus$

STRING20 v2_m4



$\begin{array}{c} {\rm intest/\ in1intest/} \\ example_3 \end{array}$

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DESCRIPTIONS

MODULE Example_3

 $Example_3$

Example: Inheritance across files mod_1 in Example_4 inherits mod_1 in Example_3

Children

1. mod_1

MODULE mod_1

Example_3 \

 mod_1

Children

- 1. v1_m1
- 2. v2_m1_ex3

ATTRIBUTE v1_m1

Example_3 \ mod_1 \

 $v1_m1$

ATTRIBUTE v2_m1_ex3

 $Example_3 \ \backslash \ mod_1 \ \backslash$

 $v2_m1_ex3$

$\begin{array}{c} {\rm intest/\ in1intest/} \\ example_4 \end{array}$

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IMPORTS

 $Example_3.mod_1$

DESCRIPTIONS

MODULE example_4

 $example_4$

Example : Inheritance across files mod_1 in Example_4 inherits mod_1 in Example_3

Children

1. mod_1

MODULE mod_1

example $_4$ \

 mod_1

Children

```
    v2_m1_ex4
    v1_m1 : Doc test 2
    v2_m1_ex3 : DOC Test 3
    long_name
```

ATTRIBUTE v2_m1_ex4

```
example_4 \setminus \text{mod}_1 \setminus
```

```
v2_m1_ex4
```

ATTRIBUTE v1_m1

example $_4 \setminus \text{mod}_1 \setminus$

v1_m1

Doc test 2. Title end by period not newline

ABCD ||||
CDEF ||||

INHERITED True

ATTRIBUTE v2_m1_ex3

 $example_4 \setminus mod_1 \setminus$

 $v2_m1_ex3$

DOC Test 3 No Period title

INHERITED True

FUNCTION long_name

 $example_4 \setminus mod_1 \setminus$

long_name

(DATASET({REAL8 u}) X, DATASET({REAL8 u}) IntW, DATASET({REAL8 u}) Intb, REAL8 BETA=0.1, REAL8 sparsityParam=0.1, REAL8 LAMBDA=0.001, REAL8 ALPHA=0.1, UNSIGNED2 MaxIter=100)

INHERITED True

$\begin{array}{c} {\rm intest/\ in1intest/} \\ example_{7} \end{array}$

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DESCRIPTIONS

MODULE example_7

example_7

Basic Type Example Source Code copied from ECL Documentation

Children

1. R

RECORD R

example $_7$ \

 \mathbf{R}