

# intest

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# intest/ example\_\_11

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## IMPORTS

std | intest | Example\_\_3 | intest.Example\_\_3 | intest.inintest | intest.inintest.Example\_\_3  
| test | Inintest | Inintest.Example\_\_3 |

## DESCRIPTIONS

**MODULE** example\_\_11

|  |             |
|--|-------------|
|  | example__11 |
|--|-------------|

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# intest/ example\_\_2

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## DESCRIPTIONS

### **MODULE** example\_\_2

|            |
|------------|
| example__2 |
|------------|

Basic Inheritance documentation : mod\_3 inherits both mod\_1 and mod\_2 . Inherits v2\_m1, v2\_m2, Overrides v1\_m1, new locals v2\_m3 . Interface Inheritance : mod\_4 inherits interface iface\_1, overrides v1\_i1

### Children

1. [rec\\_1](#)
  2. [rec\\_2](#)
  3. [rec\\_3](#)
  4. [mod\\_1](#)
  5. [mod\\_2](#)
  6. [mod\\_3](#)
  7. [iface\\_1](#)
  8. [mod\\_4](#)
- 

### **RECORD** rec\_1

[example\\_\\_2 \](#)

|  |       |
|--|-------|
|  | rec_1 |
|--|-------|

---

## **RECORD** rec\_2

[example\\_2](#) \

|  |       |
|--|-------|
|  | rec_2 |
|--|-------|

---

## **RECORD** rec\_3

[example\\_2](#) \

|  |       |
|--|-------|
|  | rec_3 |
|--|-------|

---

## **MODULE** mod\_1

[example\\_2](#) \

|  |       |
|--|-------|
|  | mod_1 |
|--|-------|

### Children

- 1. [v1\\_m1](#)
- 2. [v2\\_m1](#)

---

## **ATTRIBUTE** v1\_m1

[example\\_2](#) \ [mod\\_1](#) \

|       |       |
|-------|-------|
| real8 | v1_m1 |
|-------|-------|

---

## ATTRIBUTE v2\_m1

example\_2 \ mod\_1 \

|  |       |
|--|-------|
|  | v2_m1 |
|--|-------|

---

## MODULE mod\_2

example\_2 \

|  |       |
|--|-------|
|  | mod_2 |
|--|-------|

### Children

- 1. v1\_m1
- 2. v2\_m2

---

## ATTRIBUTE v1\_m1

example\_2 \ mod\_2 \

|  |       |
|--|-------|
|  | v1_m1 |
|--|-------|

---

## ATTRIBUTE v2\_m2

example\_2 \ mod\_2 \

|  |       |
|--|-------|
|  | v2_m2 |
|--|-------|

MODULE

mod\_3

example\_2 \

|  |       |
|--|-------|
|  | mod_3 |
|--|-------|

Children

- 1. v2\_m1
- 2. v2\_m2
- 3. v1\_m1
- 4. v2\_m3

ATTRIBUTE

v2\_m1

example\_2 \ mod\_3 \

|  |       |
|--|-------|
|  | v2_m1 |
|--|-------|

INHERITED

True

ATTRIBUTE

v2\_m2

example\_2 \ mod\_3 \

|  |       |
|--|-------|
|  | v2_m2 |
|--|-------|

**INHERITED** True

---

**ATTRIBUTE** v1\_m1

[example\\_2 \ mod\\_3 \](#)

|  |       |
|--|-------|
|  | v1_m1 |
|--|-------|

**OVERRIDE** True

---

**ATTRIBUTE** v2\_m3

[example\\_2 \ mod\\_3 \](#)

|  |       |
|--|-------|
|  | v2_m3 |
|--|-------|

**INTERFACE** iface\_1

[example\\_2 \](#)

|  |         |
|--|---------|
|  | iface_1 |
|--|---------|

Children

1. [v1\\_i1](#)
-

## ATTRIBUTE v1\_i1

example\_2 \ iface\_1 \

|       |       |
|-------|-------|
| real8 | v1_i1 |
|-------|-------|

---

## MODULE mod\_4

example\_2 \

|       |
|-------|
| mod_4 |
|-------|

### Children

1. v1\_i1
  2. v2\_m4
- 

## ATTRIBUTE v1\_i1

example\_2 \ mod\_4 \

|       |
|-------|
| v1_i1 |
|-------|

## OVERRIDE True

---

## ATTRIBUTE v2\_m4

example\_2 \ mod\_4 \

|          |       |
|----------|-------|
| STRING20 | v2_m4 |
|----------|-------|





# intest/ example\_\_3

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## DESCRIPTIONS

### **MODULE** Example\_3

|  |           |
|--|-----------|
|  | Example_3 |
|--|-----------|

Example : Inheritance across files mod\_1 in Example\_4 inherits mod\_1 in Example\_3

#### Children

1. [mod\\_1](#)
- 

### **MODULE** mod\_1

[Example\\_3](#) \

|  |       |
|--|-------|
|  | mod_1 |
|--|-------|

#### Children

1. [v1\\_m1](#)
  2. [v2\\_m1\\_ex3](#)
-

## ATTRIBUTE v1\_m1

Example\_3 \ mod\_1 \

|  |       |
|--|-------|
|  | v1_m1 |
|--|-------|

---

## ATTRIBUTE v2\_m1\_ex3

Example\_3 \ mod\_1 \

|  |           |
|--|-----------|
|  | v2_m1_ex3 |
|--|-----------|

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intest/

# example\_\_4

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[Go Up](#)

## IMPORTS

Example\_\_3.mod\_\_1 |

## DESCRIPTIONS

### **MODULE** example\_\_4

|  |            |
|--|------------|
|  | example__4 |
|--|------------|

Example : Inheritance across files mod\_\_1 in Example\_\_4 inherits mod\_\_1 in Example\_\_3

#### Children

1. [mod\\_\\_1](#)

---

### **MODULE** mod\_\_1

[example\\_\\_4 \](#)

|  |        |
|--|--------|
|  | mod__1 |
|--|--------|

## Children

1. [v2\\_m1\\_ex4](#)
2. [v1\\_m1](#) : Doc test 2
3. [v2\\_m1\\_ex3](#) : DOC Test 3
4. [long\\_name](#)

---

### **ATTRIBUTE** v2\_m1\_ex4

[example\\_4](#) \ [mod\\_1](#) \

|  |           |
|--|-----------|
|  | v2_m1_ex4 |
|--|-----------|

---

### **ATTRIBUTE** v1\_m1

[example\\_4](#) \ [mod\\_1](#) \

|  |       |
|--|-------|
|  | v1_m1 |
|--|-------|

Doc test 2. Title end by period not newline

ABCD ||||  
CDEF ||||

**INHERITED** True

---

### **ATTRIBUTE** v2\_m1\_ex3

[example\\_4](#) \ [mod\\_1](#) \

|  |           |
|--|-----------|
|  | v2_m1_ex3 |
|--|-----------|

DOC Test 3 No Period title

**INHERITED** True

**FUNCTION** long\_name

example\_4 \ mod\_1 \

|  |           |
|--|-----------|
|  | long_name |
| (DATASET({REAL8 u}) X, DATASET({REAL8 u}) IntW, DATASET({REAL8 u}) Intb, REAL8 BETA=0.1, REAL8 sparsityParam=0.1 , REAL8 LAMBDA=0.001, REAL8 ALPHA=0.1, UNSIGNED2 MaxIter=100) |           |

**INHERITED** True

intest/

# example\_\_7

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## DESCRIPTIONS

**MODULE** example\_\_7

|  |            |
|--|------------|
|  | example__7 |
|--|------------|

Basic Type Example Source Code copied from ECL Documentation

### Children

- 1. [R](#)

---

**RECORD** R

[example\\_\\_7 \](#)

|  |   |
|--|---|
|  | R |
|--|---|

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