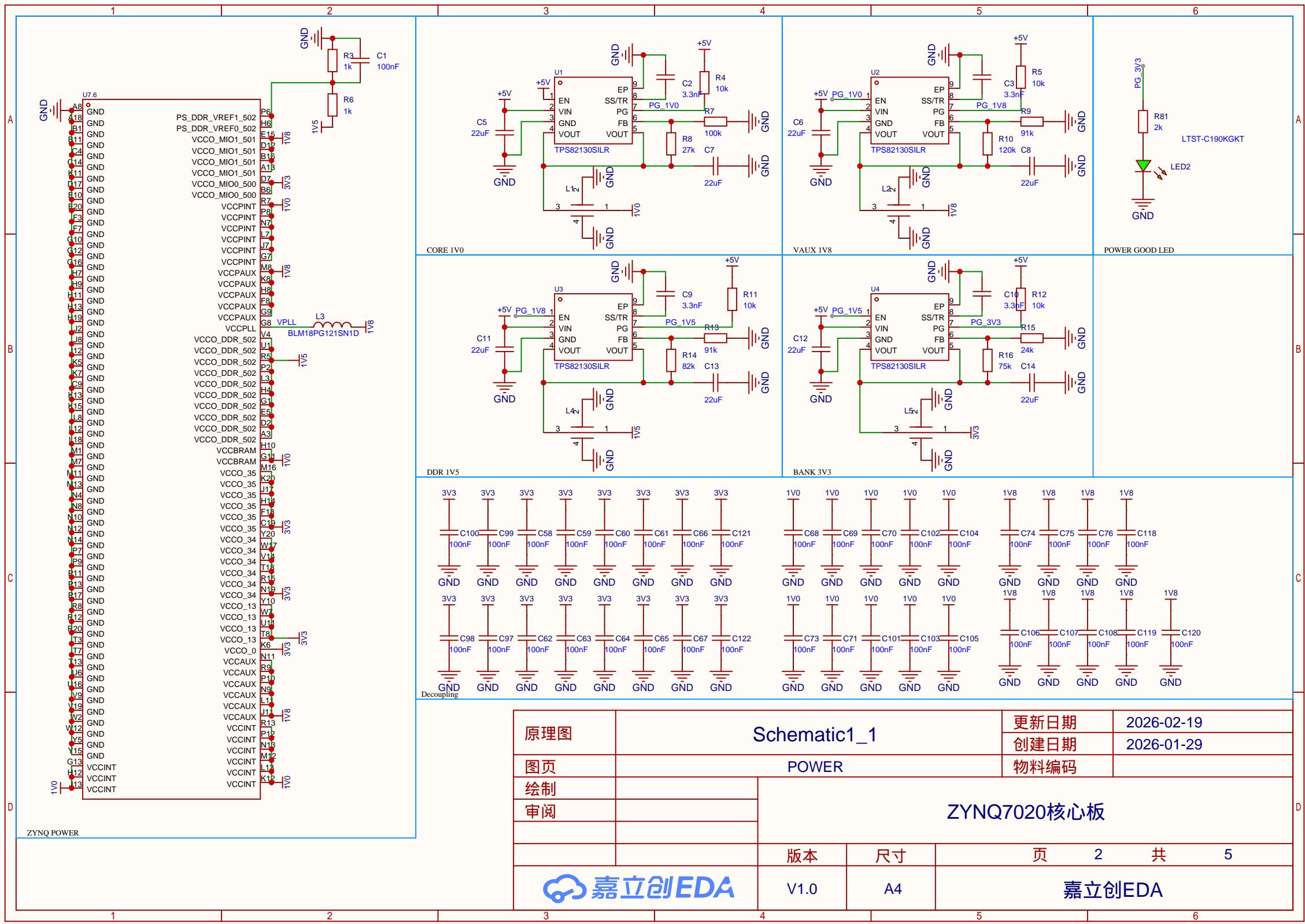
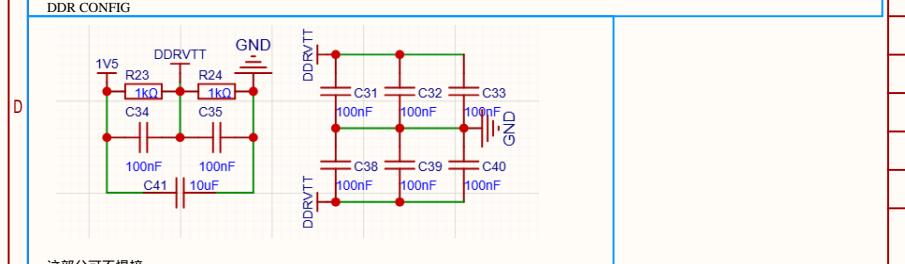
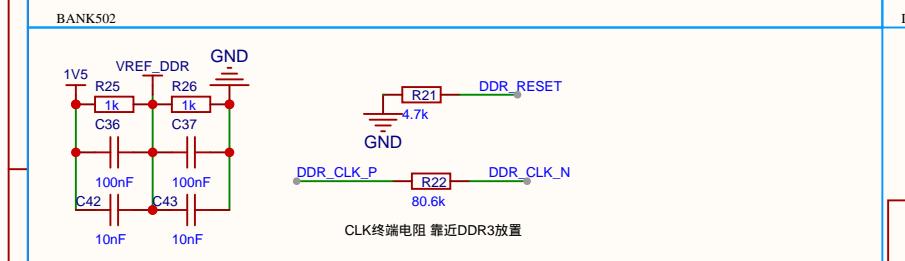
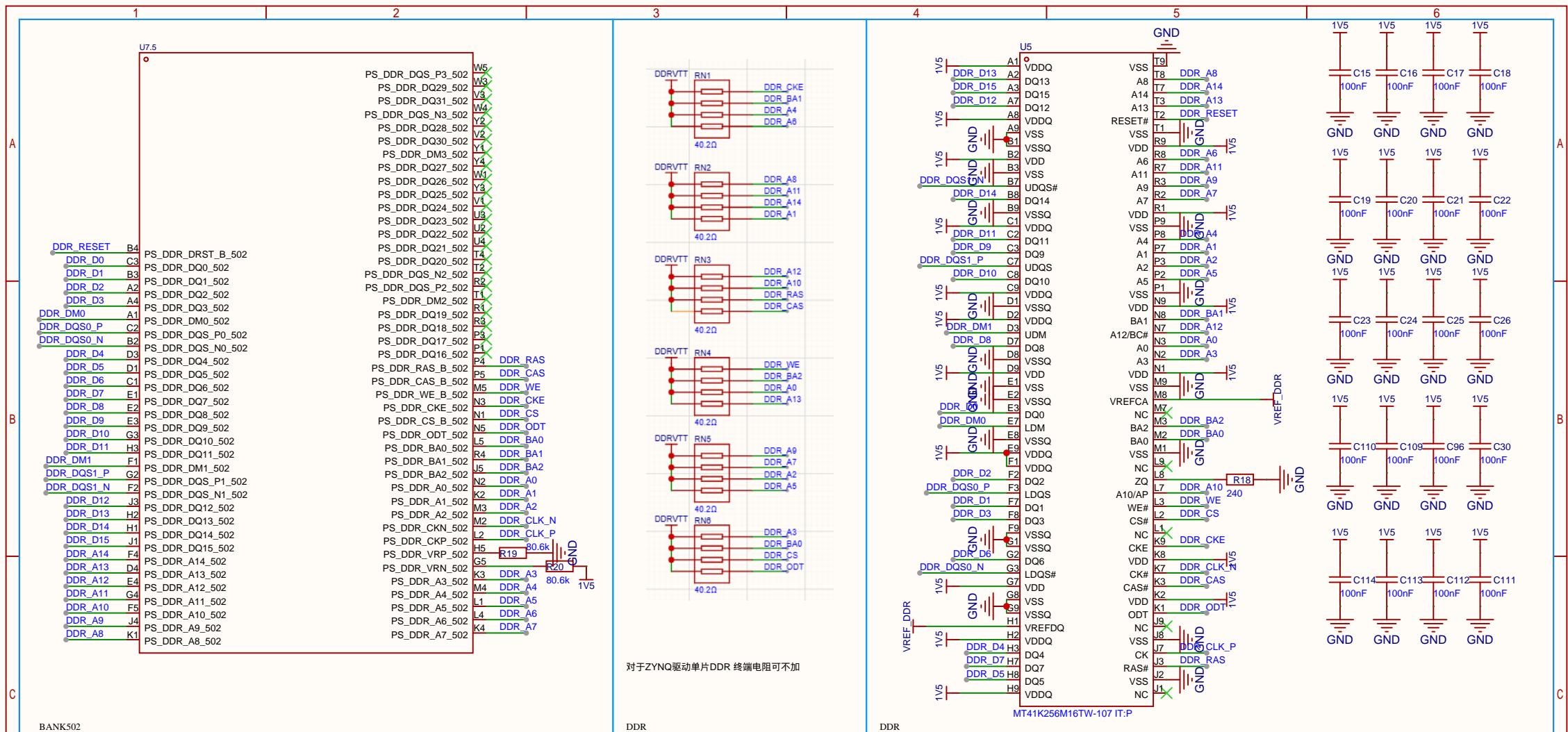


V1.0的排版





原理图

Schematic1_1

图页

DDR3

绘制

更新日期 2026-02-03

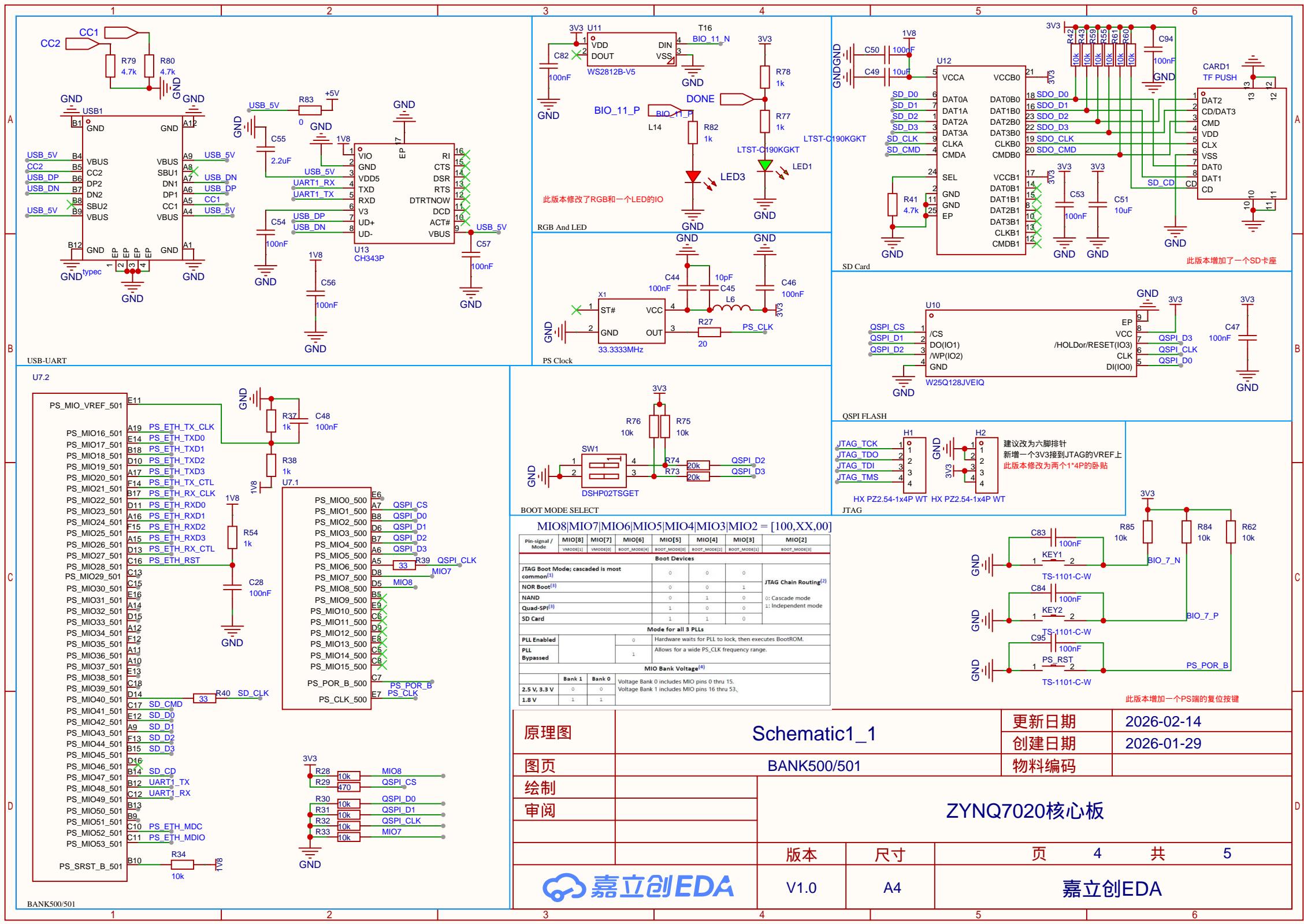
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创建日期 2026-01-29

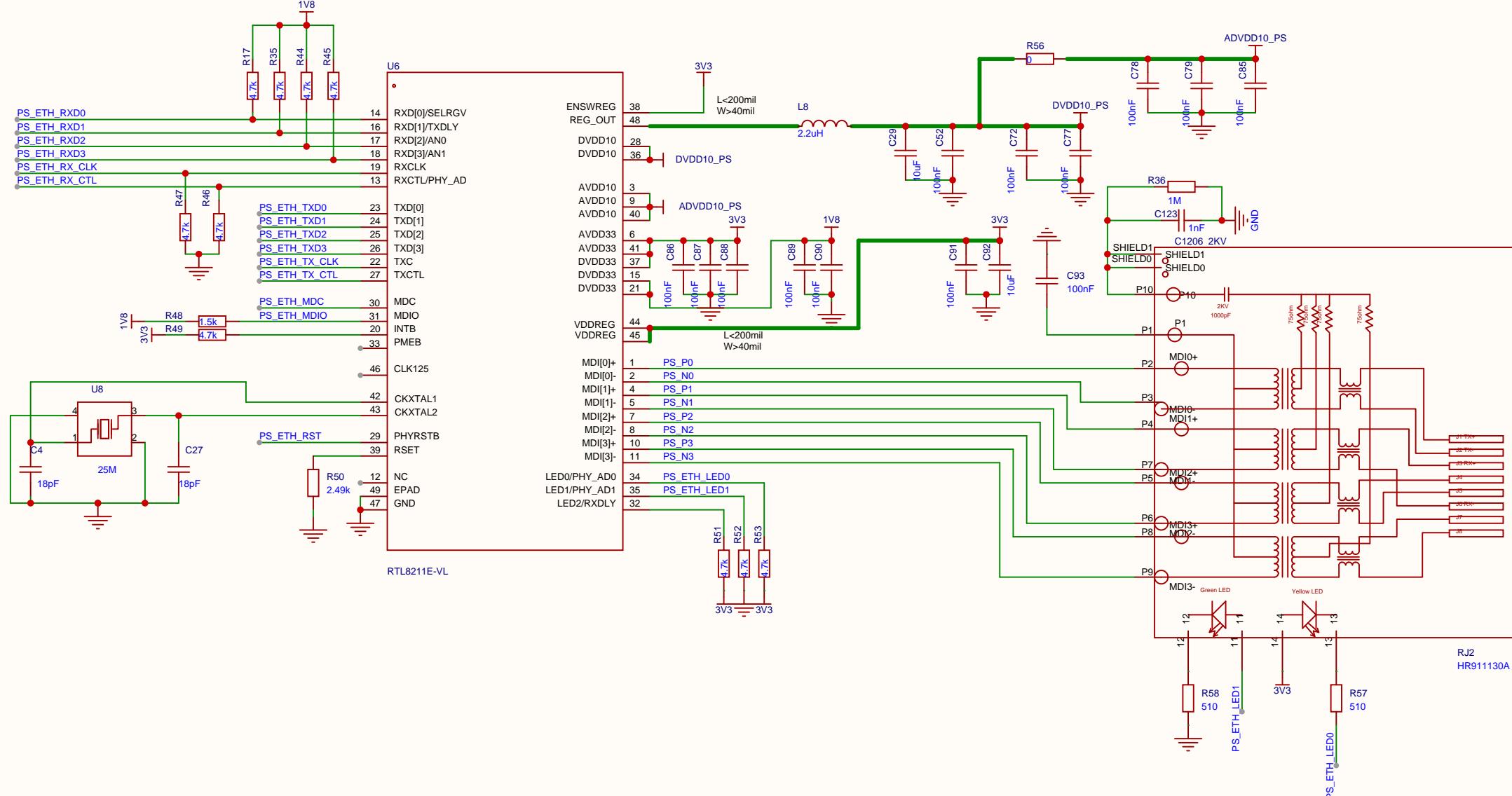
版本

尺寸

页 3 共 5



1 2 3 4 5 6



原理图	Schematic1_1	更新日期	2026-02-03
图页	PHY	创建日期	2026-01-30
绘制	ZYNQ7020核心板		
审阅		版本	尺寸
		V1.0	A4
		嘉立创EDA	嘉立创EDA