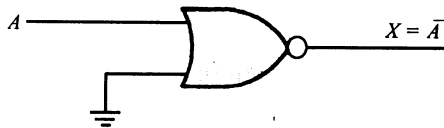
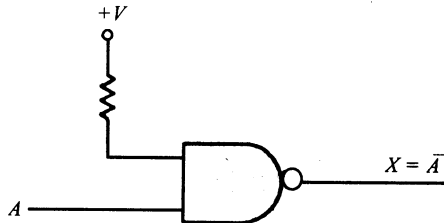




(a)



(b)

Fig. 3-3 NAND and NOR gates used as inverters: (a) theoretical method. and (b) practical method to minimize input loading.