

<i>A</i>	<i>B</i>	<i>X</i>
0	0	0
0	1	1
1	0	1
1	1	1

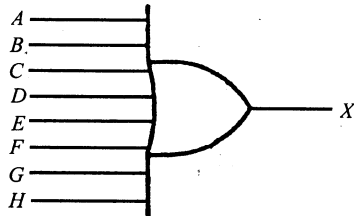
(a)

<i>A</i>	<i>B</i>	<i>X</i>
L	L	L
L	H	H
H	L	H
H	H	H

(b)



(c)



(d)

Fig. 3-1 Truth tables and symbols for the OR gate: (a) truth table for positive logic. (b) function table. (c) symbol for 2-input OR gate. and (d) symbol for 8-input OR gate.