

RESUME

SUDARSAN VEERAVALLI

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Career Objective:-

I am an enthusiastic and highly motivated team member with a keen determination to apply my knowledge, research and analytical skills with an organization that values commitment and a flexible approach to work in a dynamic environment.

Academic Profile:-

Course	University/ Board	Institution	Year of Passing	Percentage(%)
B.Tech (Electronics and Communication Engineering)	SASTRA Deemed To Be University, Thanjavur.	School of Electrical and Electronics, SASTRA.	2018	6.0178 (CGPA)
Intermediate (M.P.C)	Board of Intermediate Education - Andhra Pradesh.	NRI ARTS & SCIENCE JUNIOR COLLEGE, Tirupati, Andhra Pradesh, India.	2014	91.1
Secondary School Certificate (SSC)	Board of Secondary Education – Andhra Pradesh.	Geethanjali English Medium High School, Tirupati, Andhra Pradesh, India.	2012	8.7 (GPA)

Technical Skills:-

- Programming : Good in C, C++, JavaEE, Angular 7

- Design Tools : MATLAB, Microsoft Office.
- Operating systems familiar with : LINUX, Windows XP, 8 and 10.

Internships:-

- Successfully completed a remote internship in marketing management at the Indian Institute of Management Lucknow.
- Successfully completed a remote internship in Data Science and Analytics at the Indian Institute of Management Lucknow.
- Successfully completed internship as a full stack developer at Jocata Financial Advisory and Technology.

Activities:-

- Core member at IEEE student branch, SASTRA.
- Organizer of team workshops Daksh (The Technical Fest of SASTRA).
- Participated in WEBENCH workshop conducted by TI(Texas Instruments).
- Organizer of PCB designing workshop hosted by IEEE student branch, SASTRA.

Projects Executed:-

Project 1:

Title : DLP (Digital Learning Platform)

Type : Product

Team Size : Twenty-one

Environment : JavaEE and MySQL

Description :

DLP (Digital Learning Platform) is a core product of Jocata which is built with a set of microservices that communicates with each other.

This product Comprises of all types of modules that are needed by all the projects in Jocata. Each and every module is built with a comprehensive list of sources which are purely developed according to the intrest of the client.

Roles & Responsibilities:

Ø Development and Enhancement of the following modules:

- User Management System

Project 2:

Title : Application for Customer Risk Categorization

Type : Project

Client : ICICI

Team Size : Seven

Environment : Angular 7, JavaEE and MySQL

Description : This CRC application is developed for the bank's MLRO, RM to categorize the customers according to their risk. This risk factor is based on the customer's personal and professional details. This application helps the bank to easily provide loans to the customer

Roles & Responsibilities:

Ø Development and Enhancement of the following modules:

- User Management System(UI) with Maker Checker

project 3:

Title : Performance Characteristics Analysis of Faraday Cage

Role : Team leader and did a major role in Design

Team Size : Three

Software : EMPro (For design)

Description :

Analysis of protective structure's effectiveness against Electromagnetic Interference (EMI) in High Voltage Laboratories is significant nowadays. In this paper, the effectiveness of a Faraday cage with dimensions 8.2m x 6.2m x 5m

for High Voltage laboratory is determined. However, simulation models are limited; the cage is scaled down by a factor of 500 and is simulated in EMPro3D EM simulation software. The cage is simulated using Finite Difference Time Domain (FDTD) 3D field solver since it provides memory efficient results for the considered structure. The obtained simulation results are compared with practical results. The effects of cage's dimension, thickness, and material on its Shielding Effectiveness (SE) are determined. We have obtained an average SE of 18 dB for our structure and the ways to further improve the SE are suggested.

Keywords: Electromagnetic Interference (EMI), High Voltage Laboratory, Faraday cage, EMPro, Finite Difference Time Domain (FDTD), Shielding Effectiveness (SE).

Strengths:-

- Good communication skills
- Honest
- Fast learner
- Punctual

Industrial Visit:-

- High Voltage Laboratory, SASTRA where we have evaluated the practical results with the theoretical results obtained using the EMPro simulator

Personal Details:-

Father's Name	MOHAN BABU V
Mother's Name	MANJU LATHA V
Date of Birth	30 march, 1997
Passport Number	Z3946602
Nationality	Indian
Marital Status	Single
Languages Known	Telugu, Tamil, Hindi and English
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Declaration:-

I hereby declare that the above furnished information is true to the best of my knowledge.

SUDARSAN VEERAVALLI