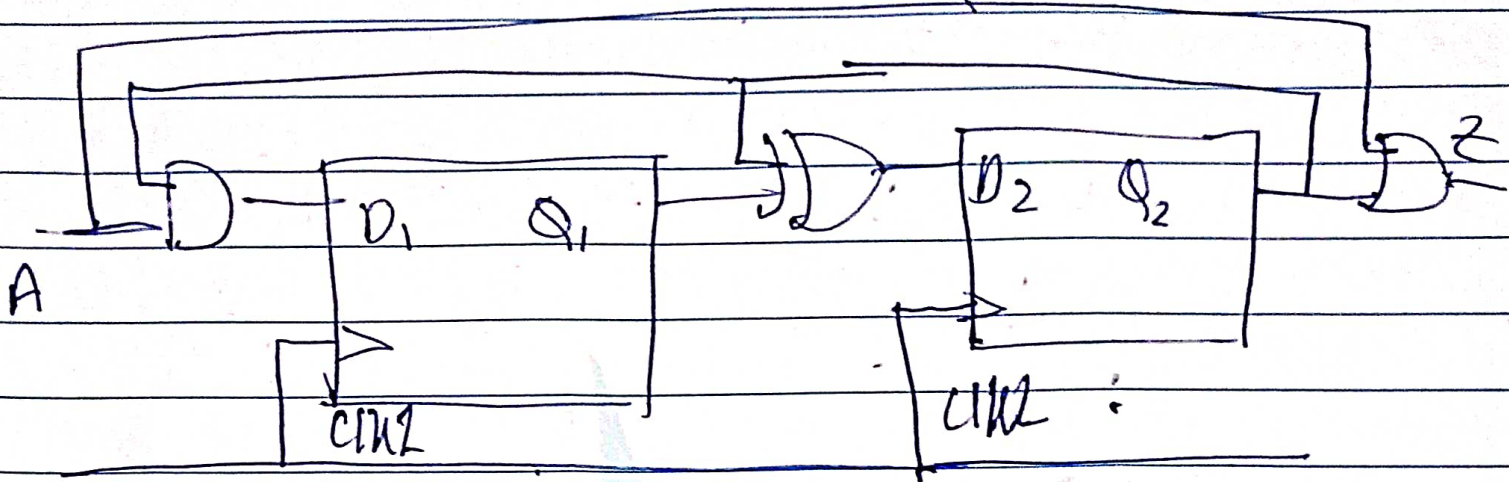


combinational logic \* LIVIDUS



clk-

From A to  $D_1$  (primary i/p to ff)

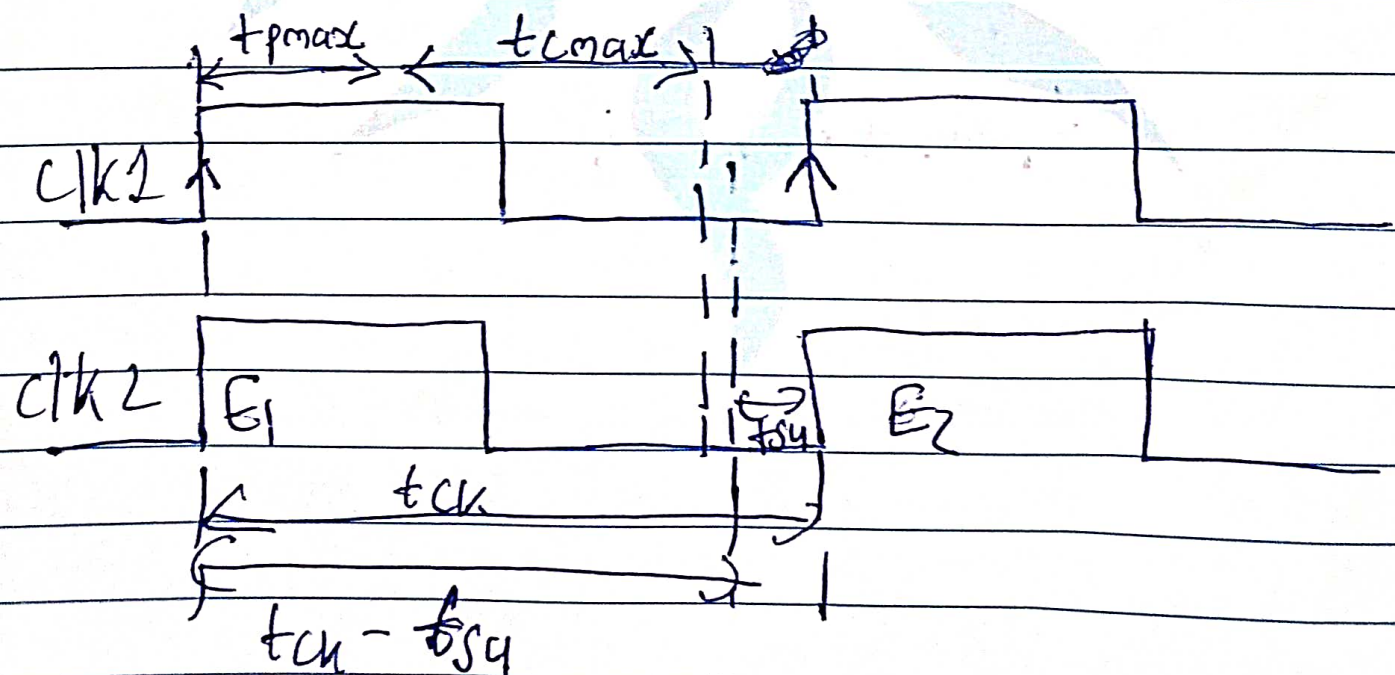
From  $D_1$  to  $D_2$  including the XOR (ff to ff)

From  $D_2$  via XOR to  $D_2$  (ff to ff)

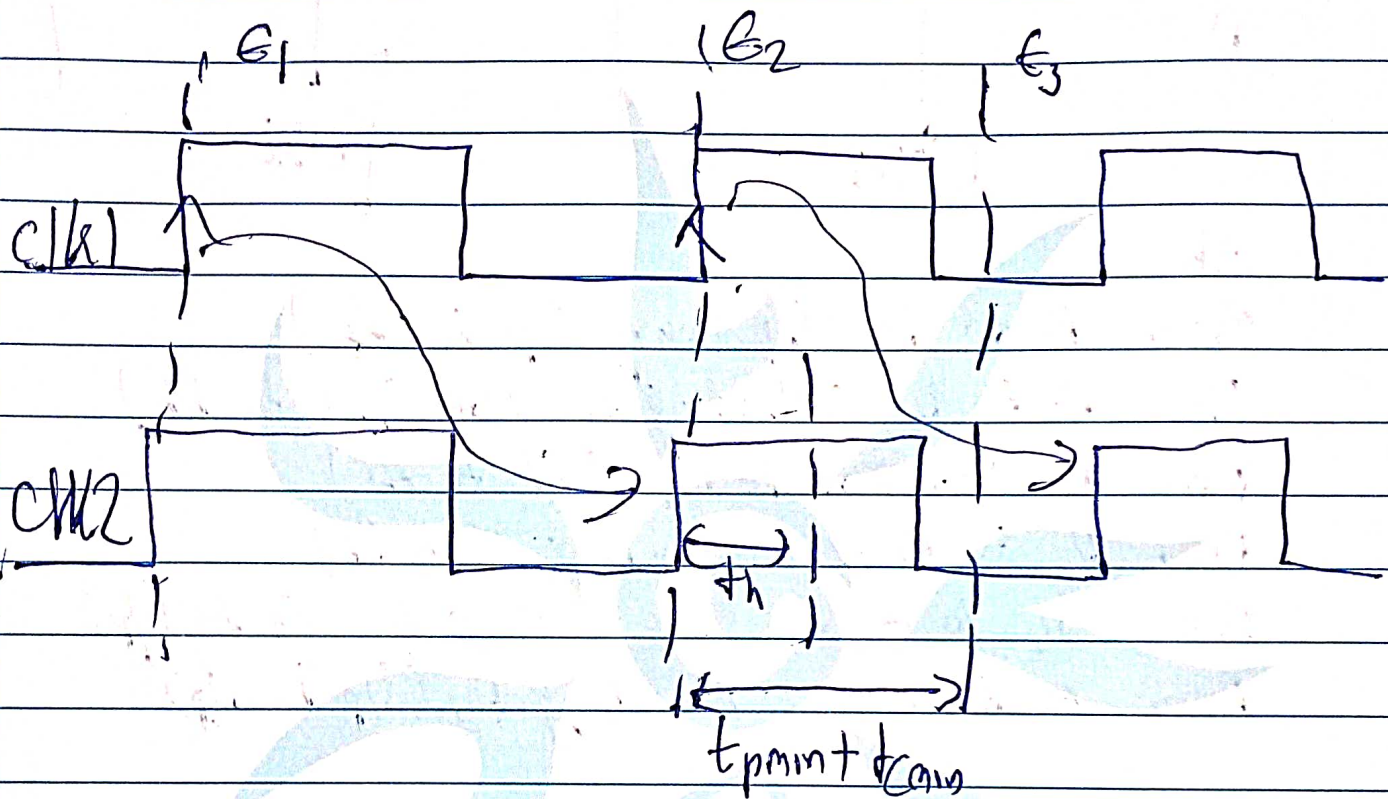
From  $D_2$  to  $P_1$  via AND (ff to ff)

From  $D_2$  to Z via the OR gate (ff to o/p)

From A to Z via the OR gate (i/p to o/p)



$$t_{clk} - t_{pmax} - t_{comax} - t_{su} \geq 0$$



$$t_{pmin} + t_{com} \geq t_h$$