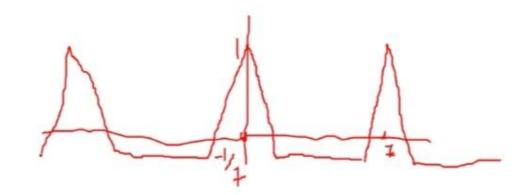
Explain the phoposties of ML Sequence For a Sequence generated from 3-Stage Shift hegister with Linear feedback. verify these proposties & determine the period of the given PN Sequence 01011100101110

# Holla



Explain the phopoties of ML Sequence to a Sequence generated from 3-Stage Shift register with Linear feedback. verify these proporties & determine the period of the given PN Sequence 01011100101110

Navya Holla

ASSOCIATION A A pseudo random sequence is generated using a feed back shift register of length m=4. The chip rate is 107 chips per second. Find the following a) PN sequence length b) Chip duration of PN sequence c) PN sequence period Solution

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- a) Length of PN sequence  $N = 2^{m}-1$  $=2^{4}-1=15$
- b) Chip duration  $T_c = 1/\text{chip rate} = 1/107 = 0.1 \mu\text{sec}$
- c) PN sequence period  $T = NT_c$  $= 15 \times 0.1 \mu sec = 1.5 \mu sec$

## Example2

A direct sequence spread binary phase shift keying system uses a feedback shift register of length 19 for the generation of PN sequence. Calculate the processing gain of the system.

### Solution

Given length of shift register = m = 19

Therefore length of PN sequence 
$$N = 2^m - 1$$
  
=  $2^{19} - 1$   
Processing gain  $PG = T_b/T_c = N$ 

Example3

in db = 
$$10\log_{10}N = 10\log_{10}(2^{19})$$
  
=  $57db$ 

A Spread spectrum communication system has the following parameters.

A Spread spectrum communication system has the following parameters.

Information bit duration Tb = 1.024 msecs and PN chip duration of 1µsecs. The average probability of error of system is not to exceed 10<sup>-5</sup>, calculate a) Length of shift register b) Processing gain c) jamming margin Solution

Processing gain PG =N= Tb/Tc =1024 corresponding length of shift register m = 10 In case of coherent BPSK For Probability of error  $10^{-5}$ .

[Referring to error function table]

$$E_b/N_0 = 10.8$$

Therefore jamming margin

(jamm ing mar gin) 
$$_{dB} = (Processi ng gain) _{dB} = (Processi ng gain) _{dB} = (Processi ng gain) _{dB} = 10 log _{10} PG _{dB} - 10 log _{10} \left(\frac{Eb}{N_0}\right)_{min}$$

$$= 10 log _{10} 1024 - 10 log _{10} 10 .8$$

$$=30.10-10.33$$

$$= 19.8 \text{ db}$$

#### Example3

A Spread spectrum communication system has the following parameters. Information bit duration Tb = 1.024 msecs and PN chip duration of 1 $\mu$ secs. The average probability of error of system is not to exceed  $10^{-5}$ . calculate a) Length of

#### Solution

Processing gain PG =N= Tb/Tc =1024 corresponding length of shift register m = 10

In case of coherent BPSK For Probability of error 10-5.

shift register b) Processing gain c) jamming margin

[Referring to error function table]

$$E_b/N_0 = 10.8$$

Therefore jamming margin

(jamm ing mar gin) 
$$_{dB}$$
 = (Processi ng gain)  $_{dB}$  - 10log  $_{10}$   $\left(\frac{Eb}{N_0}\right)_{min}$  (jamm ing mar gin)  $_{dB}$  = 10log  $_{10}$  PG  $_{dB}$  - 10log  $_{10}$   $\left(\frac{Eb}{N_0}\right)_{min}$  = 10log  $_{10}$  1024 - 10log  $_{10}$  10 .8

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$$= 30.10 - 10.33$$
  
= 19.8 db



- The type of spread spectrum in which the carrier hops randomly from one frequency to another is called frequency-hop(FH) spread spectrum.
- A common modulation format for FH systems is that of M-ary frequency shift keying(MFSK). The combination is referred as FH/MFSK

Since frequency hopping does not cover the entire spread spectrum instantaneously, we are led to consider the rate at which the hops occur. In this context, we may identify two basic (technology-independent) characterizations of frequency hopping:

- Slow-frequency hopping, in which the symbol rate R<sub>s</sub> of the MFSK signal is an integer multiple of the hop rate R<sub>h</sub>. That is, several symbols are transmitted on each frequency hop.
- Fast-frequency hopping, in which the hop rate Rh is an integer multiple.
   the MFSK symbol rate Rs. That is, the carrier frequency will change or hop several times during the transmission of one symbol.





