# Module 4

## Structures for Realization of Discrete time systems:

Realization of FIR and IIR filters using direct form structures and cascade form structures, Lattice structure for FIR Filters.

### Introduction to Digital Signal (DS) Processors:

Introduction, Digital Signal Processor Architecture- Von Neumann architecture and Harvard architecture, Fixed- and Floating-Point Format for DS processors.

### **Text Books**

Proakis&Manolakis, "Digital signal processing – Principles Algorithms & Applications", *Pearson education*, 4<sup>th</sup> Edition, New Delhi, 2007.

Li Tan, "Digital Signal Processing", Academic Press, Elsevier, 2007.

## Basic IIR Filter Structures

The causal IIR digital filters we are concerned with in this course are characterized by a real rational transfer function of  $z^{-1}$  or, equivalently by a constant coefficient difference equation

From the difference equation representation, it can be seen that the realization of the causal IIR digital filters requires some form of feedback

An N-th order IIR digital transfer function is characterized by 2N+1 unique coefficients, and in general, requires 2N+1 multipliers and 2N two-input adders for implementation

Direct form IIR filters: Filter structures in which the multiplier coefficients are precisely the coefficients of the transfer

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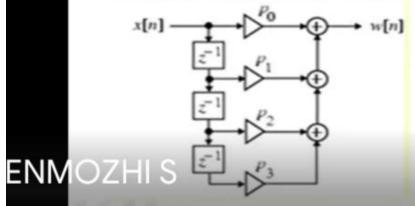
## Contd...

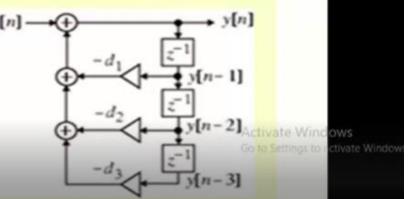
$$H(z) = \frac{P(z)}{D(z)} = \frac{p_0 + p_1 z^{-1} + p_2 z^{-2} + p_3 z^{-3}}{1 + d_1 z^{-1} + d_2 z^{-2} + d_3 z^{-3}}$$

$$H_1(z) = \frac{W(z)}{X(z)} = P(z) = p_0 + p_1 z^{-1} + p_2 z^{-2} + p_3 z^{-3}$$

$$H_2(z) = \frac{Y(z)}{W(z)} = \frac{1}{D(z)} = \frac{1}{1 + d_1 z^{-1} + d_2 z^{-2} + d_3 z^{-3}}$$

$$w[n] = p_0 x[n] + p_1 x[n-1] + p_2 x[n-2] + p_3 x[n-3] \quad y[n] = w[n] - d_1 y[n-1] - d_2 y[n-2] - d_3 y[n-3]$$

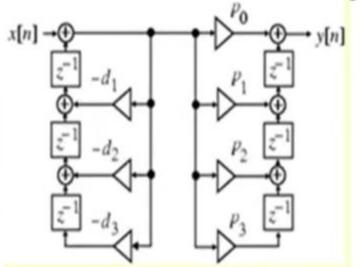


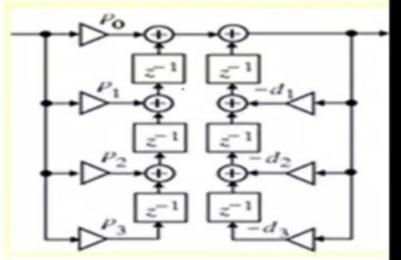


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 The direct form-I structure is non canonical as it requires 6 delay elements to realize a 3<sup>rd</sup>

order transfer function





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