Module 4

Introduction to Digital Signal (DS) Processors:

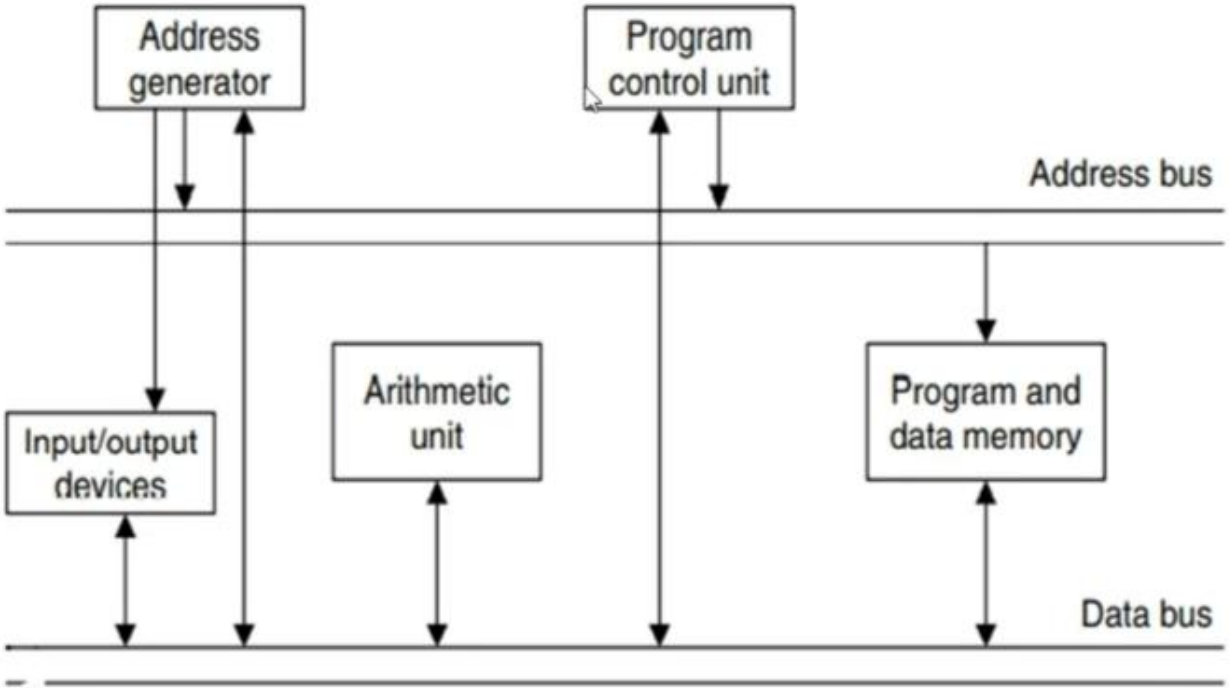
Introduction, Digital Signal Processor Architecture- Von Neumann architecture and Harvard architecture, Fixed- and Floating-Point Format for DS processors.

Text Book

1.Proakis&Manolakis, "Digital signal processing – Principles Algorithms & Applications", *Pearson education*, 4th Edition, New Delhi, 2007.

2. Li Tan, "Digital Signal Processing", Academic Press, *Elsevier*, 2007.

Von Neumann Architecture



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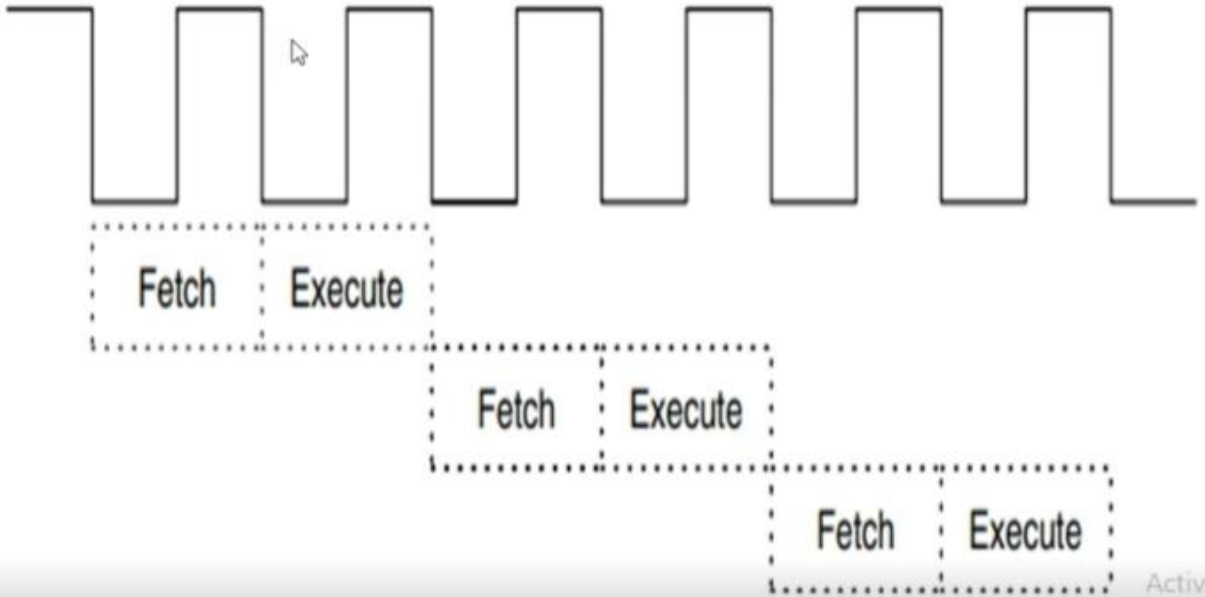
Execution cycle based on the Von Neumann Architecture



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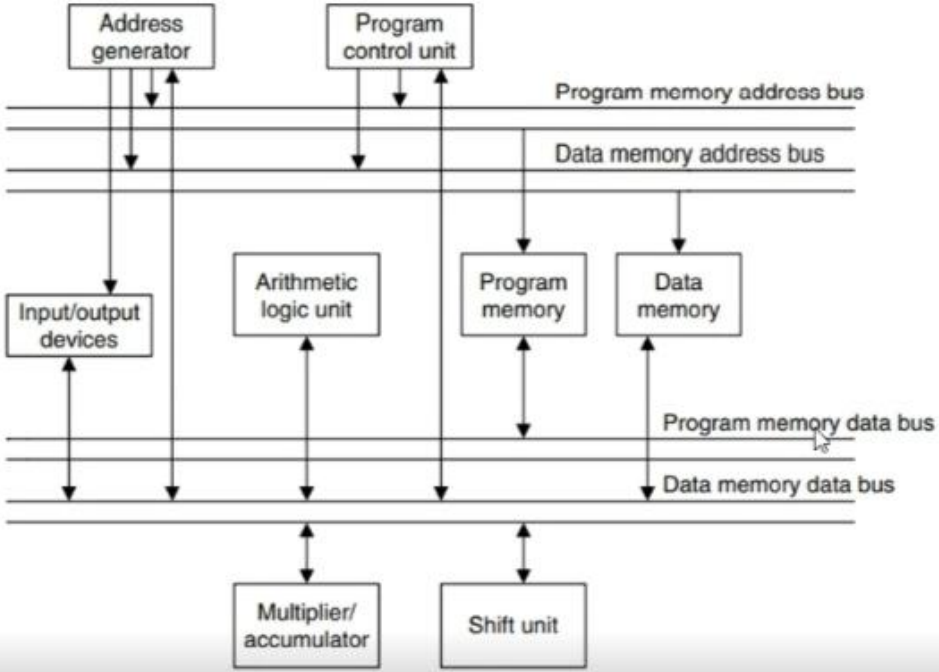
Harvard Architecture



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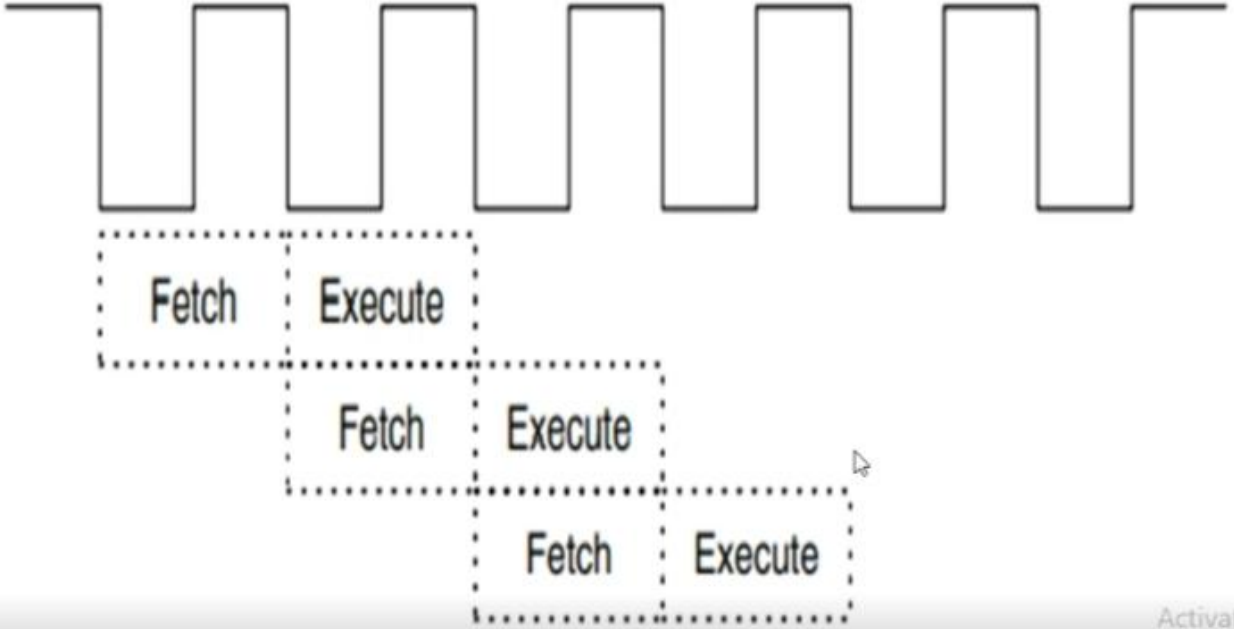
Execution cycle based on the Harvard Architecture



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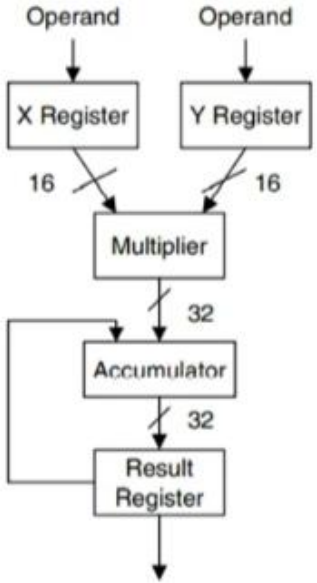
Multiplier and Accumalator Unit(MAC) Dedicated to DSP



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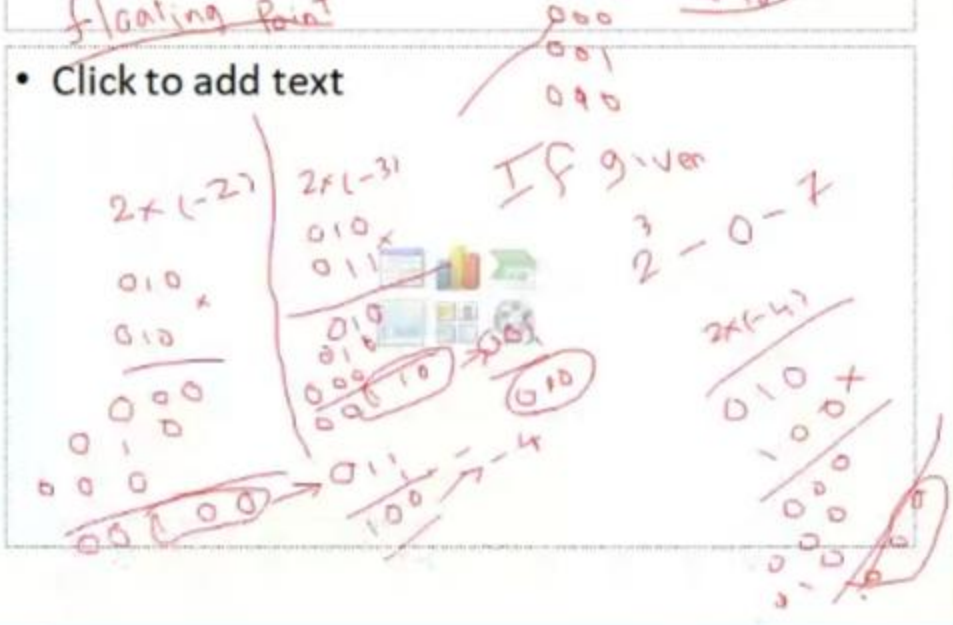
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