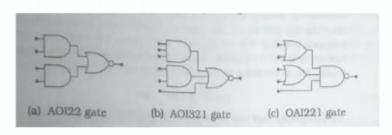
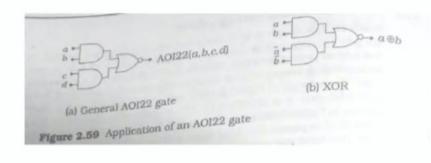


FIGURE 3.2 Stick diagrams and corresponding mask layout examples.

Generalized AOI and OAI Logic Gates





Transmissio Gate Circuits

