

Variant: [No Variations]

29-08-2018
V1I1

RELEASED 15-SEP-2018

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DESIGN CONSIDERATIONS

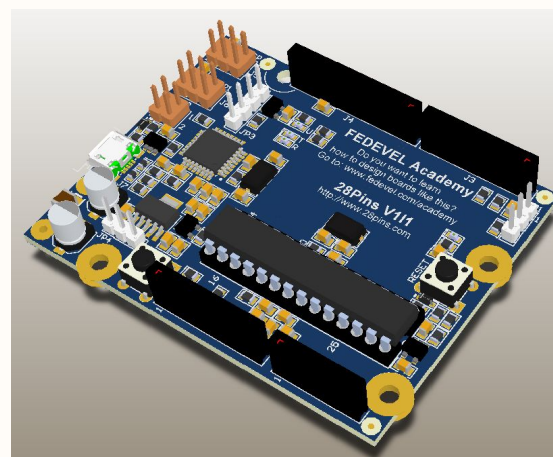
DESIGN NOTE:
Example text for informational
design notes.

DESIGN NOTE:
Example text for cautionary
design notes.

DESIGN NOTE:
Example text for debug notes.

DESIGN NOTE:
Example text for critical
design notes.

LAYOUT NOTE:
Example text for critical
layout guidelines.

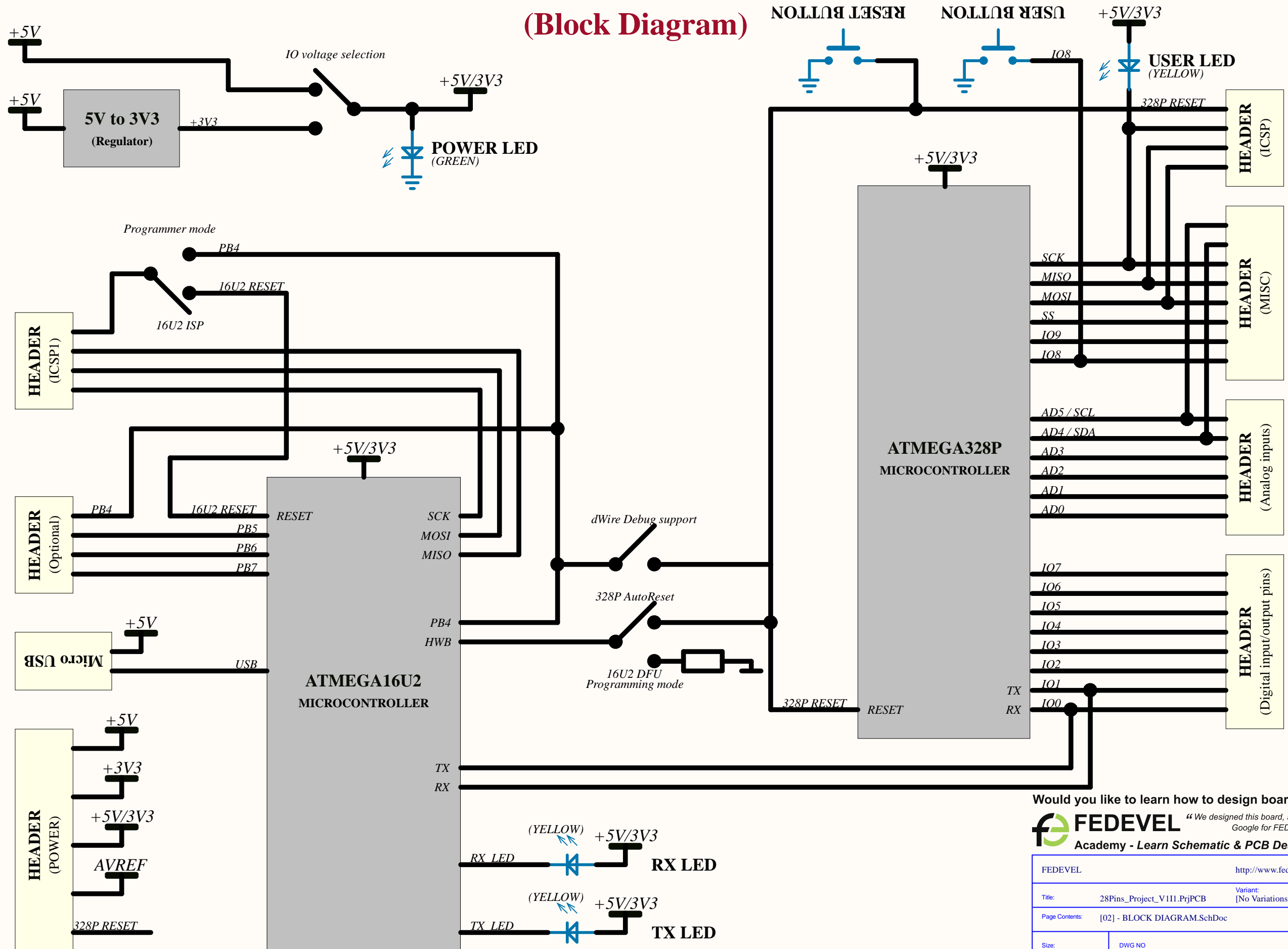


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Page Contents:	[01] - COVER PAGE.SchDoc		Checked by: Revision:
Size:	DWG NO		V1I1
Date:	29-08-2018 Designed by www.fedevel.com		Sheet 1 of 5

(Block Diagram)



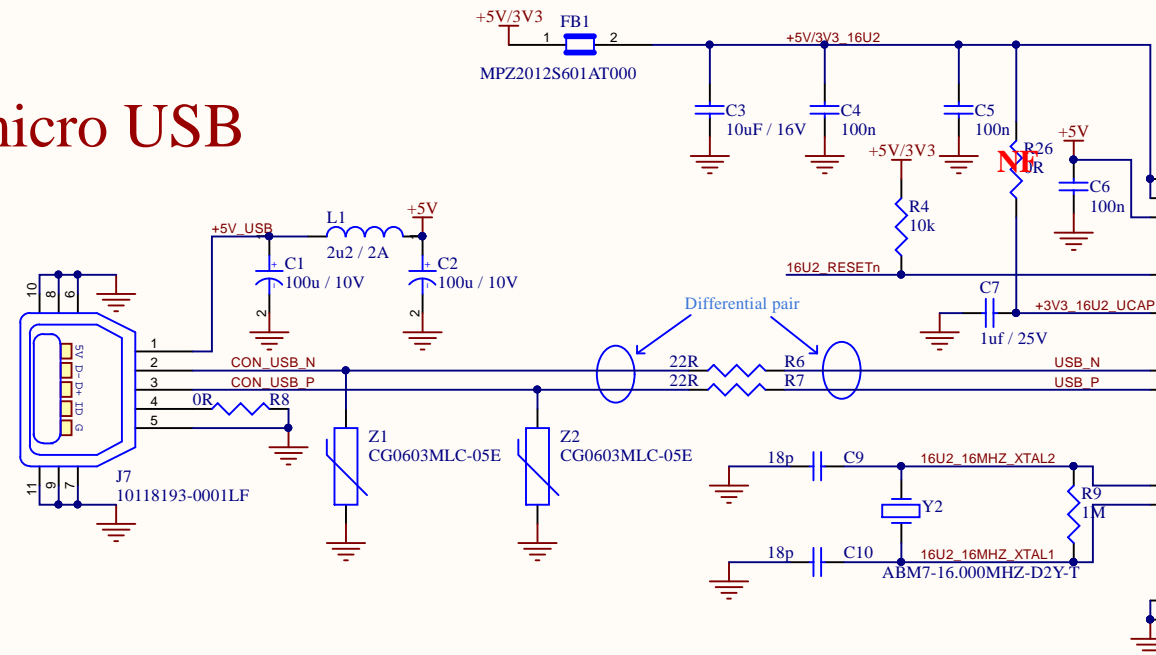
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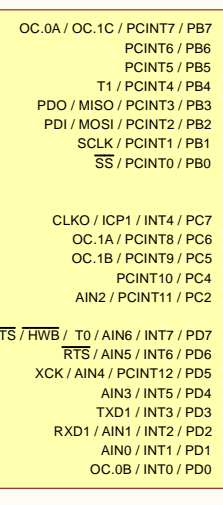
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Page Contents:	[02] - BLOCK DIAGRAM.SchDoc		
Size:	DWG NO	Revision:	V111
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28PINS - SCHEMATIC

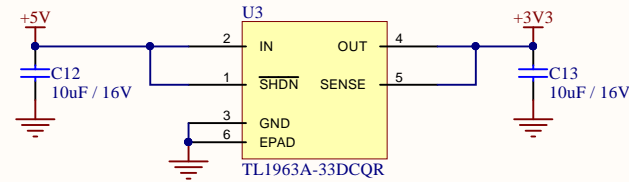
micro USB



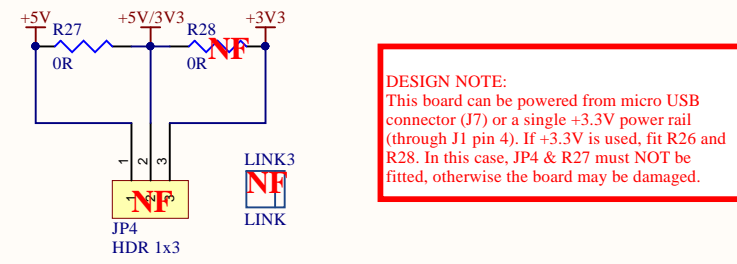
16U2



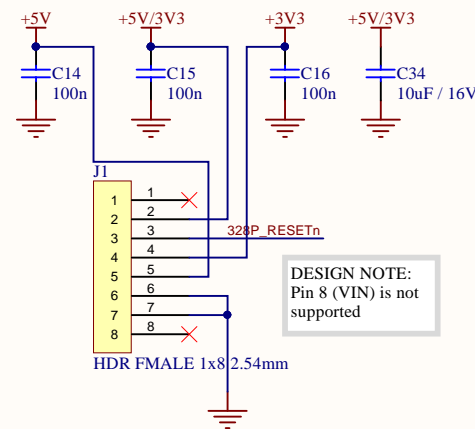
3V3 LDO



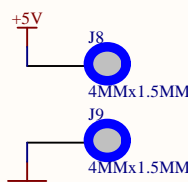
Power Selection



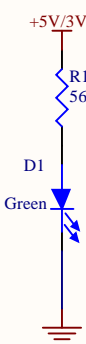
POWER



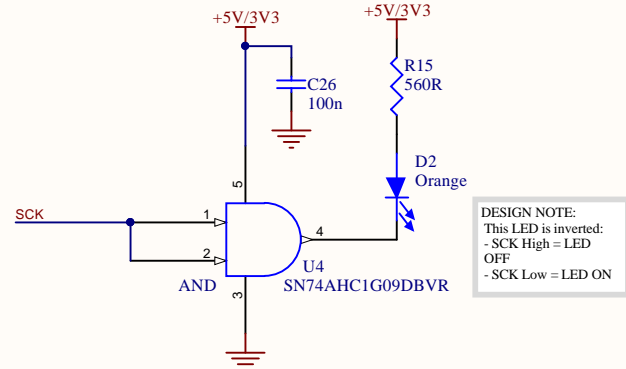
POWER PADS



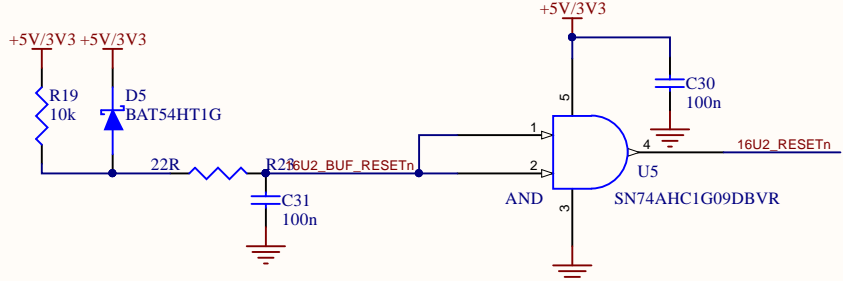
POWER LED



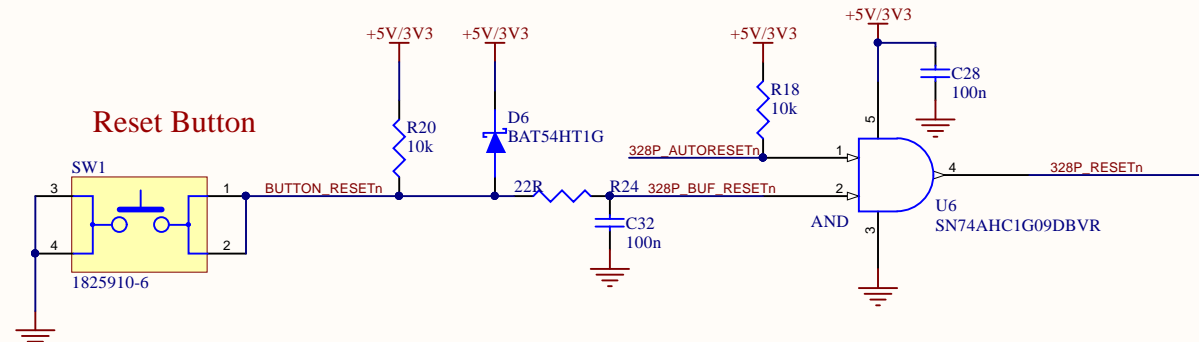
USER LED



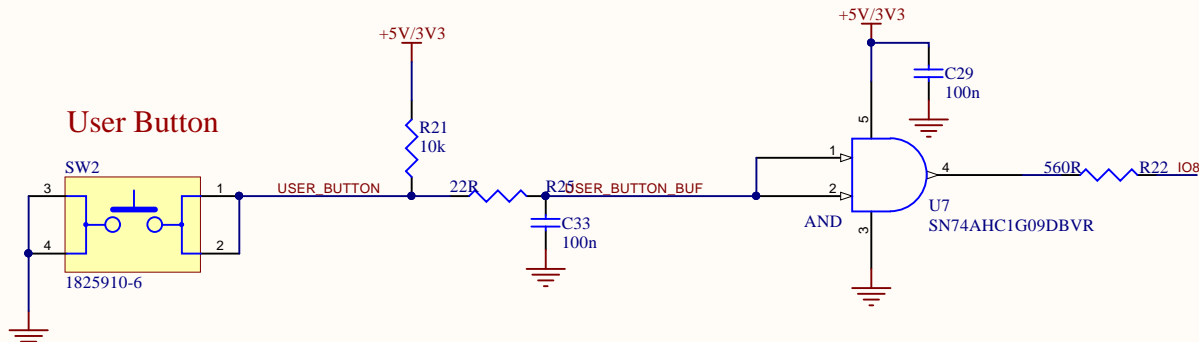
RESET (16U2)



RESET (328P)



USER BUTTON



DESIGN NOTE:
This board supports 5V or 3V3 voltage level on the IO pins:
1) 5V IO - Fit everything as defined in this schematic. NF means, do not fit this component.
2) 3.3V IO - Remove R27, Fit R28, *Replace Y1 (change from 16MHz to 10MHz), *Replace Y2 (change from 16MHz to 8MHz).
3) Both 5V and 3V3, selected through JP4 - Remove R27, Remove R28, Fit JP4, *Replace Y1 (change from 16MHz to 10MHz), *Replace Y2 (change from 16MHz to 8MHz).
*Note: The 16MHz crystals are not recommended for 3.3V operation. We need to adjust their values, that's why the change.
IMPORTANT: Once you change the crystal value, you may need to re-compile your source code.

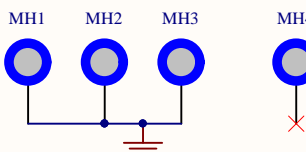
DESIGN NOTE:
About JP3:
1) DebugWire support - Short 1&2. This was added to support possible debugWire debugging (programming) of 328P through 16U2. In this case, the 16U2 needs to have a correct firmware and has to behave as a debugWire tool.
2) ISP programmer mode - Short 2&3. In this case, take a cable and connect J5 & J6 together. Upload AVRISP MKII firmware into 16U2 and you can program 328P. Example of AVRISP MKII firmware can be found at LUFA projects: <http://www.fourwalledcubicle.com/LUFA.php> (Tip: remap LEDs of the default AVRISP MKII LUFA project to the RX and TX LEDs on the 28Pin board)
3) ISP header - Short 3 & 4. In this mode, the ICSP1 header is used as a standard ISP header

DESIGN NOTE:
About JP1:
1) Autoreset Enabled - Short 1&2. In this case, 16U2 is used to reset 328P when firmware inside 328P is updated from Arduino IDE.
2) 16U2 DFU mode Enabled - Short 2&3. 16U2 HWB pin is sampled by 16U2 during RESET. If pulled low, then after Reset the 16U2 will go into DFU mode (it's the mode when you can flash 16U2 firmware through USB and Atmel Flip software: <http://www.atmel.com/tools/flip.aspx>).

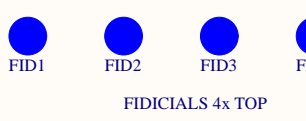
DIP SOCKET



MOUNTING HOLES



FIDUCIALS



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Page Contents:	[03] - 28PINS SCHEMATIC.SchDoc		Checked by
Size:	DWG NO	Revision: VIII	
Date:	29-08-2018	Designed by	Sudhanshu
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REVISION HISTORY

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Size:	DWG NO		Revision: V1111
Date:	29-08-2018	Sheet	4 of 5

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A	<div>Designator [01] - COVER PAGE.SchDoc</div> <div></div>	<div>Designator [02] - BLOCK DIAGRAM.SchDoc</div> <div></div>	<div>Designator [03] - 28PINS SCHEMATIC.SchDoc</div> <div></div>	<div>Designator [04] - REVISION HISTORY.SchDoc</div> <div></div>				
B								
C								
D								

NOTES

Mark Not Fitted Components as
NF

DRAFT - Very early stage of schematic, ignore details.

PRELIMINARY - Close to final schematic.

CHECKED - There should not be any mistakes. Tell the engineer if you find one.

RELEASED - A board with this schematic has been sent to production.

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