

Variant: 5V/3V3

29-08-2018
V1I1

RELEASED 15-SEP-2018

Page	Index	Page	Index	Page	Index	Page	Index
1	COVER PAGE	11	21	31
2	BLOCK DIAGRAM	12	22	32
3	28PINS SCHEMATIC	13	23	33
4	REVISION HISTORY	14	24	34
5	15	25	35
6	16	26	36
7	17	27	37
8	18	28	38
9	19	29	39
10	20	30	40

DESIGN CONSIDERATIONS

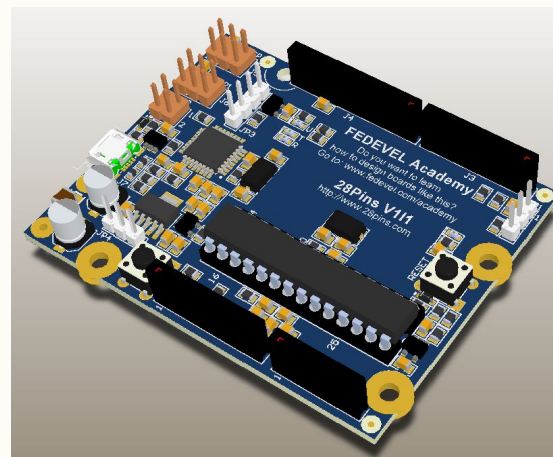
DESIGN NOTE:
Example text for informational
design notes.

DESIGN NOTE:
Example text for cautionary
design notes.

DESIGN NOTE:
Example text for debug notes.

DESIGN NOTE:
Example text for critical
design notes.

LAYOUT NOTE:
Example text for critical
layout guidelines.



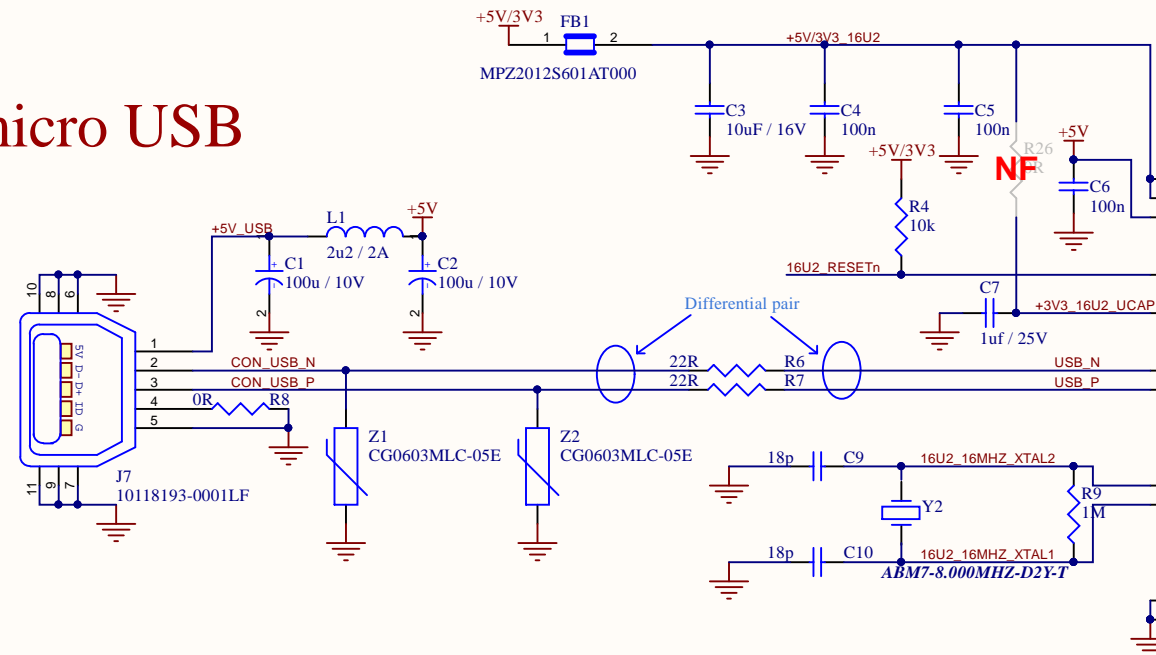
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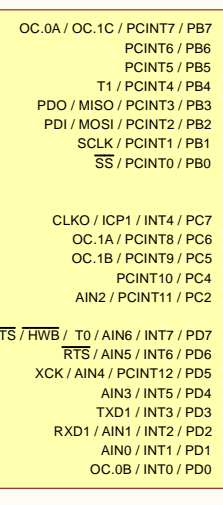
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Title:	28Pins_Project_V1I1.PrjPCB		Variant: 5V/3V3
Page Contents:	[01] - COVER PAGE.SchDoc		Checked by: Revision:
Size:	DWG NO		V1I1
Date:	29-08-2018 Designed by www.fedevel.com		Sheet 1 of 5

28PINS - SCHEMATIC

micro USB

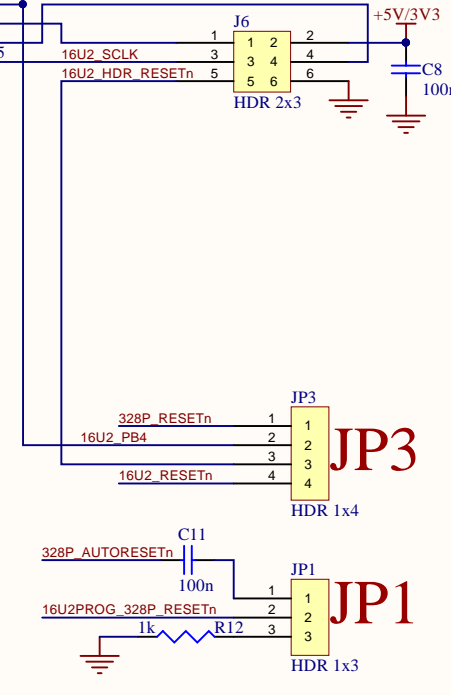


16U2



JP2

ICSP1

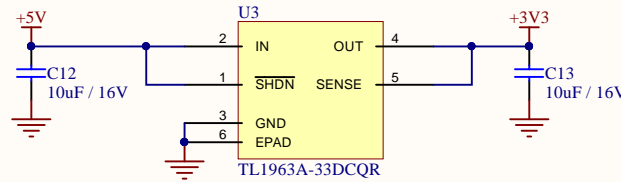


DESIGN NOTE:
This board supports 5V or 3V3 voltage level on the IO pins:
1) 5V IO - Fit everything as defined in this schematic. NF means, do not fit this component.
2) 3.3V IO - Remove R27, Fit R28, *Replace Y1 (change from 16MHz to 10MHz), *Replace Y2 (change from 16MHz to 8MHz).
3) Both 5V and 3V3, selected through JP4 - Remove R27, Remove R28, Fit JP4, *Replace Y1 (change from 16MHz to 10MHz), *Replace Y2 (change from 16MHz to 8MHz).
*Note: The 16MHz crystals are not recommended for 3.3V operation. We need to adjust their values, that's why the change.
IMPORTANT: Once you change the crystal value, you may need to re-compile your source code.

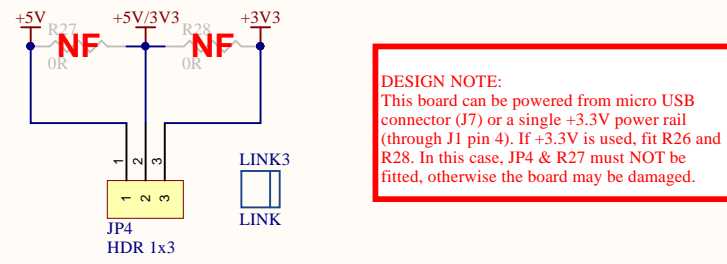
DESIGN NOTE:
About JP3:
1) DebugWire support - Short 1&2. This was added to support possible debugWire debugging (programming?) of 328P through 16U2. In this case, the 16U2 needs to have a correct firmware and has to behave as a debugWire tool.
2) ISP programmer mode - Short 2&3. In this case, take a cable and connect J5 & J6 together. Upload AVRISP MKII firmware into 16U2 and you can program 328P. Example of AVRISP MKII firmware can be found at LUFA projects: <http://www.fourwalledcubicle.com/LUFA.php> (Tip: remap LEDs of the default AVRISP MKII LUFA project to the RX and TX LEDs on the 28Pin board)
3) ISP header - Short 3 & 4. In this mode, the ICSP1 header is used as a standard ISP header

DESIGN NOTE:
About JP1:
1) Autoreset Enabled - Short 1&2. In this case, 16U2 is used to reset 328P when firmware inside 328P is updated from Arduino IDE.
2) 16U2 DFU mode Enabled - Short 2&3. 16U2 HWB pin is sampled by 16U2 during RESET. If pulled low, then after Reset the 16U2 will go into DFU mode (it's the mode when you can flash 16U2 firmware through USB and Atmel Flip software: <http://www.atmel.com/tools/flip.aspx>).

3V3 LDO



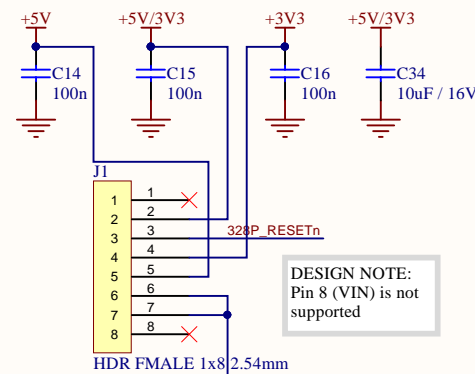
Power Selection



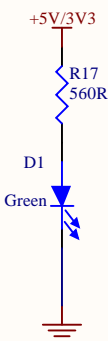
DESIGN NOTE:
This board can be powered from micro USB connector (J7) or a single +3.3V power rail (through J1 pin 4). If +3.3V is used, fit R26 and R28. In this case, JP4 & R27 must NOT be fitted, otherwise the board may be damaged.

POWER

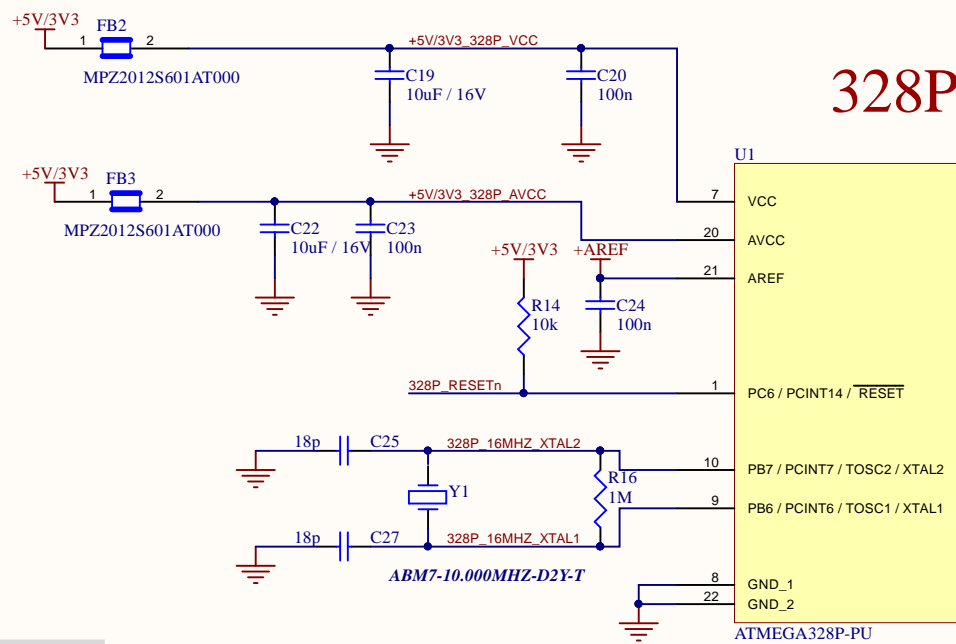
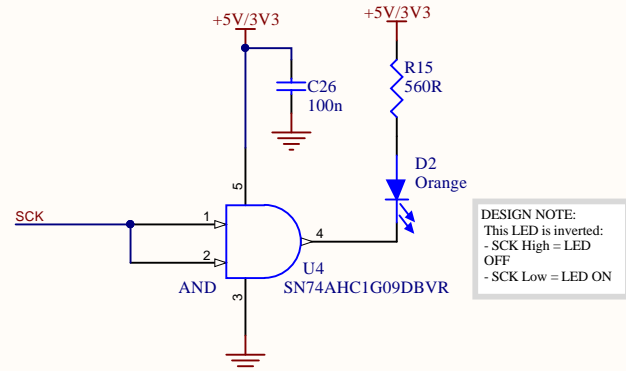
POWER PADS



POWER LED

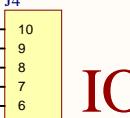


USER LED



328P

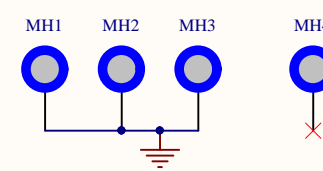
IOH



DIP SOCKET

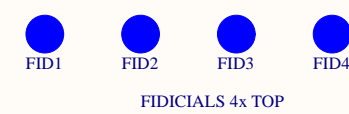


MOUTING HOLES

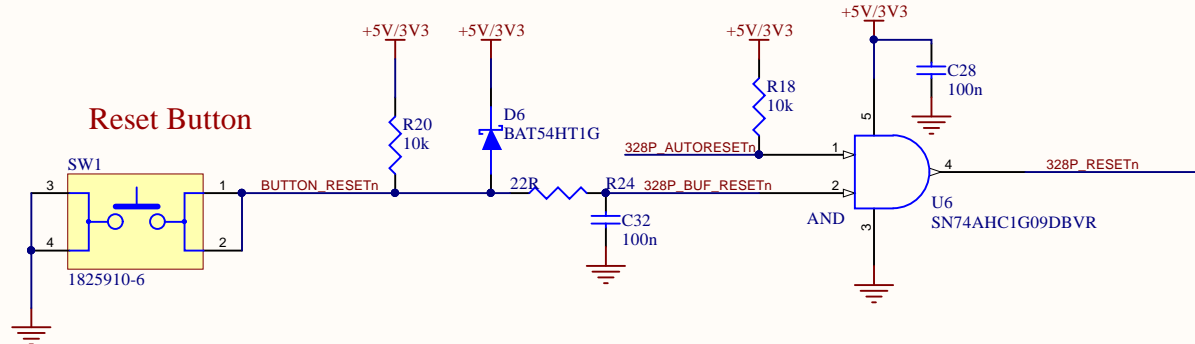


Mounting holes 7.4mm pad 3.2mm drill
BOARD MOUNTING HOLES
ONE IN EACH CORNER

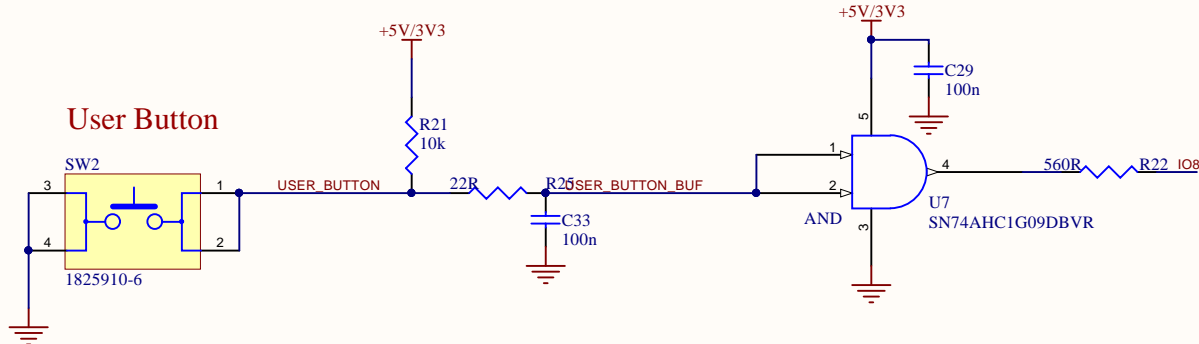
FIDUCIALS



RESET (328P)



USER BUTTON



LAYOUT NOTE:
1) Route all the POWER tracks with minimum track width 0.4mm.
2) Route all the other tracks by 0.4mm and change them by the end of the design to 0.2mm. To change all of them at once, use this filter "(not InNet(+*))" and not InNet("GND")) and IsTrack and (OnLayer("L1") or OnLayer("L2"))" and then set 0.2mm width in PCB Inspector panel.

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Title:	28Pins_Project_VIII.PjPCB	Variant:	5V/3V3
Page Contents:	[03] - 28PINS SCHEMATIC.SchDoc		Checked by
Size:	DWG NO	Revision: VIII	
Date:	29-08-2018	Designed by	Sudhanshu
Sheet	3	of	5

REVISION HISTORY

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Page Contents:	[04] - REVISION HISTORY.SchDoc		Checked by
Size:	DWG NO		Revision: V1111
Date:	29-08-2018	Sheet	4 of 5

	1	2	3	4	5	6	7	8																								
A	<div>Designator [01] - COVER PAGE.SchDoc</div> <div></div>	<div>Designator [02] - BLOCK DIAGRAM.SchDoc</div> <div></div>	<div>Designator [03] - 28PINS SCHEMATIC.SchDoc</div> <div></div>	<div>Designator [04] - REVISION HISTORY.SchDoc</div> <div></div>																												
B																																
C																																
D	<div><div>NOTES</div><div><div>Mark Not Fitted Components as NF</div><div>DRAFT - Very early stage of schematic, ignore details.</div><div>PRELIMINARY - Close to final schematic.</div><div>CHECKED - There should not be any mistakes. Tell the engineer if you find one.</div><div>RELEASED - A board with this schematic has been sent to production.</div></div></div> <div><div><div>Would you like to learn how to design boards like this?</div><div><div><div><div>f</div><div>FEDEVEL</div></div><div><div>Academy - Learn Schematic & PCB Design ONLINE</div></div></div><div><div>“ We designed this board, so you can learn, Google for FEDEVEL Academy “</div></div></div><table><tr><td colspan="4">http://www.fedevel.com/academy</td></tr><tr><td colspan="2">Title:</td><td colspan="2">Variant: 5V/3V3</td></tr><tr><td colspan="2">Page Contents:</td><td colspan="2">28Pins_Project_V1111 Project.SchDoc</td></tr><tr><td colspan="2">Size:</td><td colspan="2">DWG NO</td></tr><tr><td colspan="2">Date: 29-08-2018</td><td colspan="2">Designed by www.fedevel.com</td></tr><tr><td colspan="2">Sheet 5 of 5</td><td colspan="2">Revision: V1111</td></tr></table></div></div>				http://www.fedevel.com/academy				Title:		Variant: 5V/3V3		Page Contents:		28Pins_Project_V1111 Project.SchDoc		Size:		DWG NO		Date: 29-08-2018		Designed by www.fedevel.com		Sheet 5 of 5		Revision: V1111					
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	1	2	3	4	5	6	7	8																								