

Variant: FIXED 5V

29-08-2018
V1I1

RELEASED 15-SEP-2018

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DESIGN CONSIDERATIONS

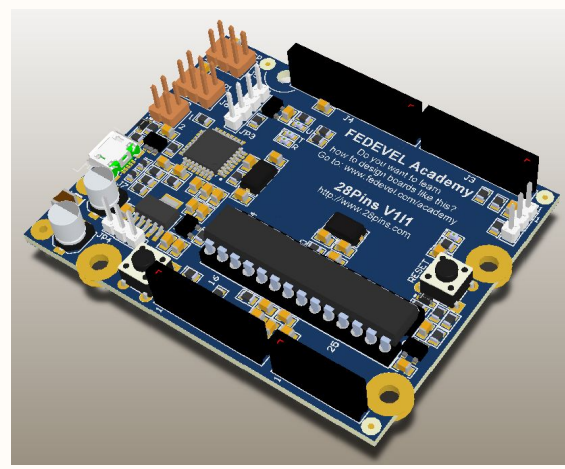
DESIGN NOTE:
Example text for informational
design notes.

DESIGN NOTE:
Example text for cautionary
design notes.


DESIGN NOTE:
Example text for debug notes.

DESIGN NOTE:
Example text for critical
design notes.

LAYOUT NOTE:
Example text for critical
layout guidelines.



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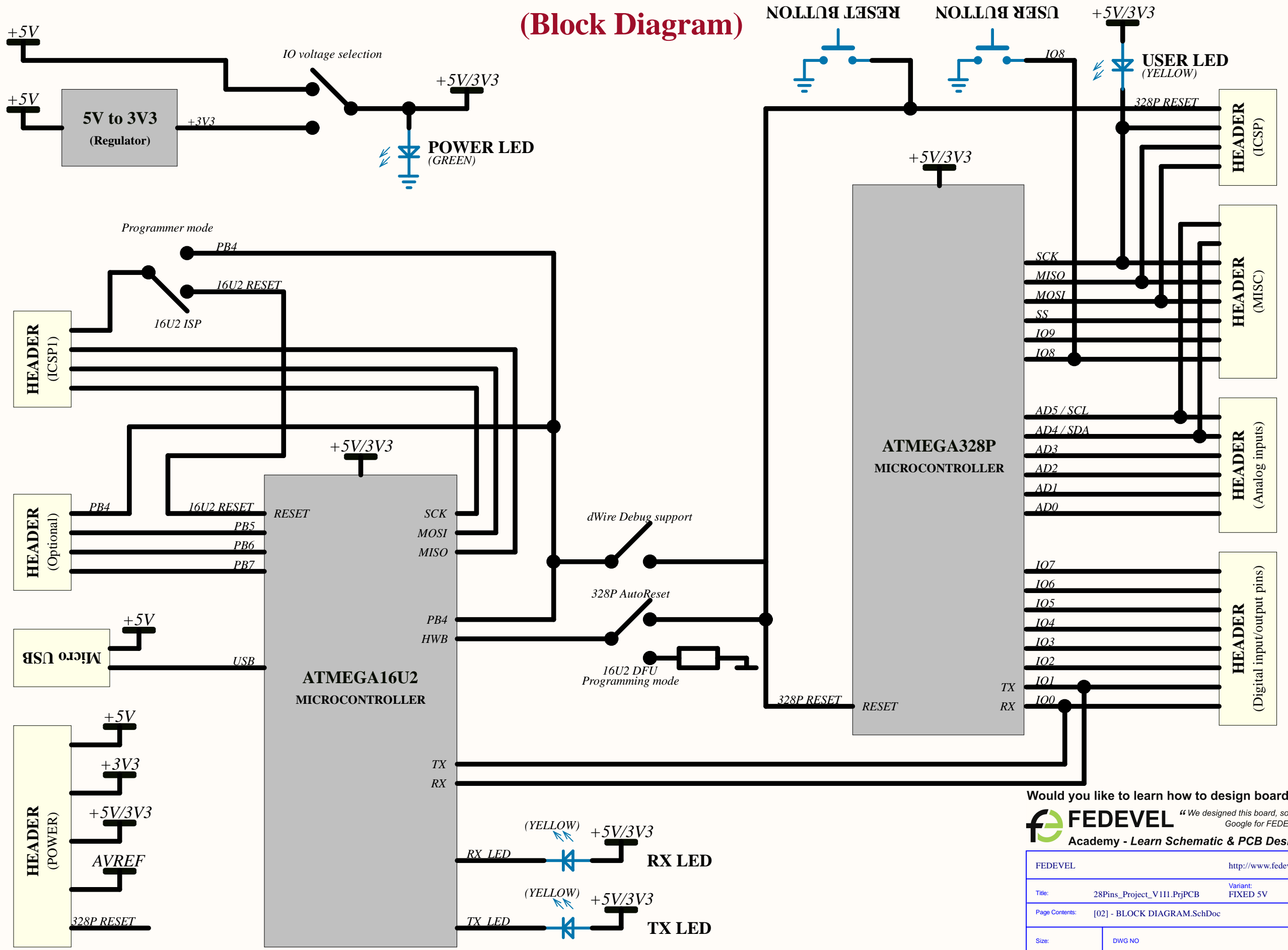


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Page Contents:	[01] - COVER PAGE.SchDoc		Checked by:
Size:	DWG NO		Revision: V1I1
Date:	29-08-2018 Designed by www.fedevel.com		Sheet 1 of 5

(Block Diagram)



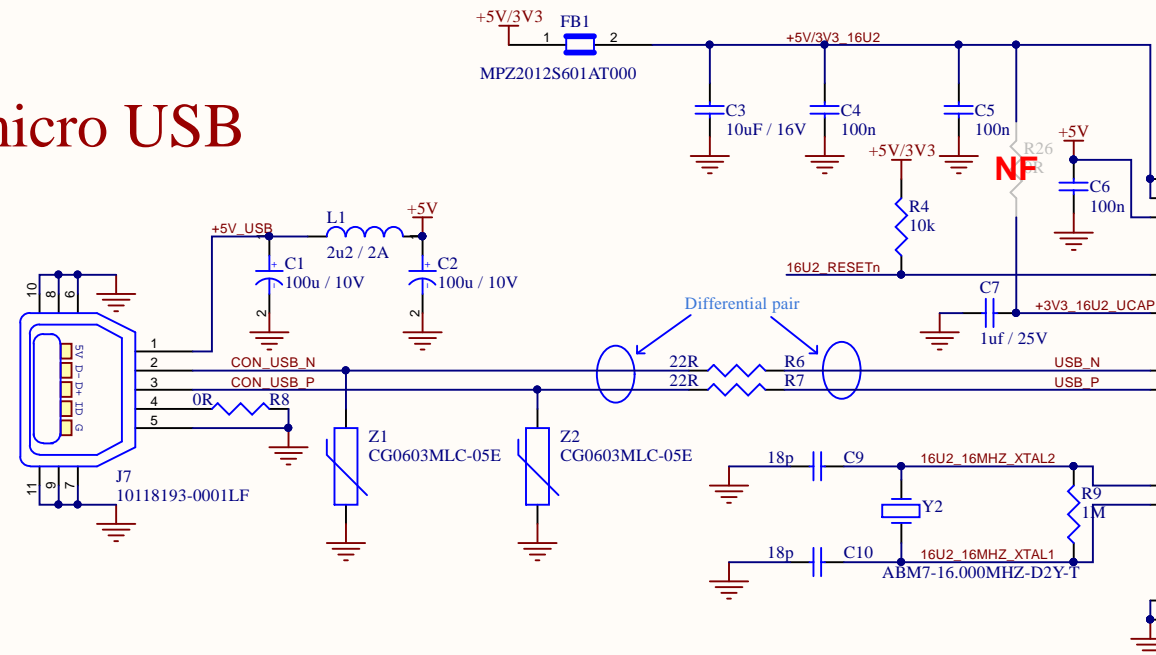
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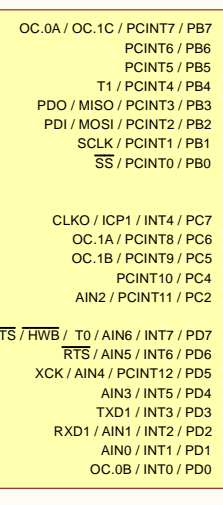
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28PINS - SCHEMATIC

micro USB

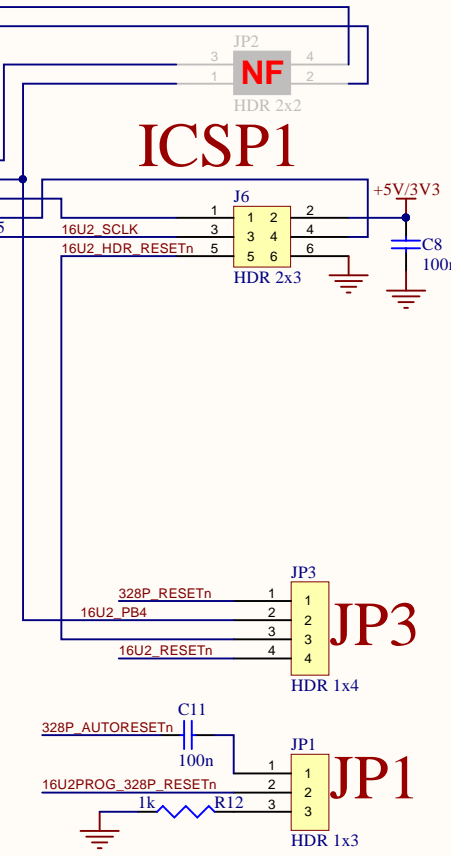


16U2



JP2

ICSP1



REVISION HISTORY

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A	<div>Designator [01] - COVER PAGE.SchDoc</div> <div></div>	<div>Designator [02] - BLOCK DIAGRAM.SchDoc</div> <div></div>	<div>Designator [03] - 28PINS SCHEMATIC.SchDoc</div> <div></div>	<div>Designator [04] - REVISION HISTORY.SchDoc</div> <div></div>				
B								
C								
D								

NOTES

Mark Not Fitted Components as
NF

DRAFT - Very early stage of schematic, ignore details.

PRELIMINARY - Close to final schematic.

CHECKED - There should not be any mistakes. Tell the engineer if you find one.

RELEASED - A board with this schematic has been sent to production.

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