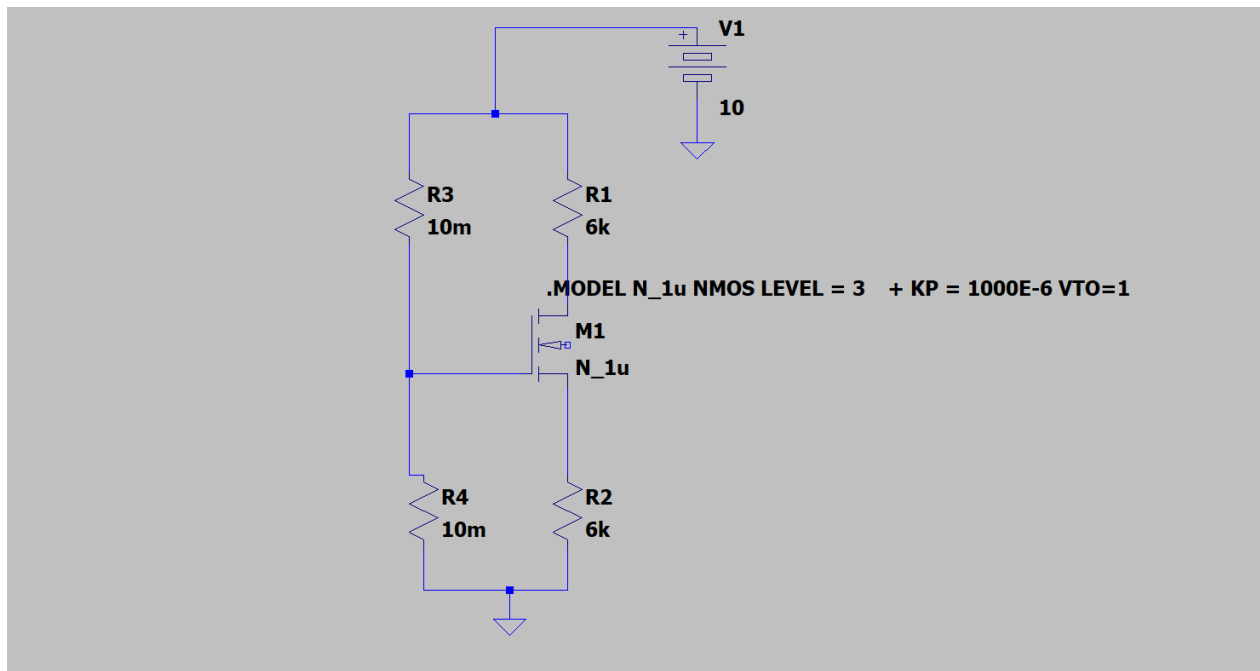


3.

b)



3. (d) Gate current is zero in MOS transistor always because gate terminal is isolated from substrate of MOS transistor by an dielectric medium SiO_2 hence no gate current flows.

(e) ~~It~~ It is a voltage divider bias circuit.

(f) The threshold voltage signifies that when $V_{GS} \geq V_T$ then the MOSFET is in saturation region and it acts as amplifier and at this voltage channel is made in E-MOSFET.